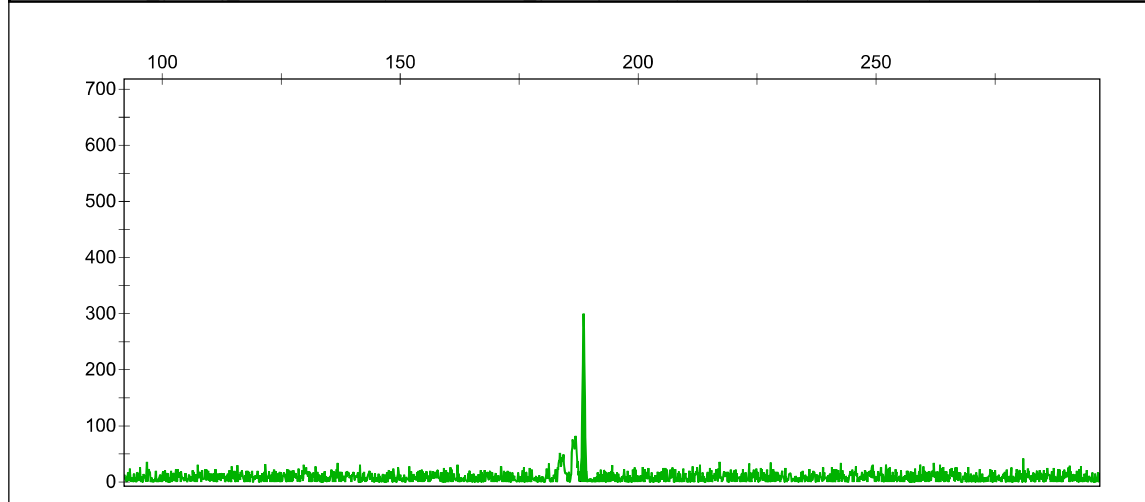
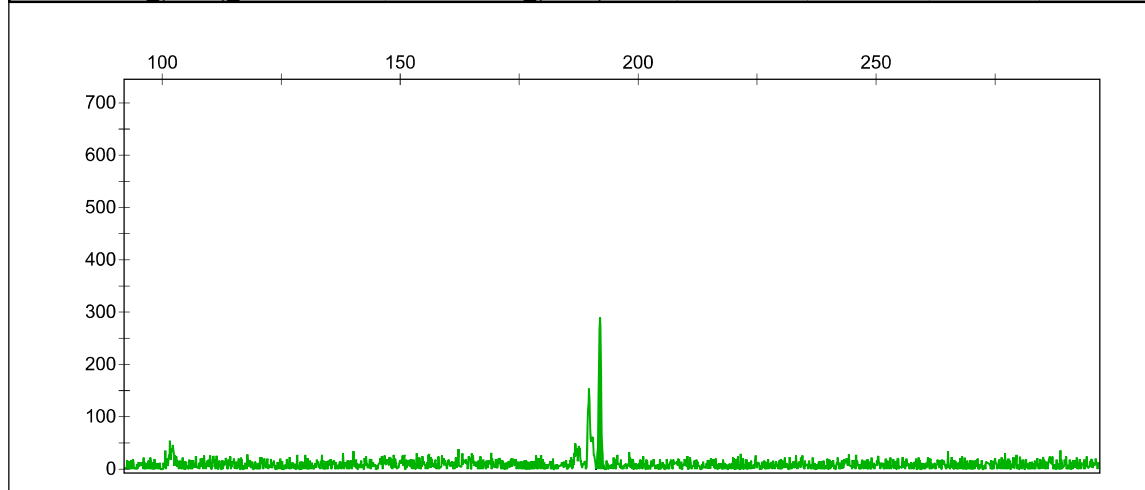


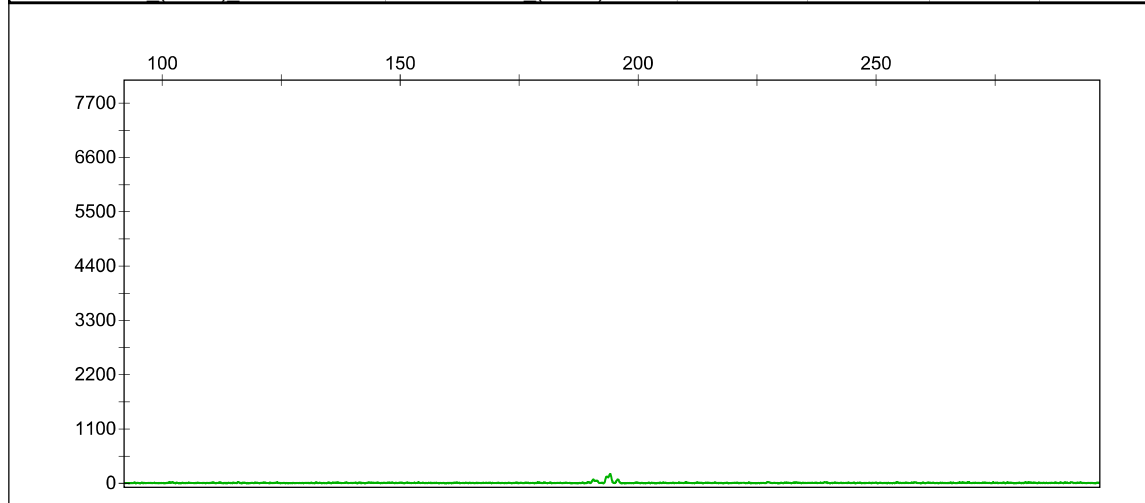
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731271_(100-1)_A08.fsa	71102731271_(100-1)	None			



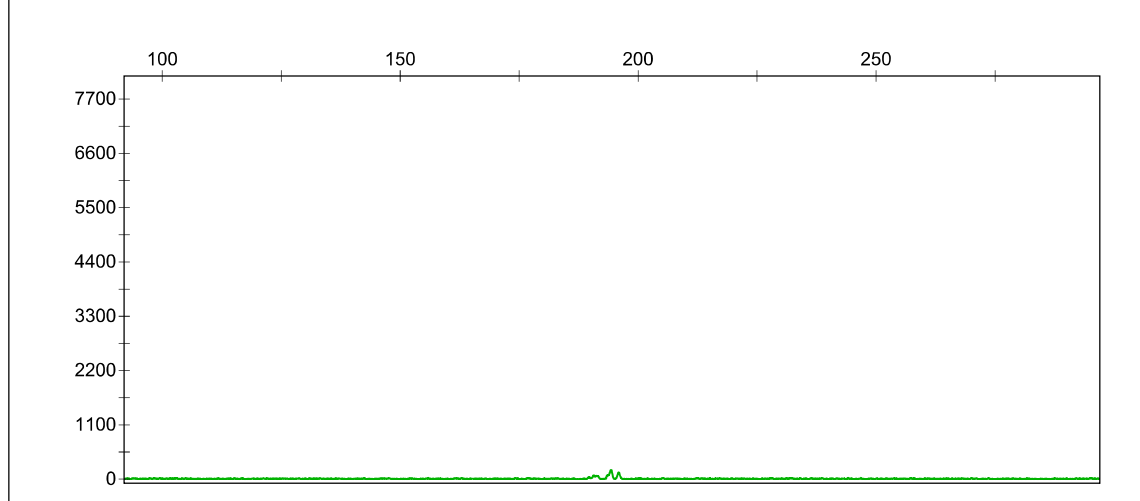
71102731272_(100-3)_B08.fsa	71102731272_(100-3)	None			
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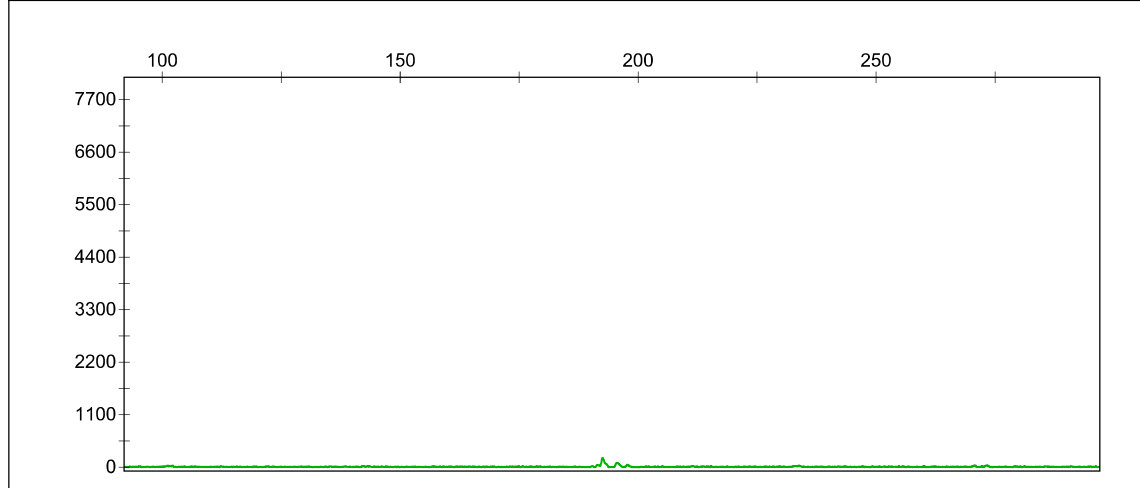
71102731273_(100-5)_C08.fsa	71102731273_(100-5)	None			
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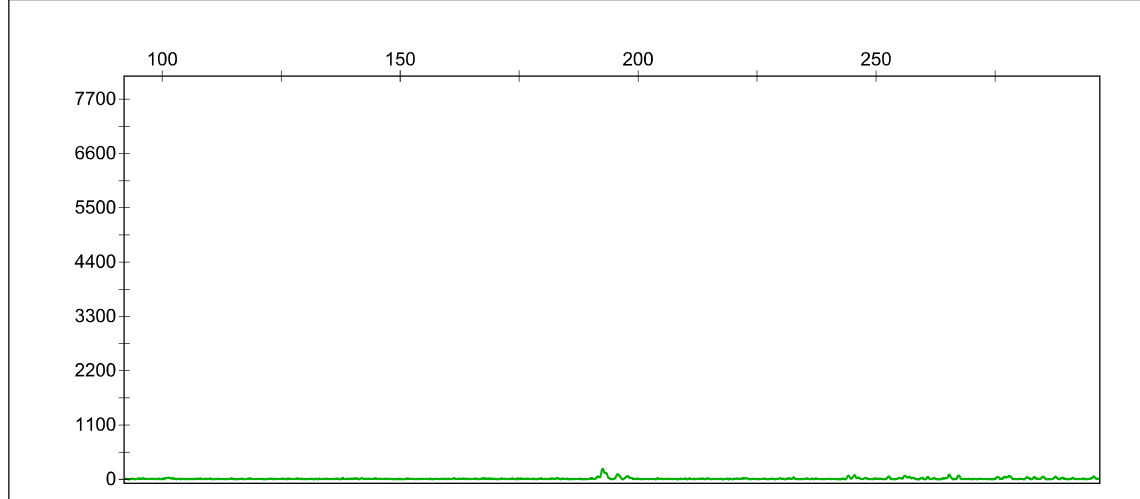
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731274_(100-6)_D08.fsa	71102731274_(100-6)	None			



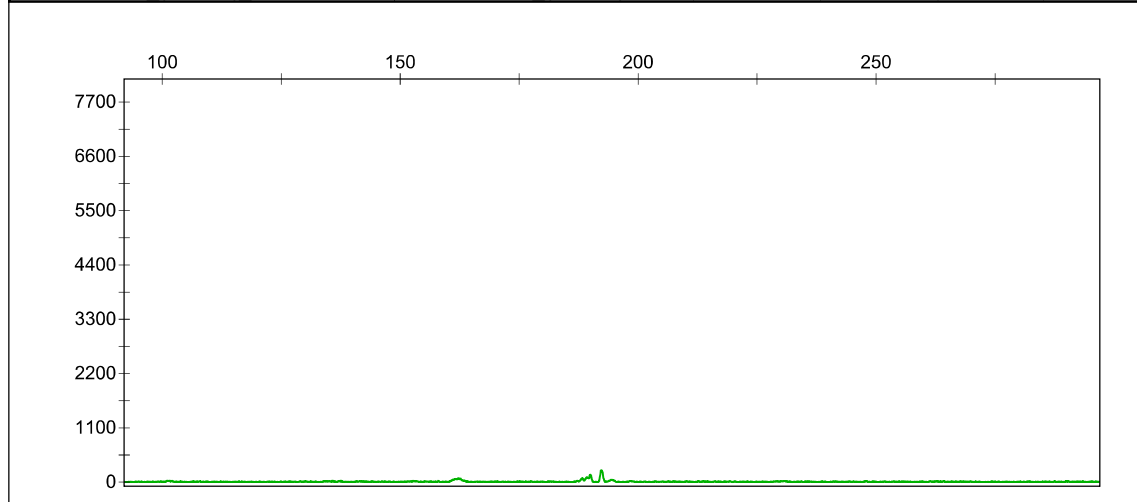
71102731275_(100-7)_E08.fsa	71102731275_(100-7)	None			
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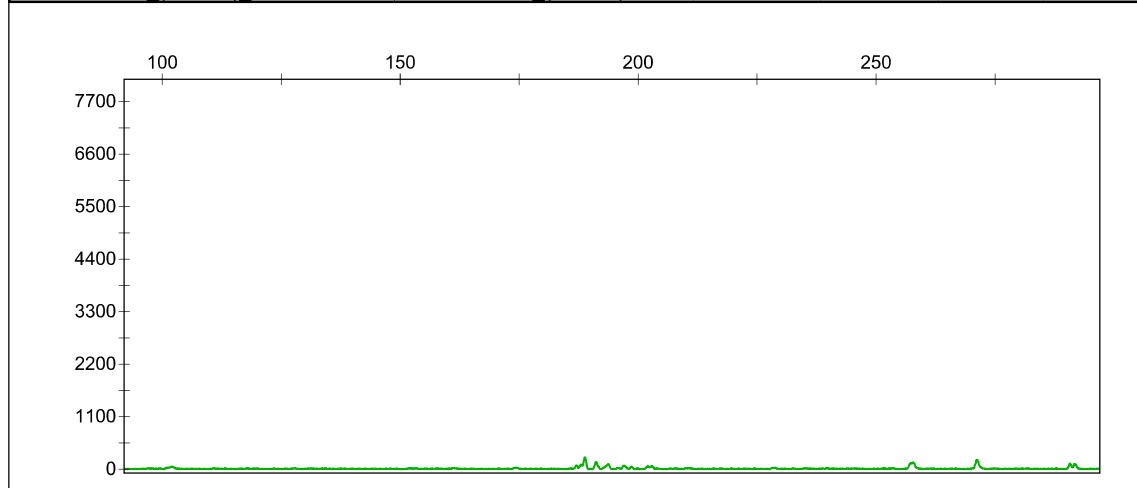
71102731276_(100-9)_F08.fsa	71102731276_(100-9)	None			
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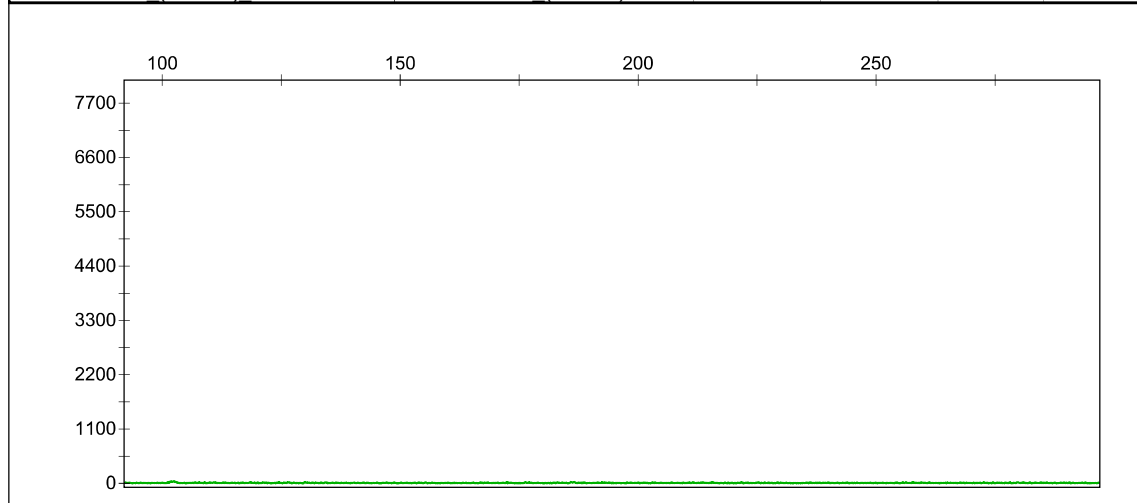
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731277_(100-10)_G08.fsa	71102731277_(100-10)	None		■	■



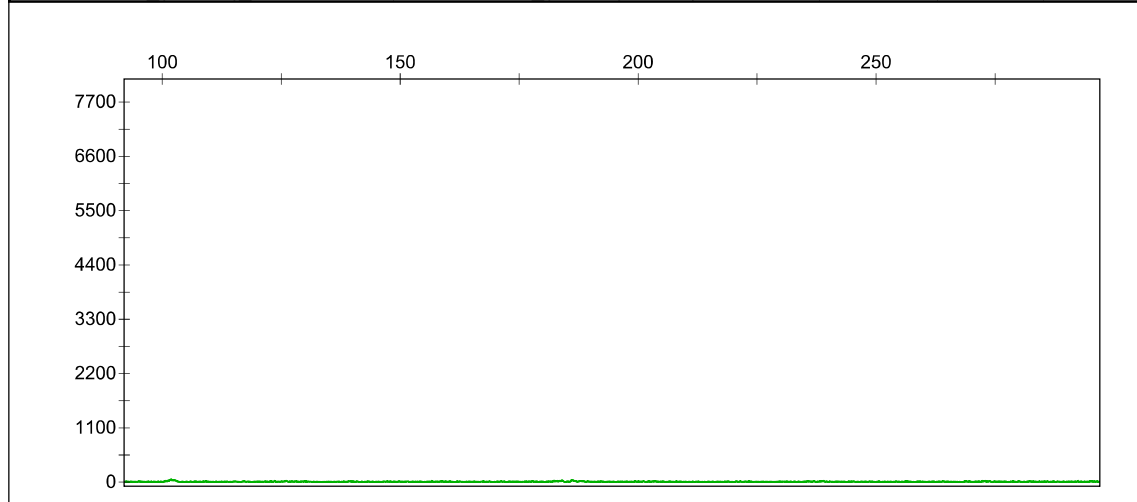
71102731278_(100-12)_H08.fsa	71102731278_(100-12)	None		■	■
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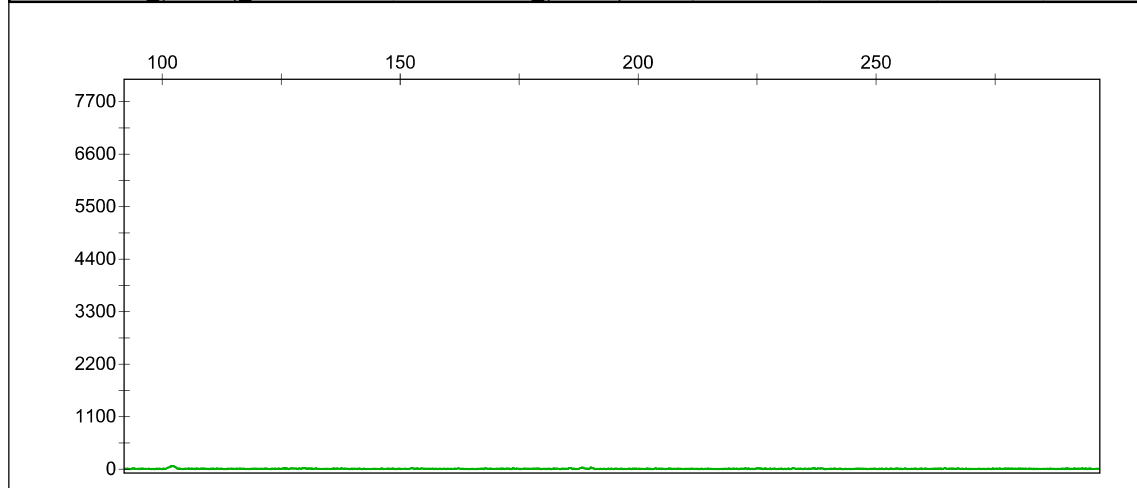
71102731279_(100-13)_A09.fsa	71102731279_(100-13)	None		■	■
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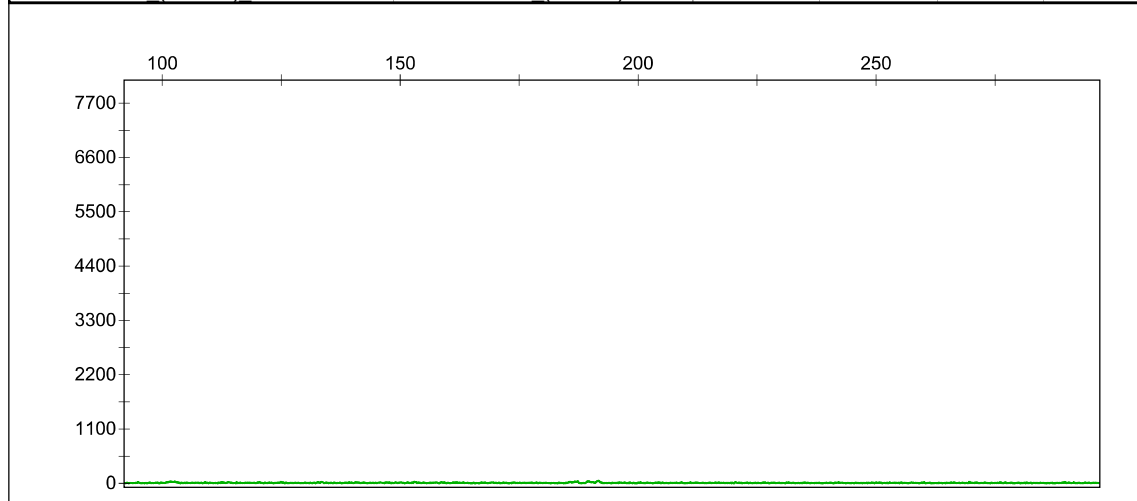
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731280_ (100-14)_ B09.fsa	71102731280_ (100-14)	None		■	■



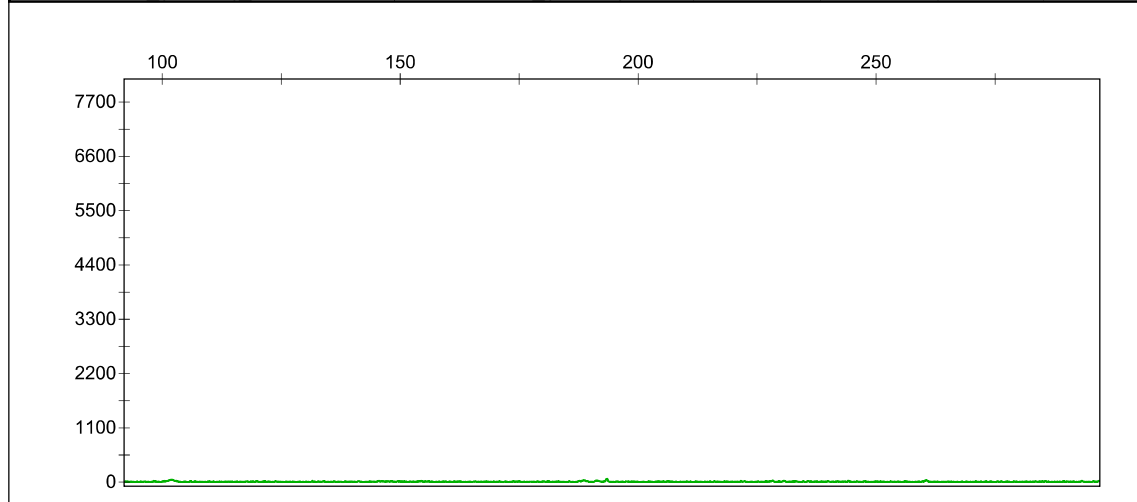
71102731281_ (100-15)_ C09.fsa	71102731281_ (100-15)	None		■	■
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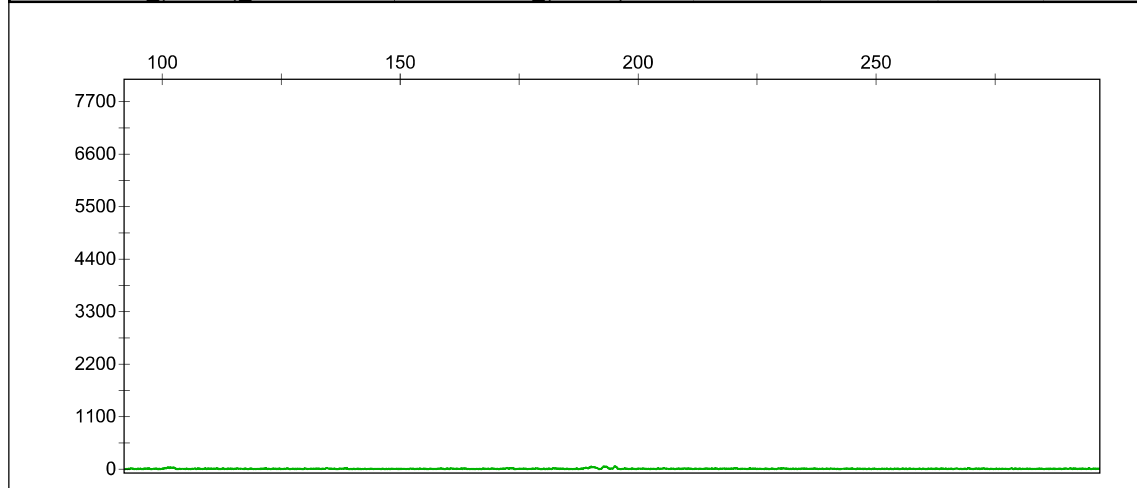
71102731282_ (100-16)_ D09.fsa	71102731282_ (100-16)	None		■	■
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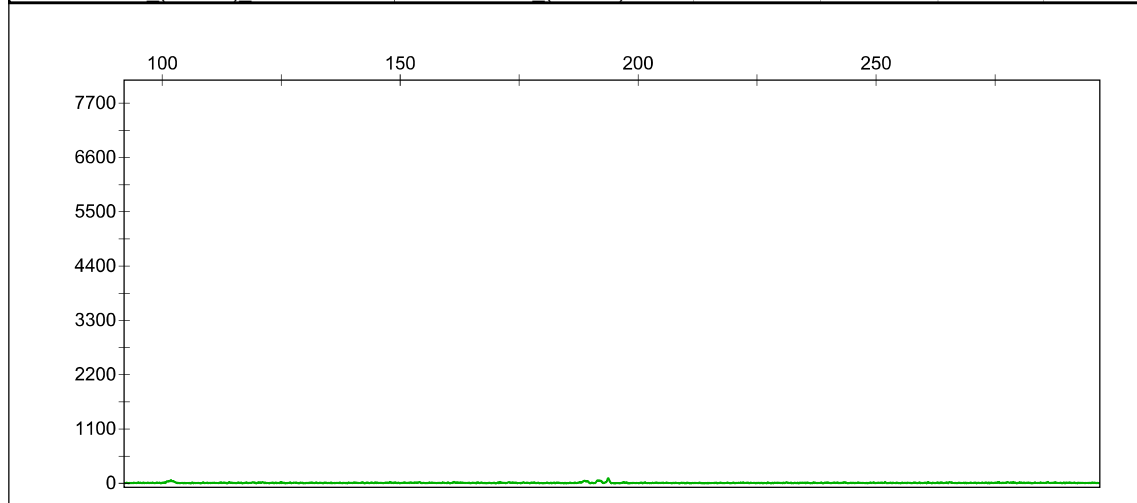
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731283_(100-17)_E09.fsa	71102731283_(100-17)	None		■	■



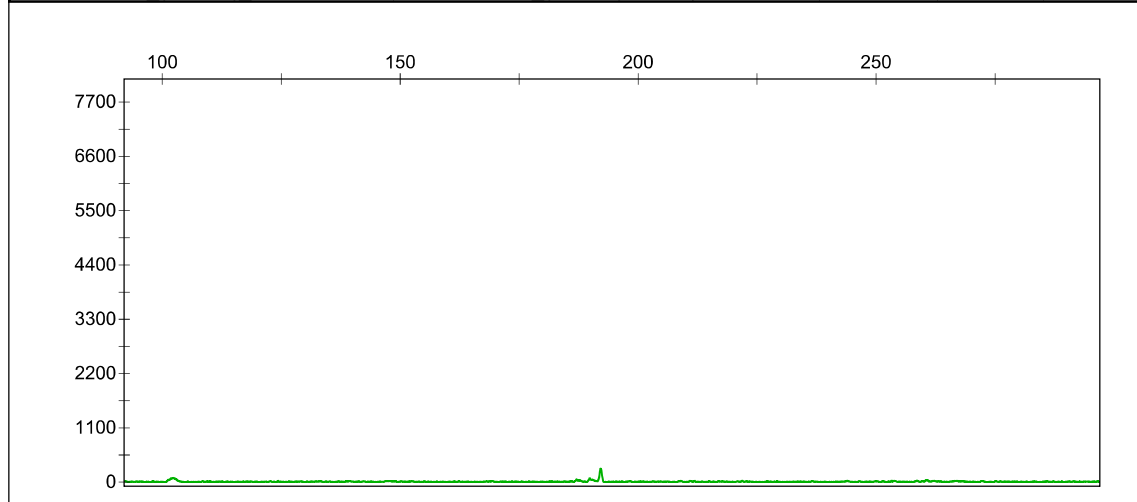
71102731284_(100-18)_F09.fsa	71102731284_(100-18)	None		■	■
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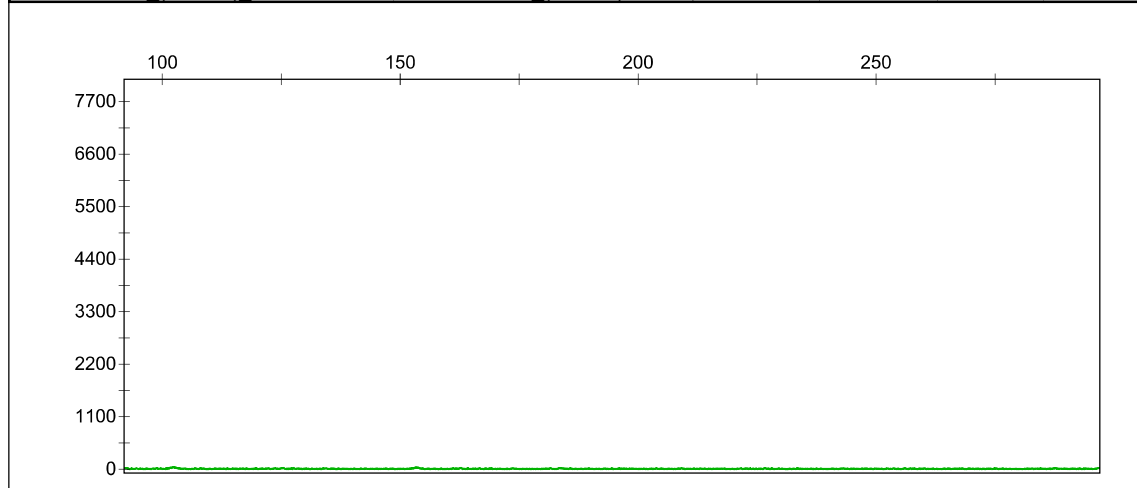
71102731285_(100-19)_G09.fsa	71102731285_(100-19)	None		■	■
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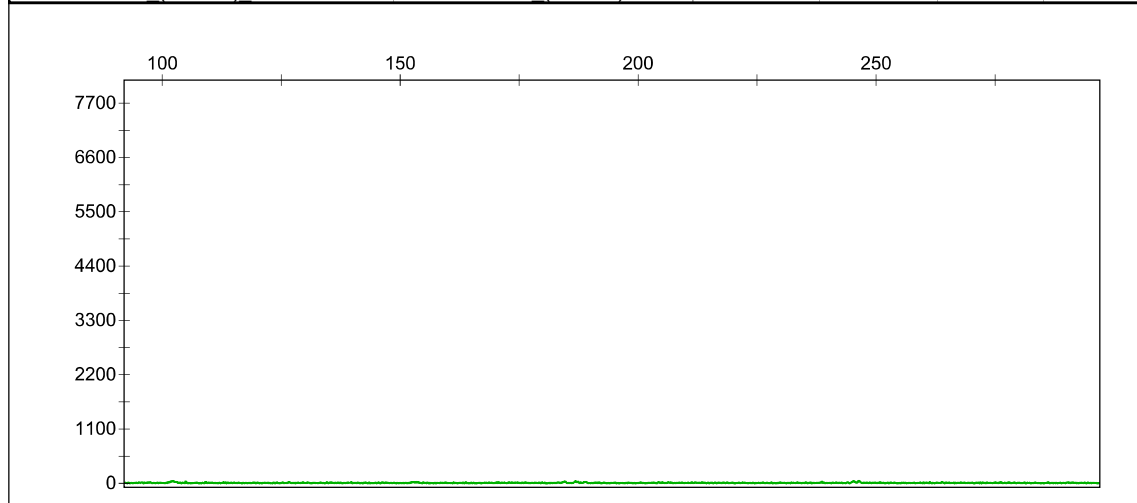
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731286_ (100-20)_ H09.fsa	71102731286_ (100-20)	None		■	■



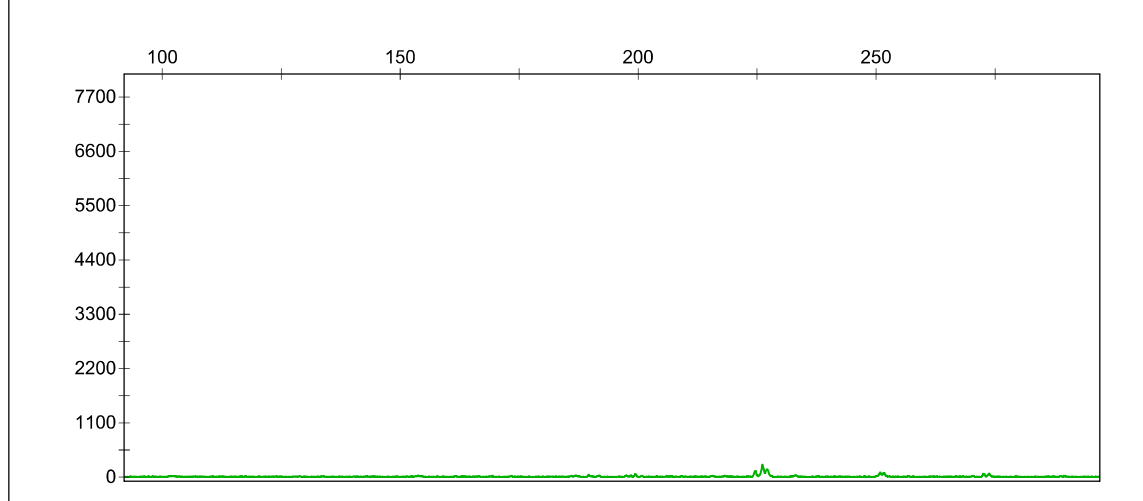
71102731287_ (100-21)_ A10.fsa	71102731287_ (100-21)	None		■	■
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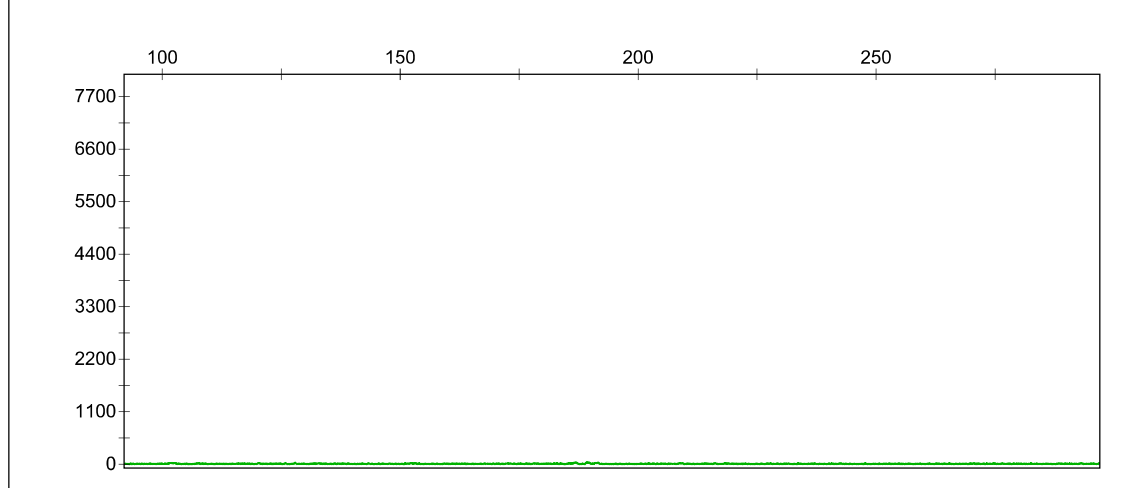
71102731288_ (100-22)_ B10.fsa	71102731288_ (100-22)	None		■	■
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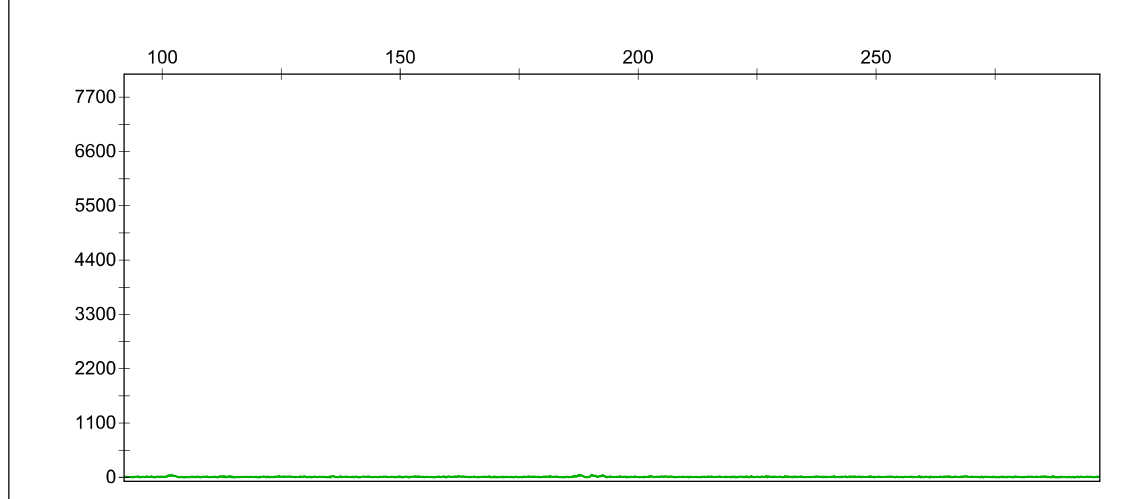
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731289_ (100-23)_ C10.fsa	71102731289_ (100-23)	None		■	■



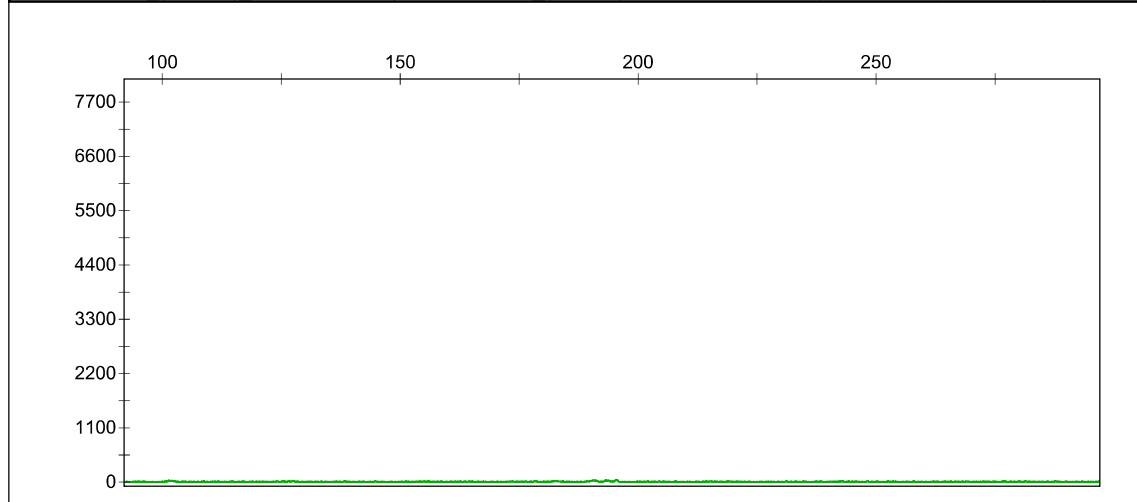
71102731290_ (100-24)_ D10.fsa	71102731290_ (100-24)	None		■	■
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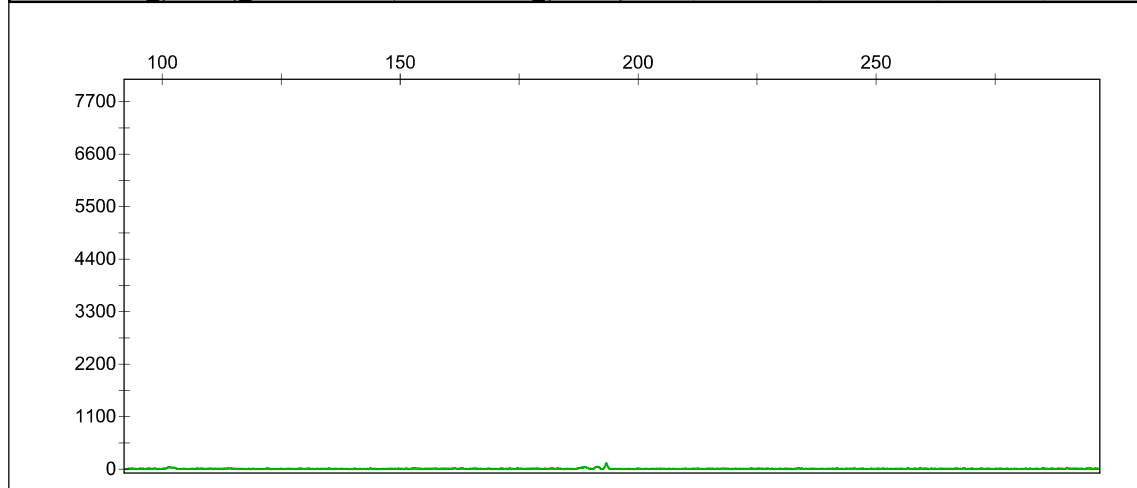
71102731291_ (100-25)_ E10.fsa	71102731291_ (100-25)	None		■	■
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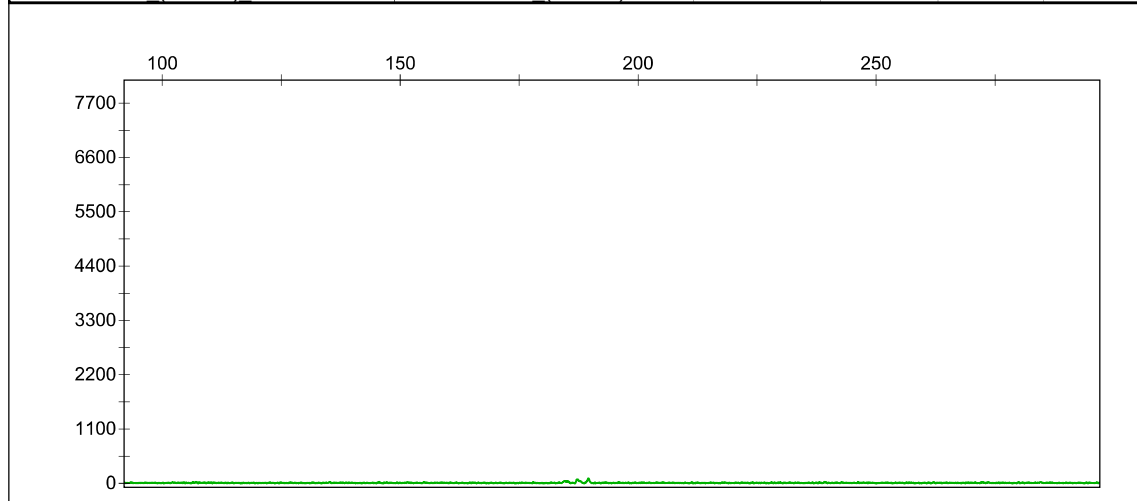
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731292_(100-26)_F10.fsa	71102731292_(100-26)	None		■	■



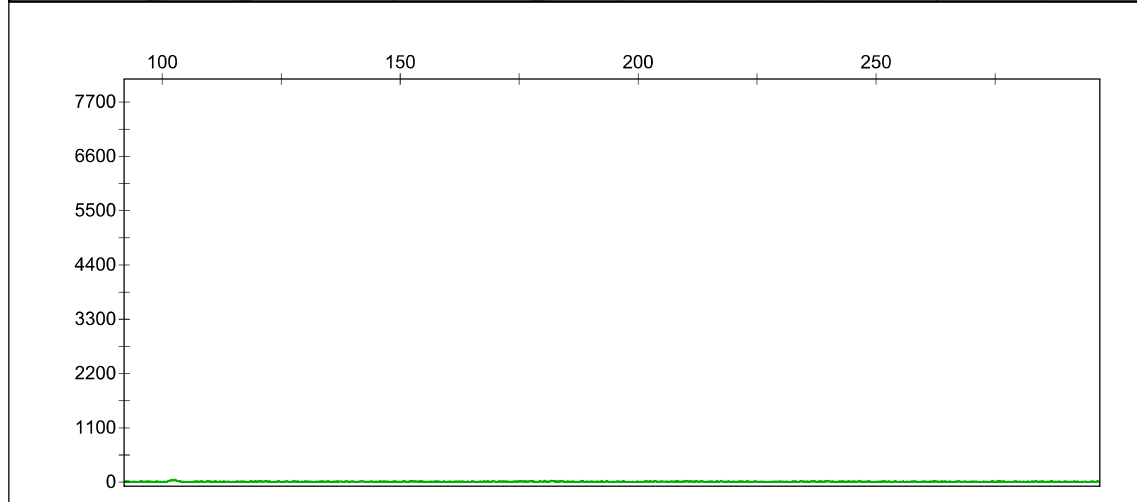
71102731293_(100-27)_G10.fsa	71102731293_(100-27)	None		■	■
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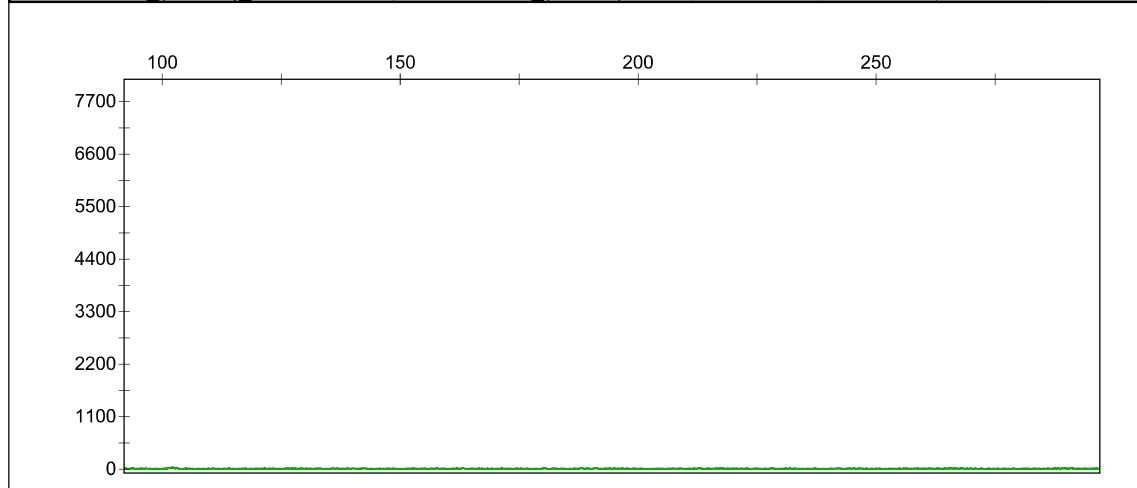
71102731294_(100-29)_H10.fsa	71102731294_(100-29)	None		■	■
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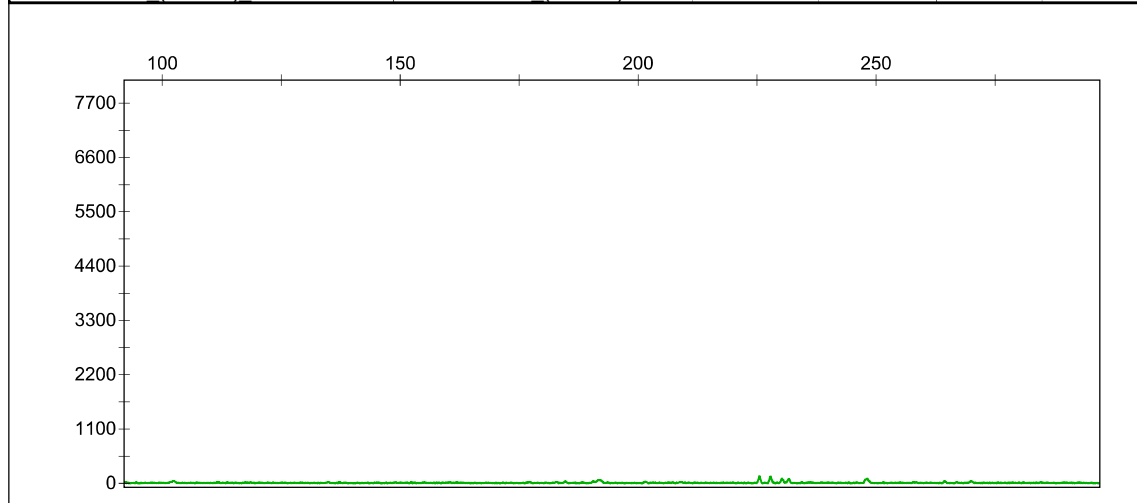
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731295_ (100-30)_A11.fsa	71102731295_ (100-30)	None		■	■



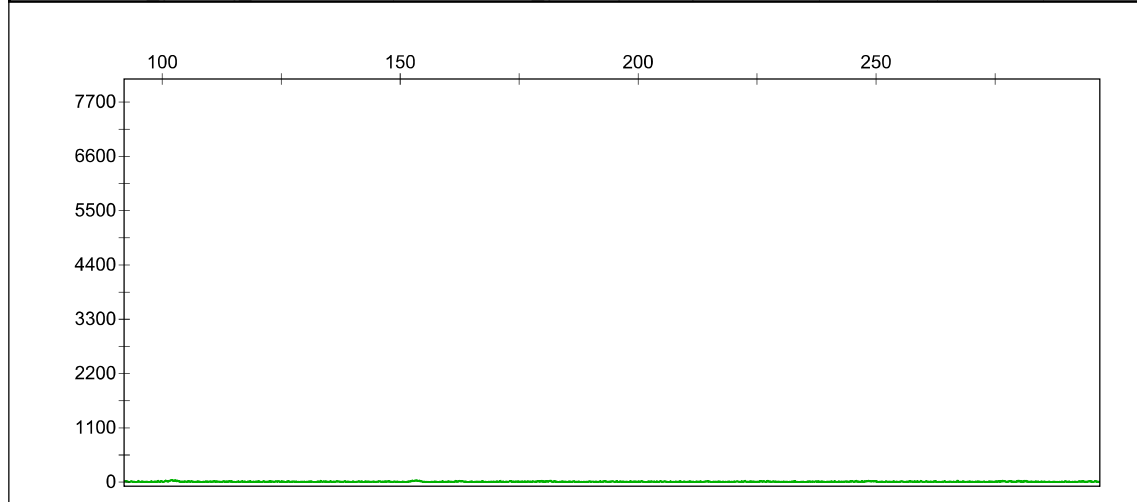
71102731296_ (100-31)_B11.fsa	71102731296_ (100-31)	None		■	■
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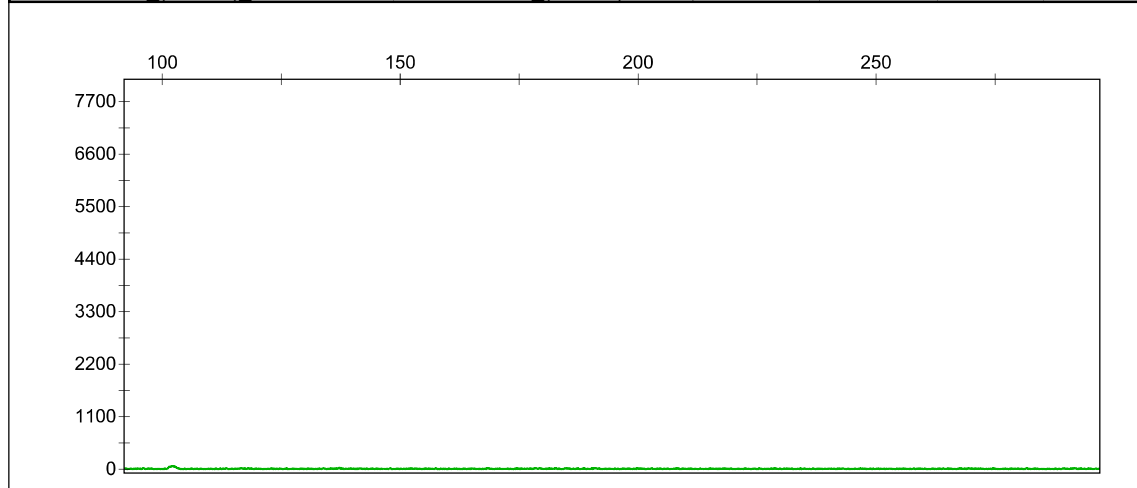
71102731297_ (100-33)_C11.fsa	71102731297_ (100-33)	None		■	■
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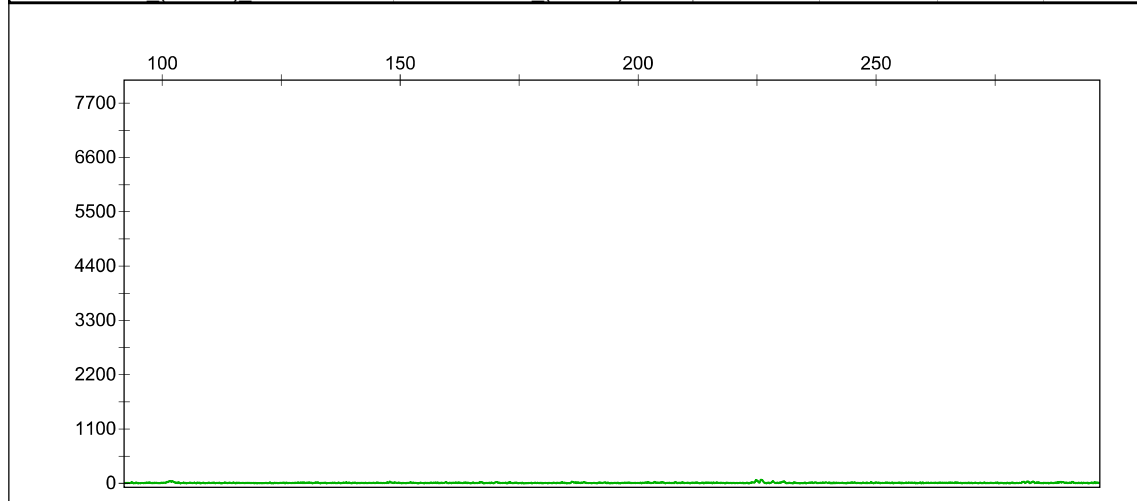
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731298_ (100-34)_D11.fsa	71102731298_ (100-34)	None		■	■



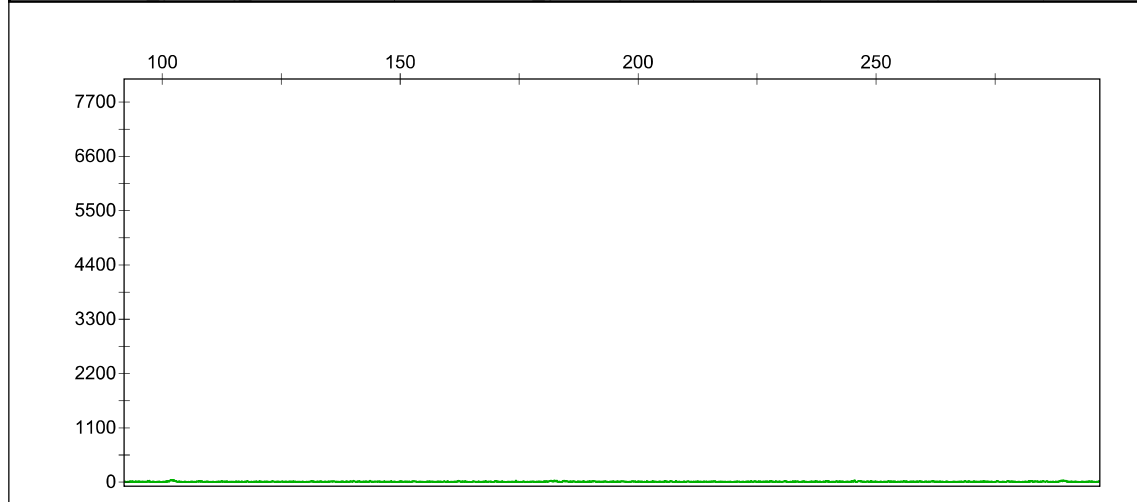
71102731299_ (100-35)_E11.fsa	71102731299_ (100-35)	None		■	■
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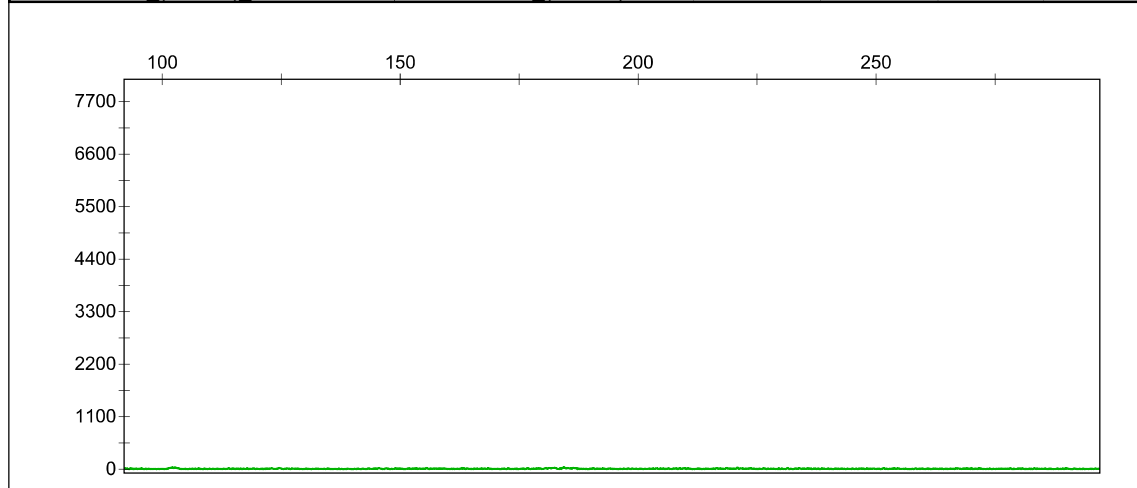
71102731300_ (100-36)_F11.fsa	71102731300_ (100-36)	None		■	■
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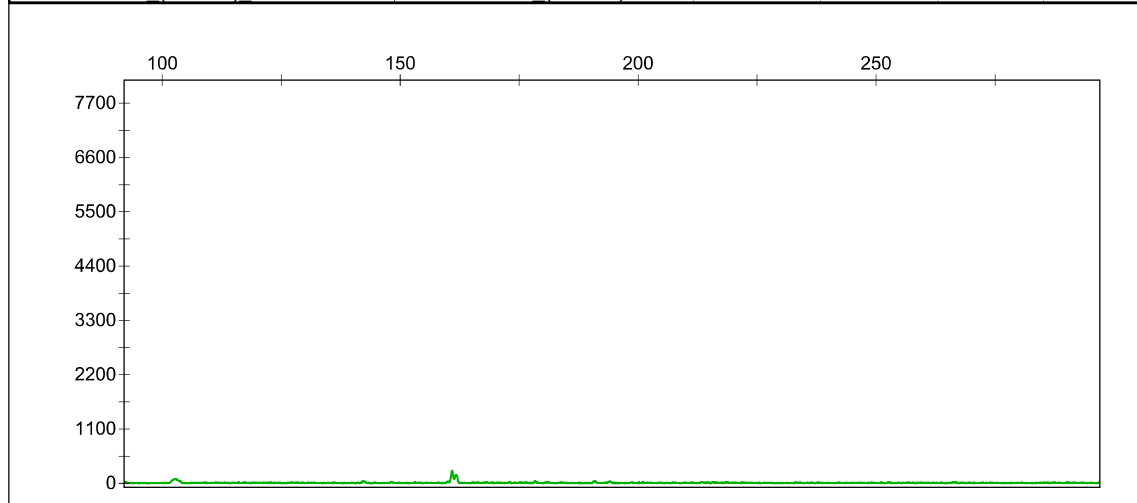
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731301_(100-37)_G11.fsa	71102731301_(100-37)	None		■	■



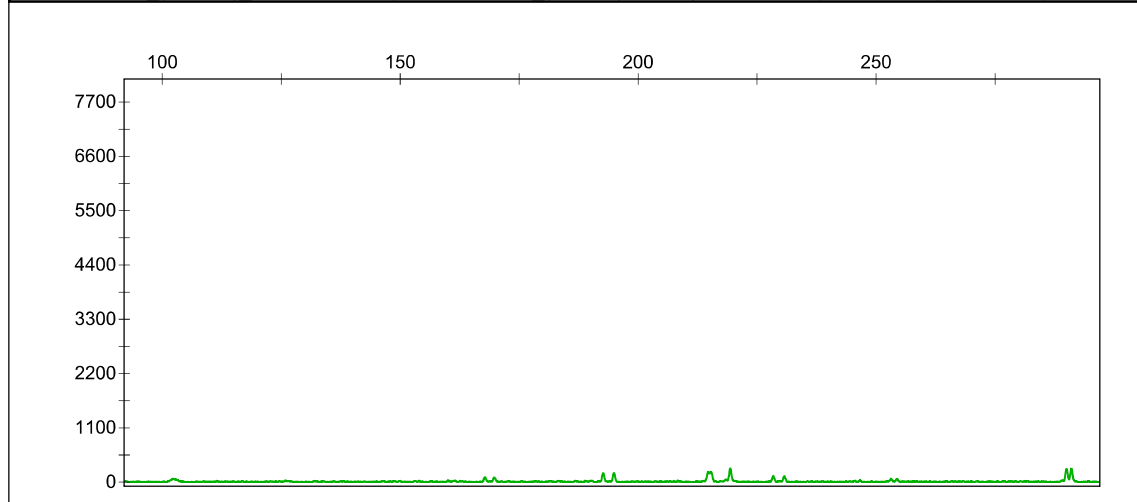
71102731302_(100-38)_H11.fsa	71102731302_(100-38)	None		■	■
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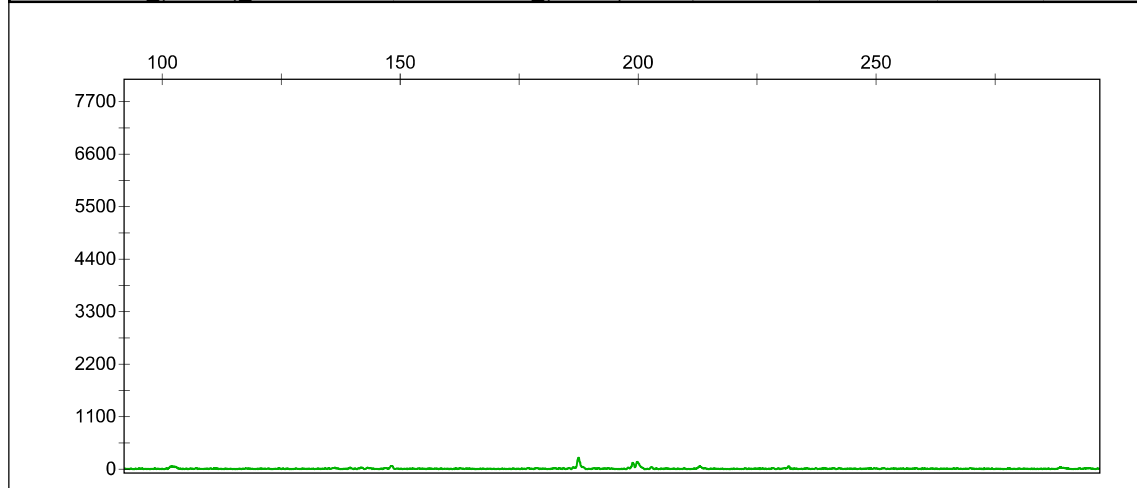
71102731303_(100-39)_A12.fsa	71102731303_(100-39)	None		■	■
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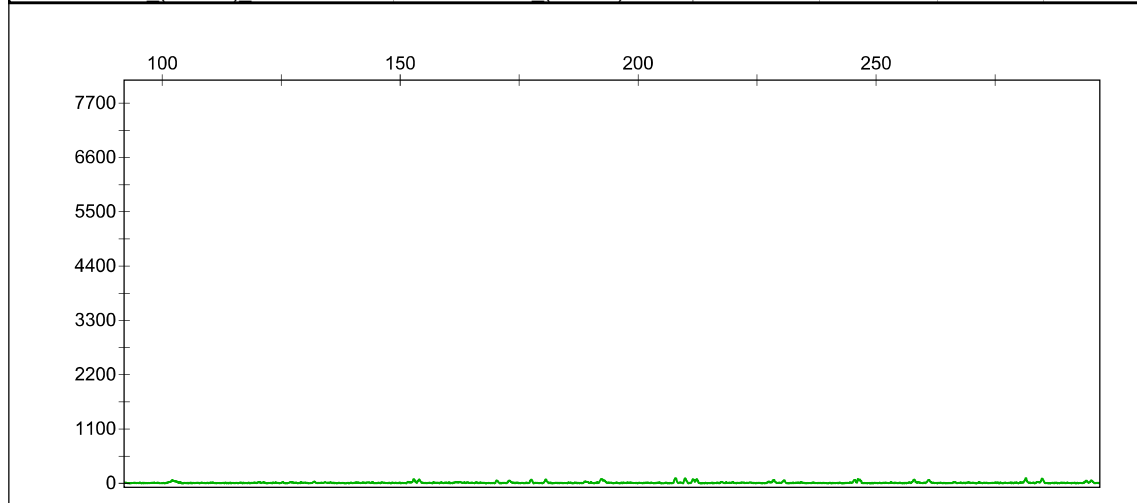
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731304_(100-40)_B12.fsa	71102731304_(100-40)	None		■	■



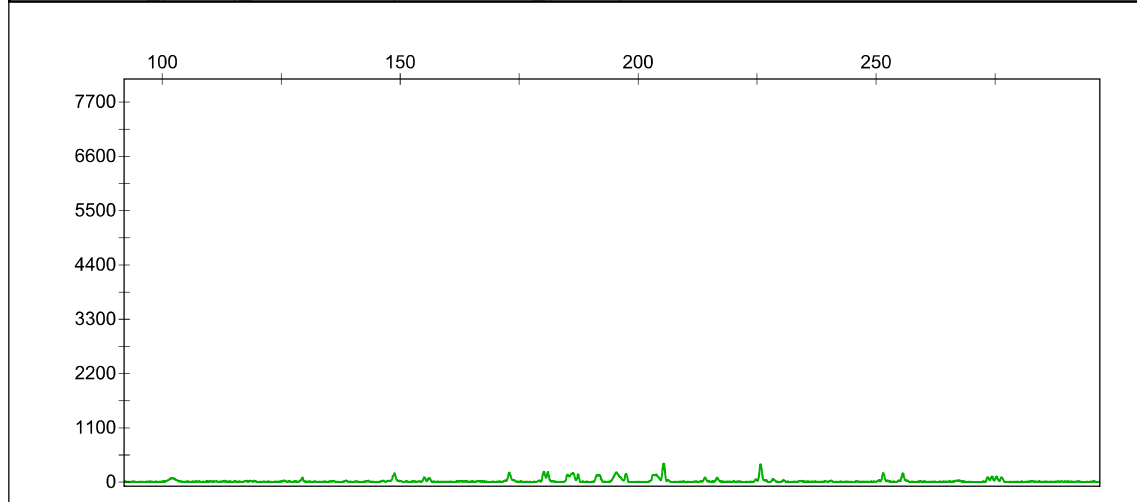
71102731305_(100-41)_C12.fsa	71102731305_(100-41)	None		■	■
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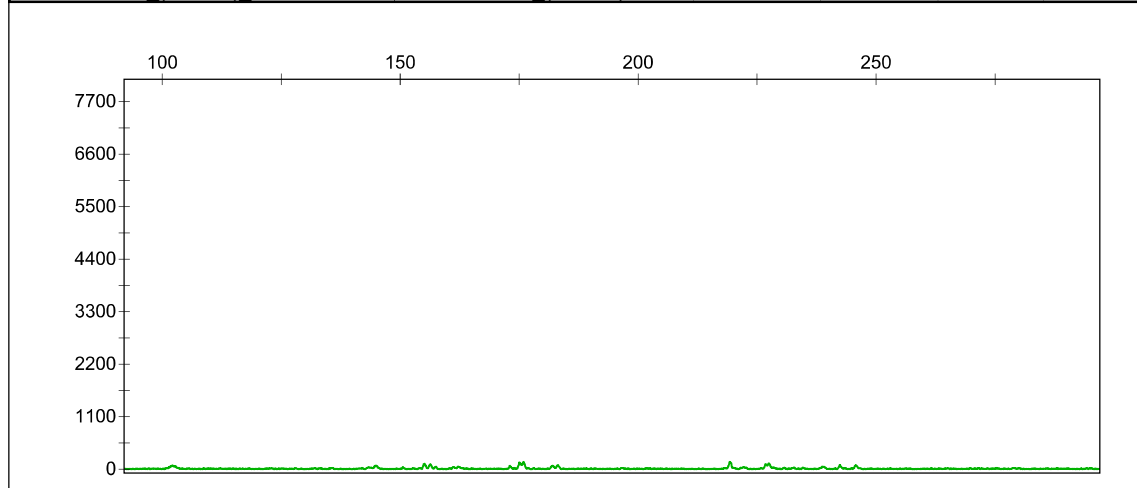
71102731306_(100-42)_D12.fsa	71102731306_(100-42)	None		■	■
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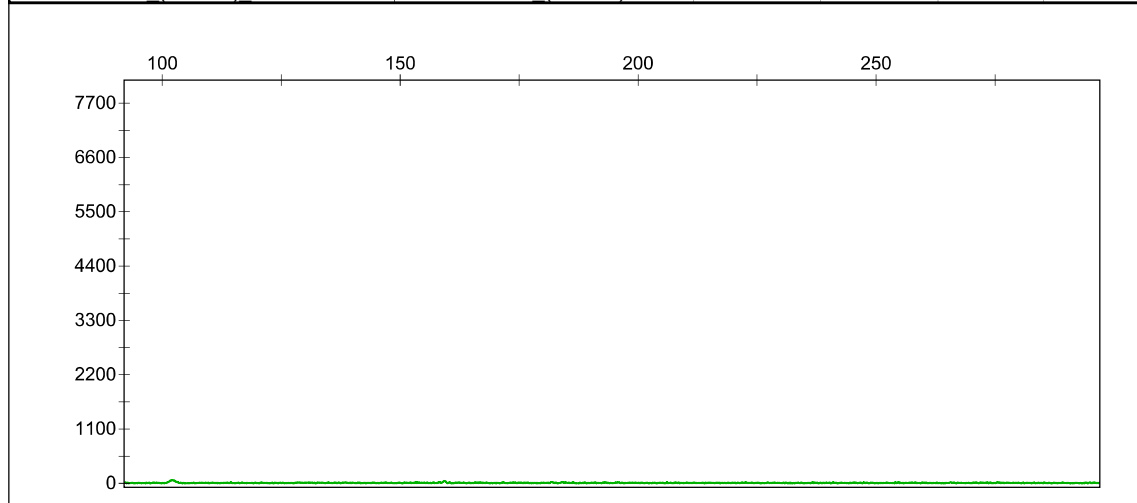
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731307_(100-43)_E12.fsa	71102731307_(100-43)	None		■	■



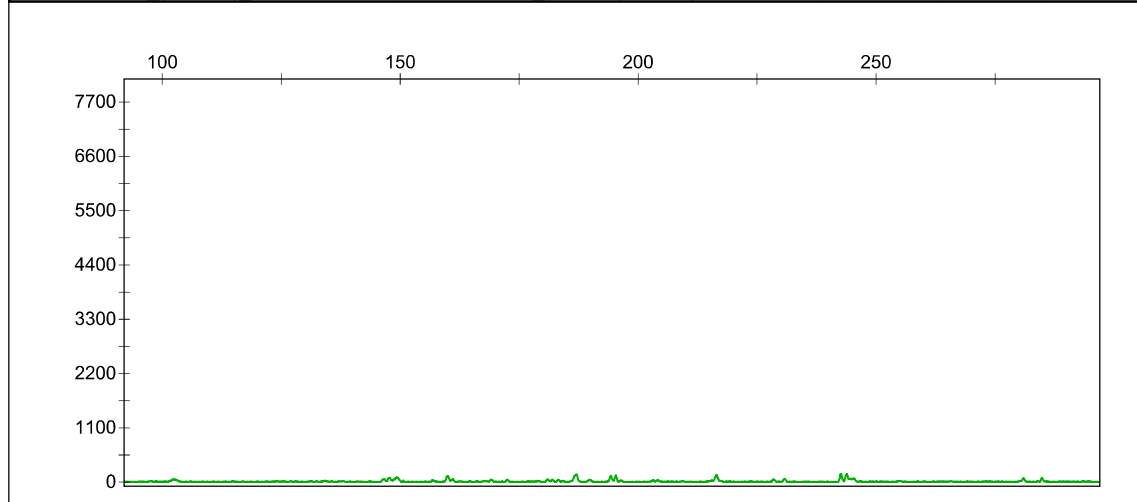
71102731308_(100-45)_F12.fsa	71102731308_(100-45)	None		■	■
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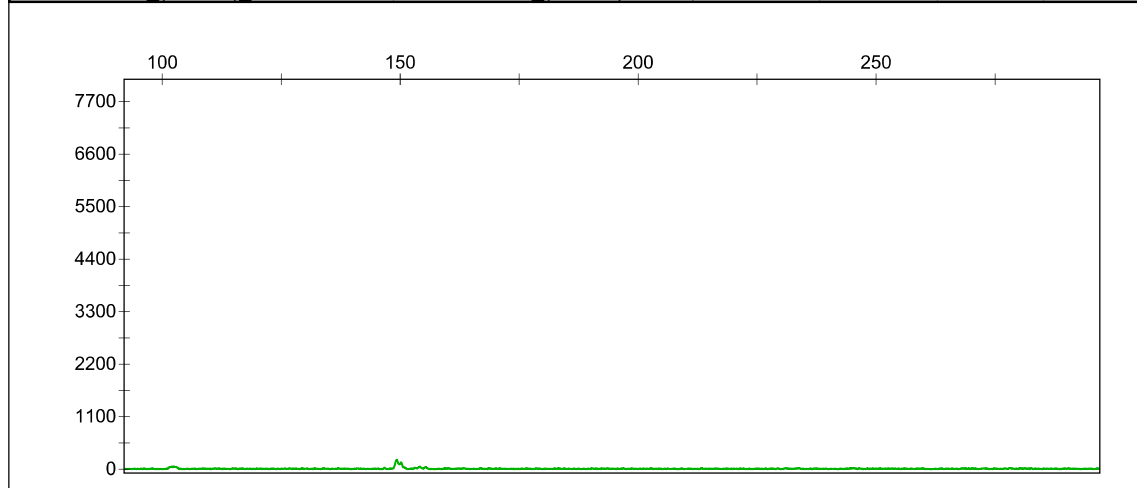
71102731309_(100-46)_G12.fsa	71102731309_(100-46)	None		■	■
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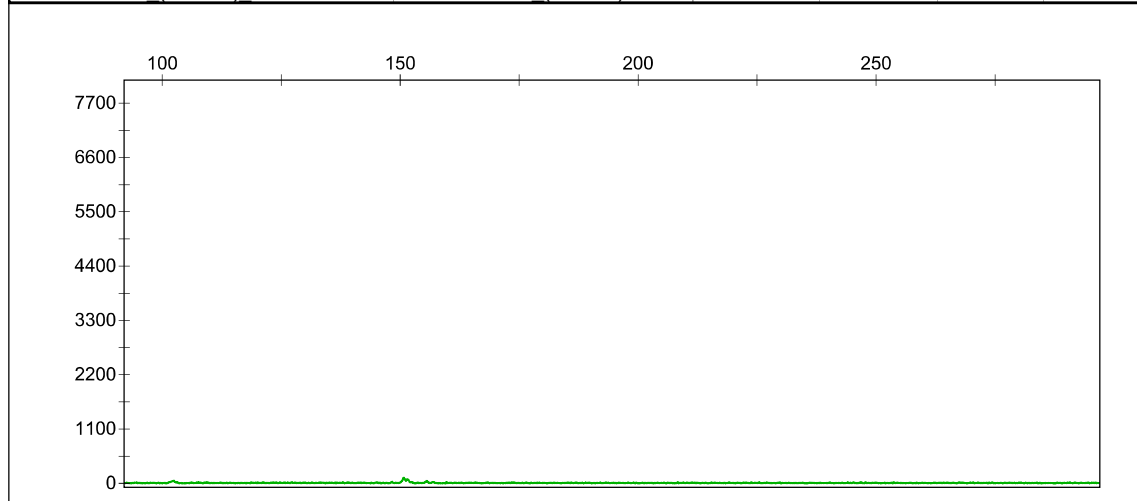
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731310_(100-47)_H12.fsa	71102731310_(100-47)	None		■	■



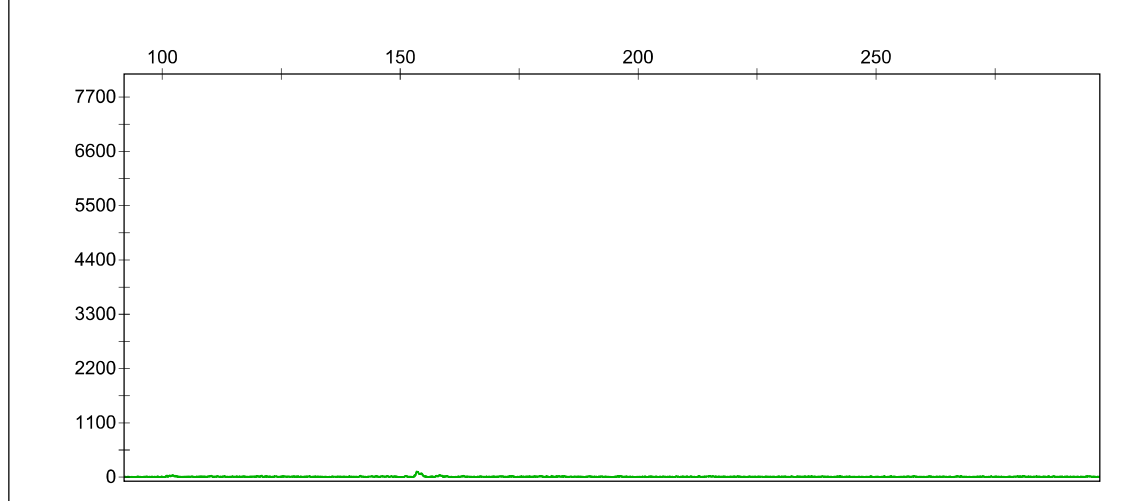
71102731311_(100-48)_A01.fsa	71102731311_(100-48)	None		■	■
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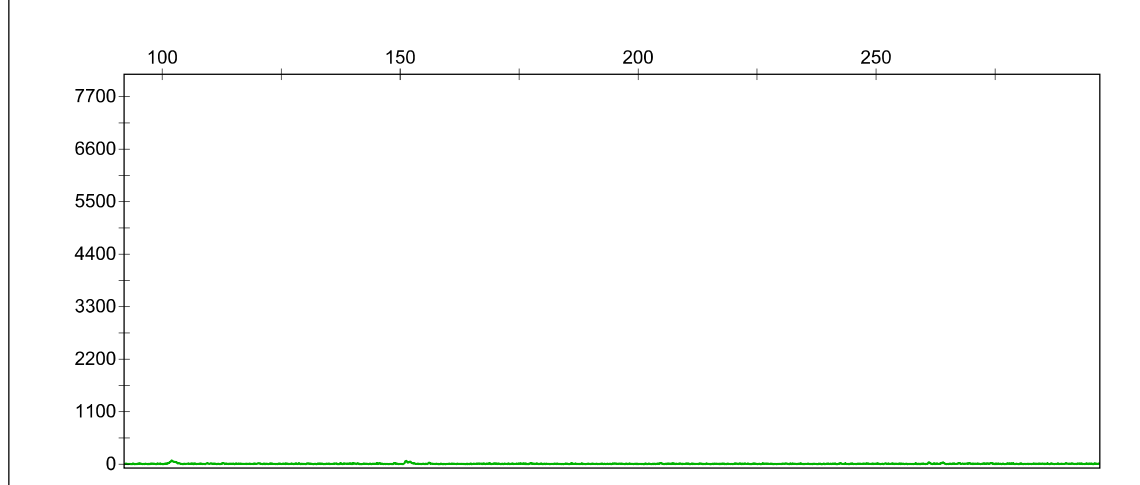
71102731312_(100-49)_B01.fsa	71102731312_(100-49)	None		■	■
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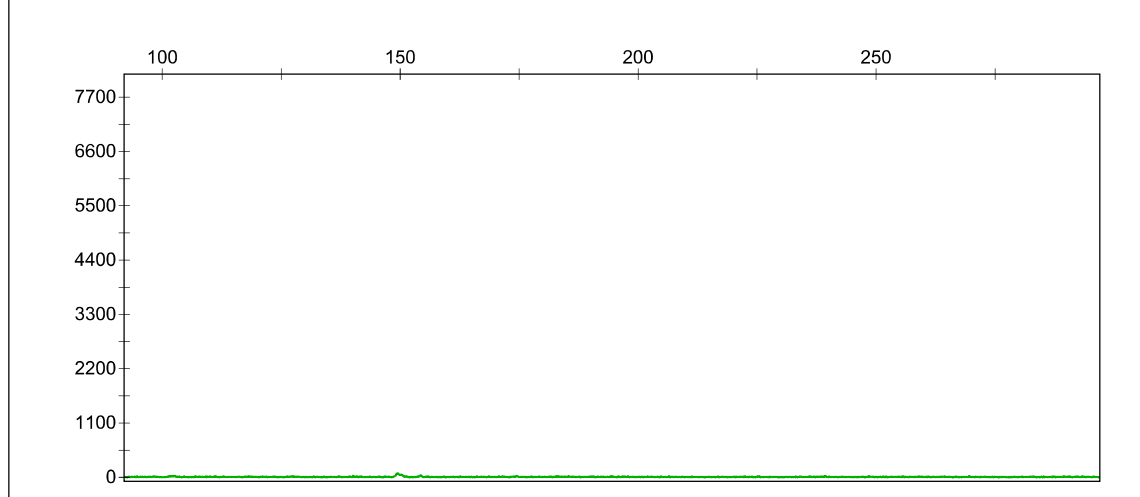
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731313_ (100-50)_ C01.fsa	71102731313_ (100-50)	None		■	■



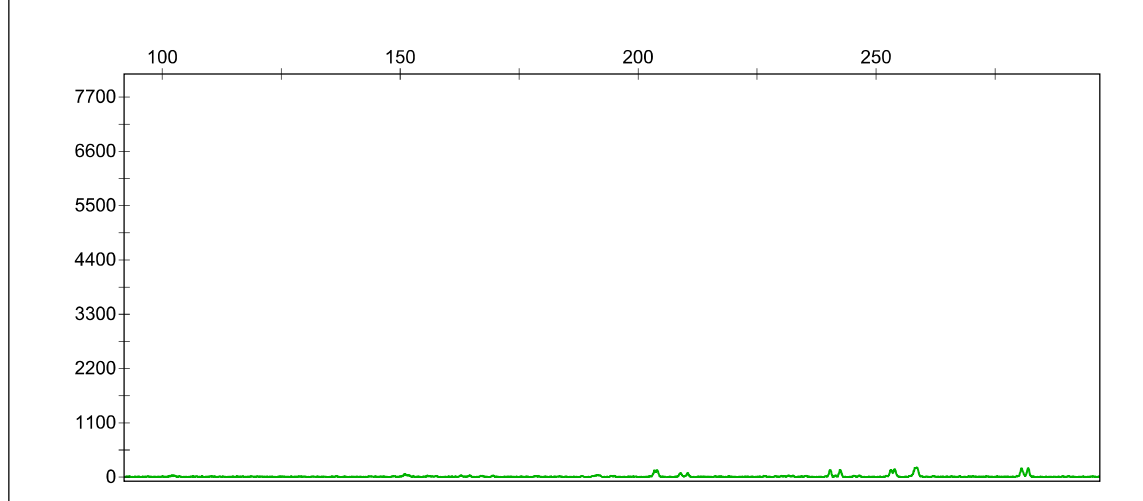
71102731314_ (100-51)_ D01.fsa	71102731314_ (100-51)	None		■	■
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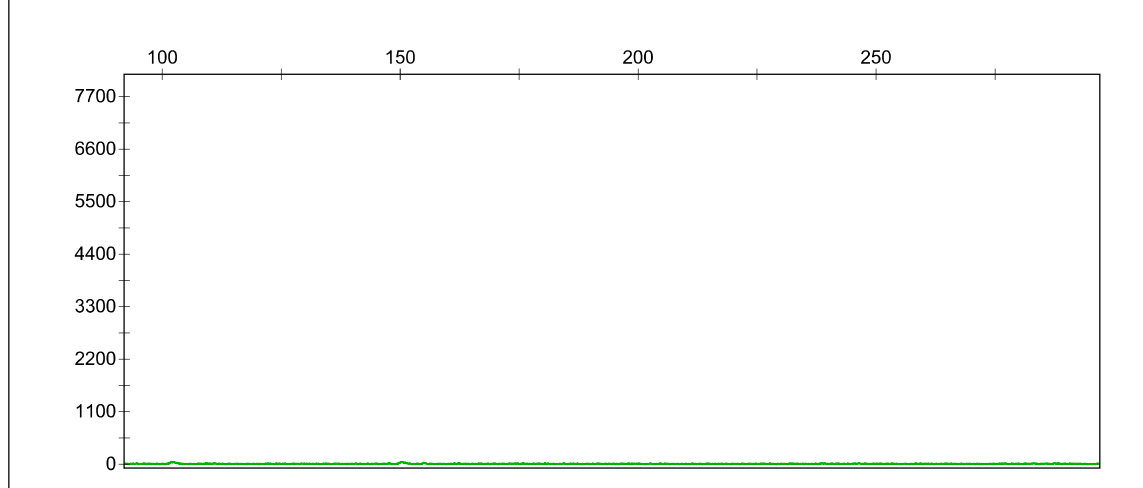
71102731315_ (100-53)_ E01.fsa	71102731315_ (100-53)	None		■	■
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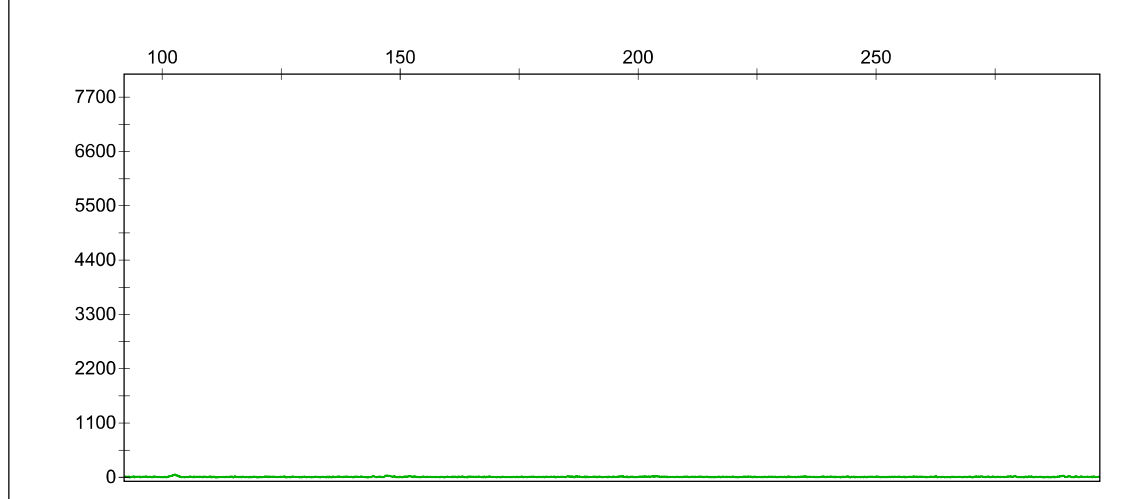
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731316_(100-54)_F01.fsa	71102731316_(100-54)	None		■	■



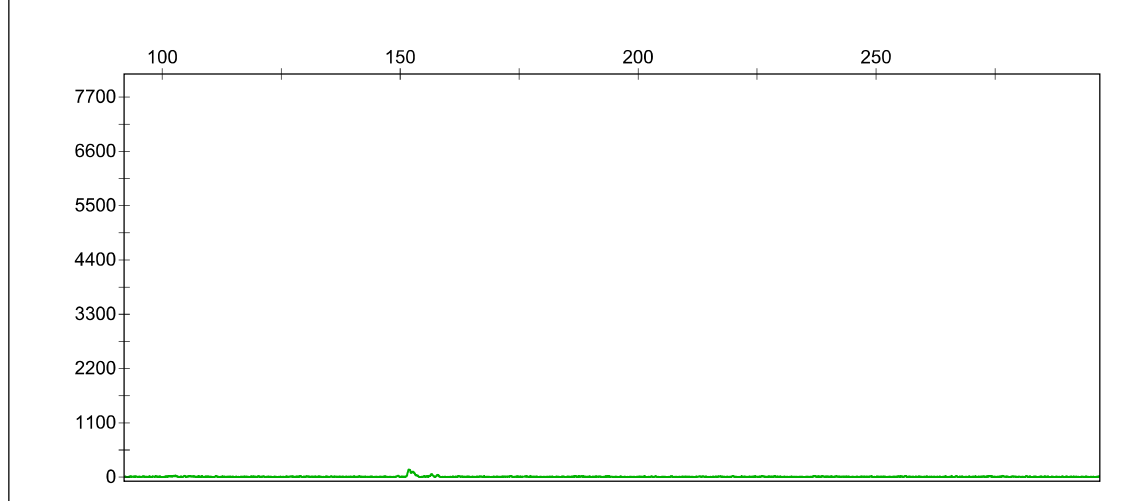
71102731317_(100-55)_G01.fsa	71102731317_(100-55)	None		■	■
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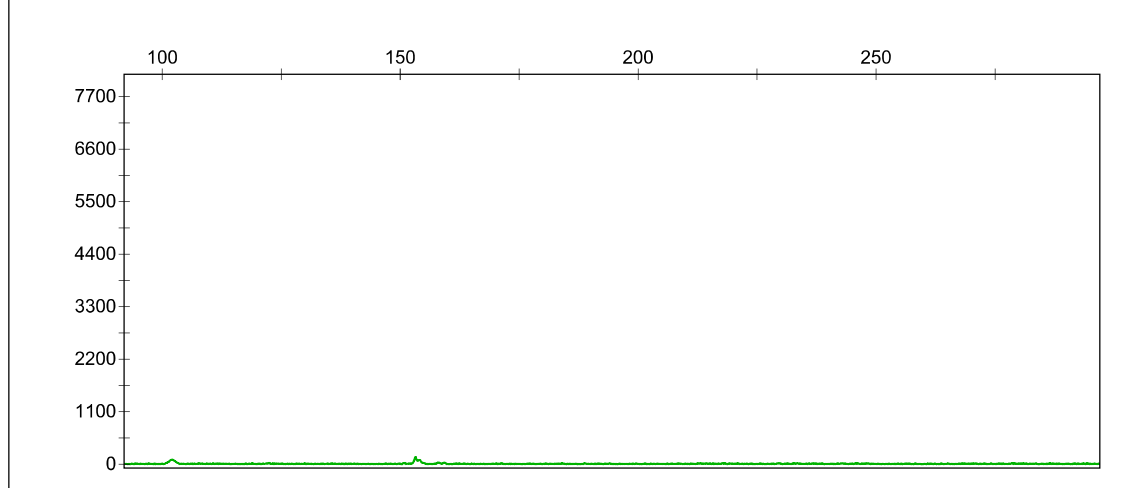
71102731318_(100-56)_H01.fsa	71102731318_(100-56)	None		■	■
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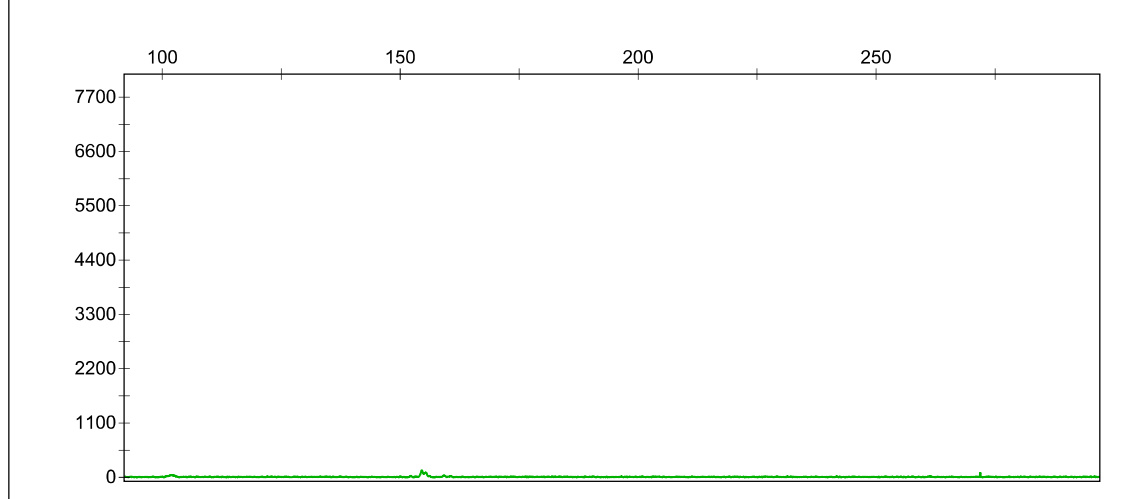
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731319_(100-57)_A02.fsa	71102731319_(100-57)	None		■	■



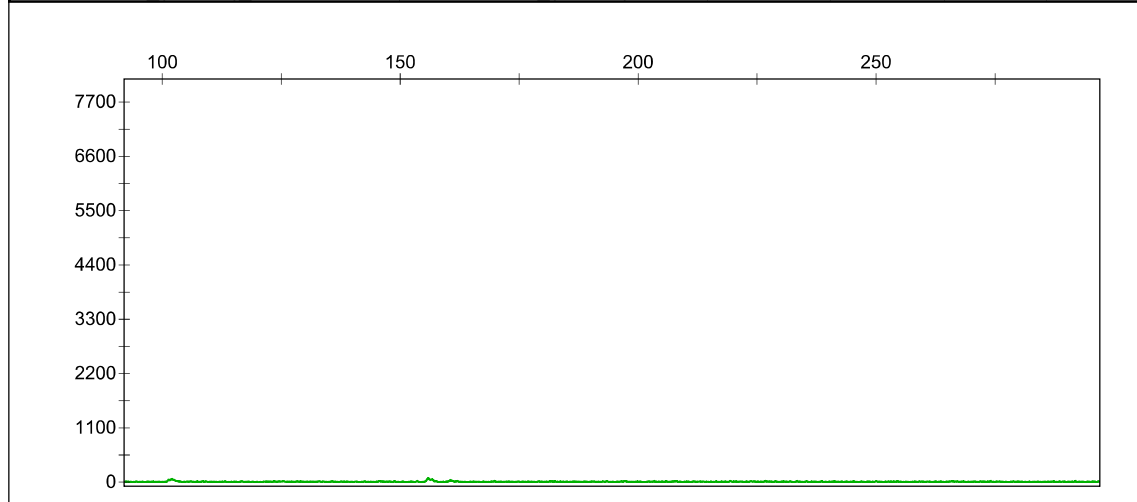
71102731320_(100-58)_B02.fsa	71102731320_(100-58)	None		■	■
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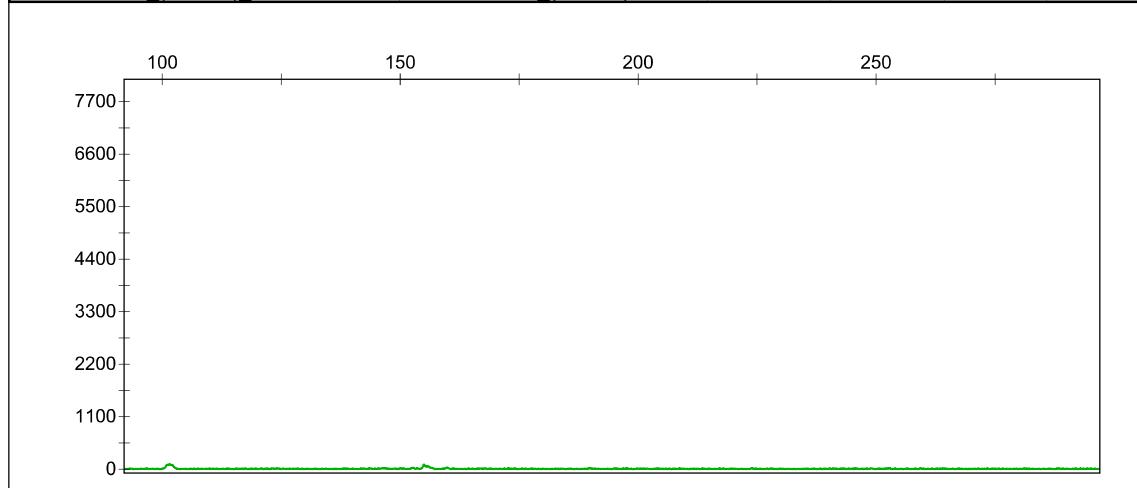
71102731321_(100-59)_C02.fsa	71102731321_(100-59)	None		■	■
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Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731322_(100-60)_D02.fsa	71102731322_(100-60)	None		■	■



71102731323_(100-61)_E02.fsa	71102731323_(100-61)	None		■	■
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71102731324_(100-100)_F02.fsa	71102731324_(100-100)	None		■	■
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