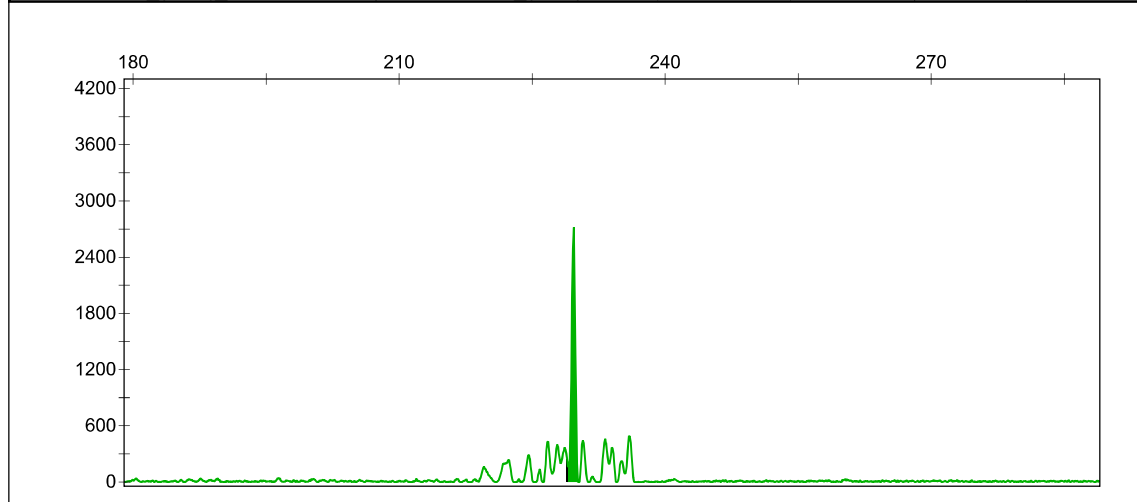
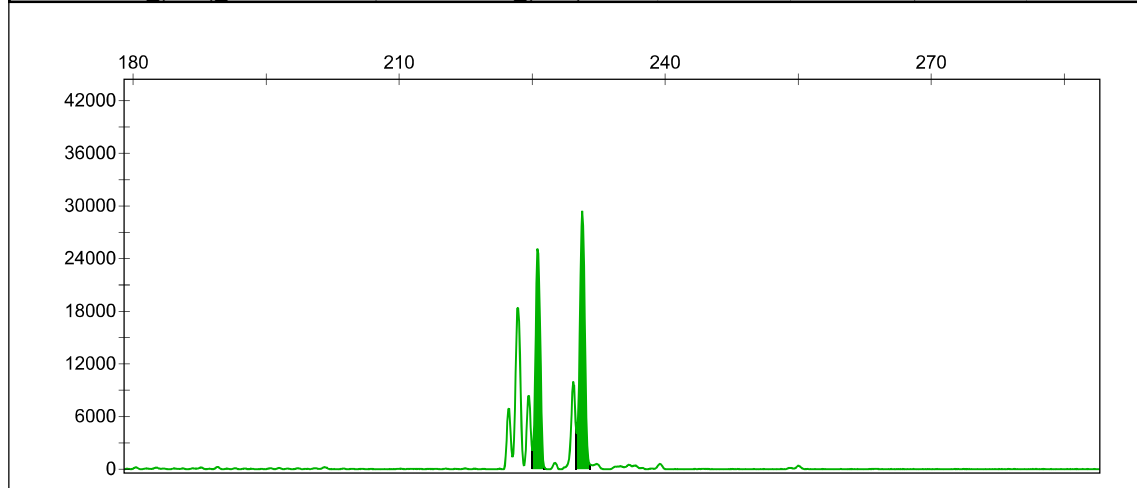


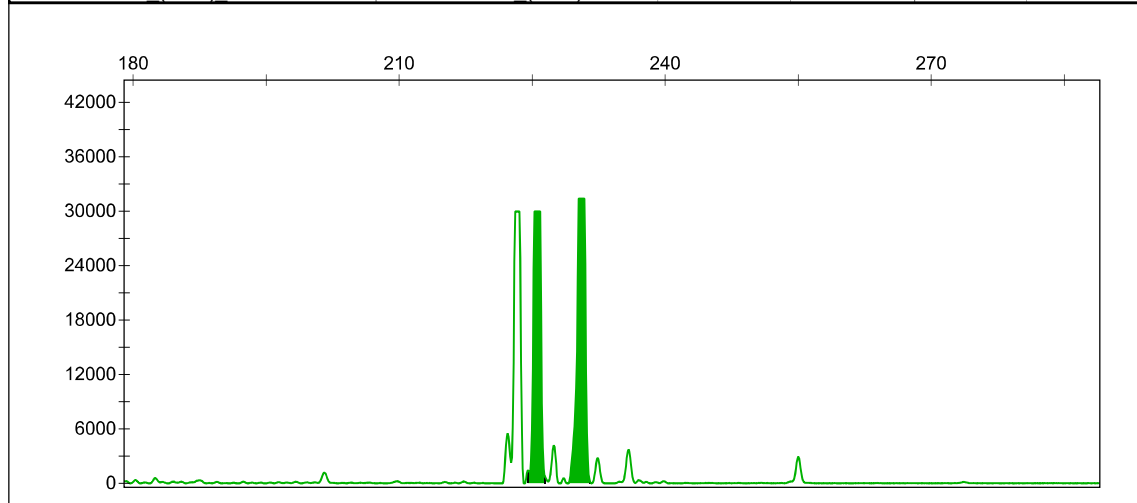
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731541_(14-1)_A10.fsa	71102731541_(14-1)	None		■	■



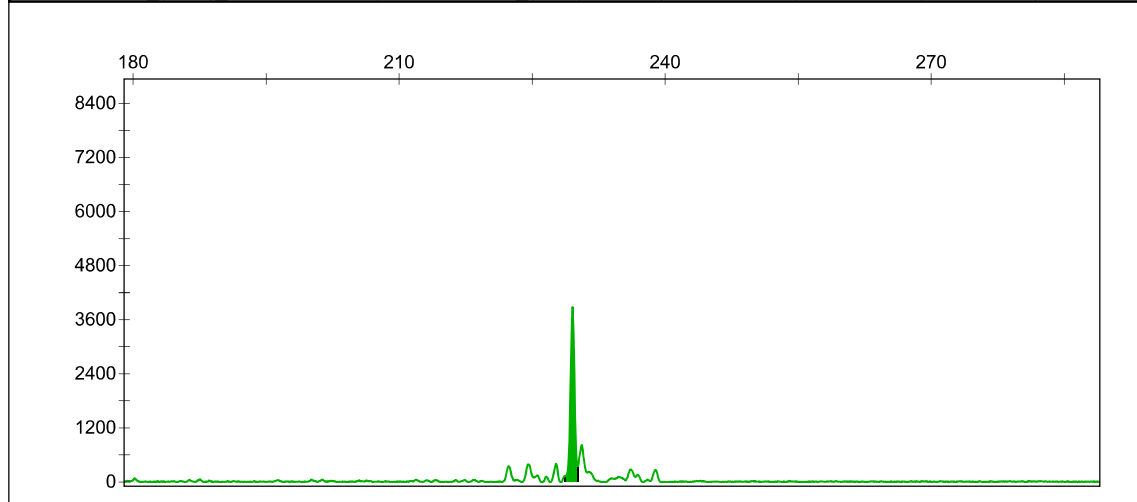
71102731542_(14-3)_B10.fsa	71102731542_(14-3)	None		■	■
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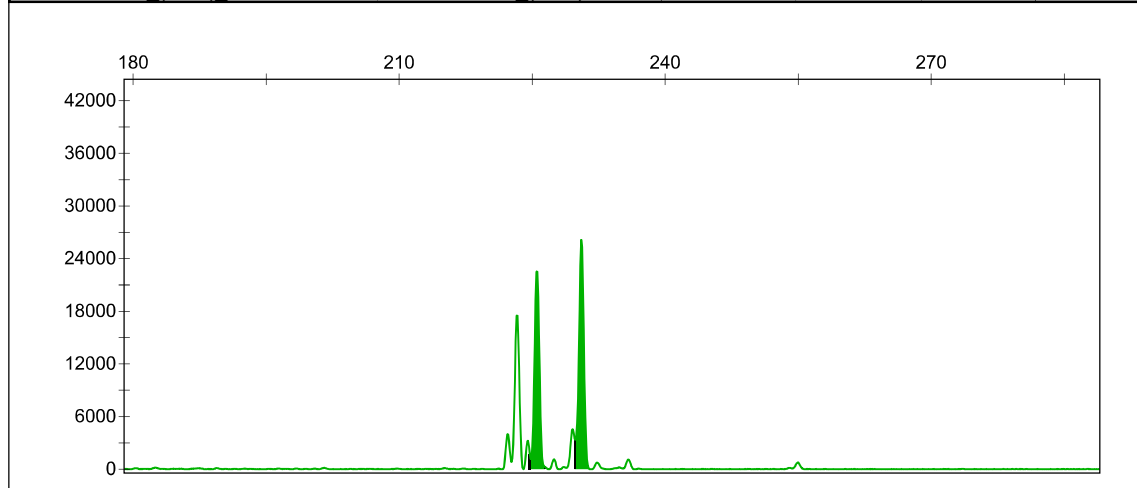
71102731543_(14-5)_C10.fsa	71102731543_(14-5)	None		▲	■
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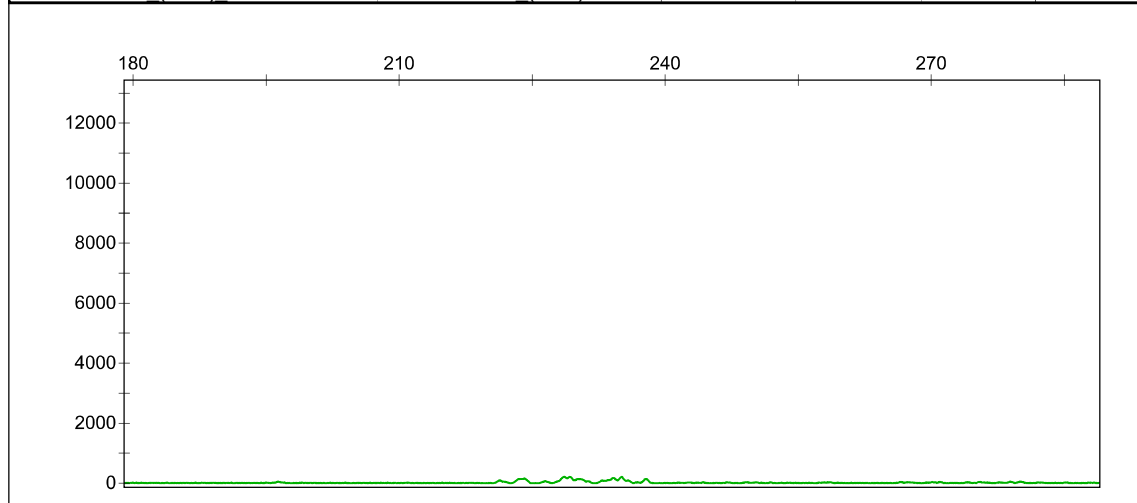
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731544_(14-6)_D10.fsa	71102731544_(14-6)	None			



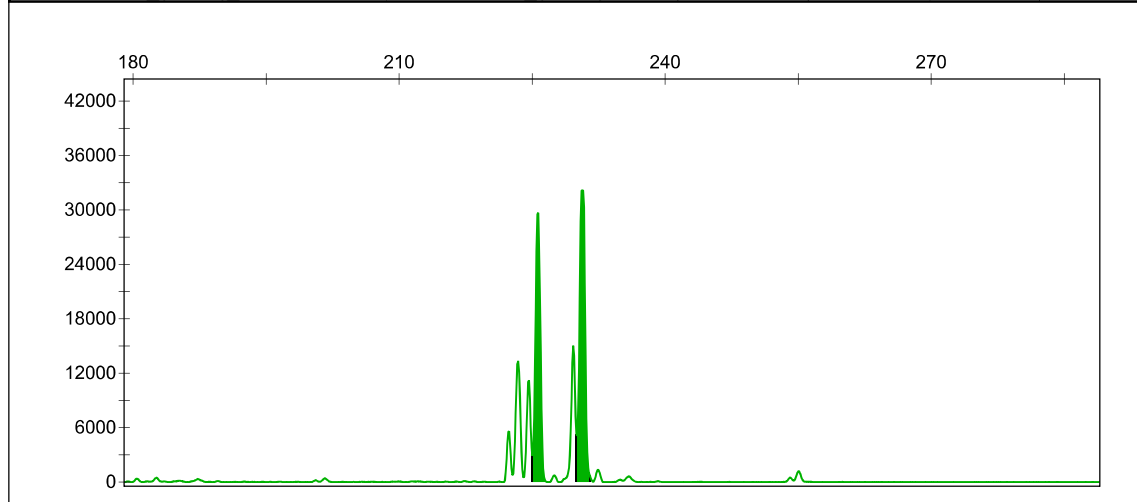
71102731545_(14-7)_E10.fsa	71102731545_(14-7)	None			
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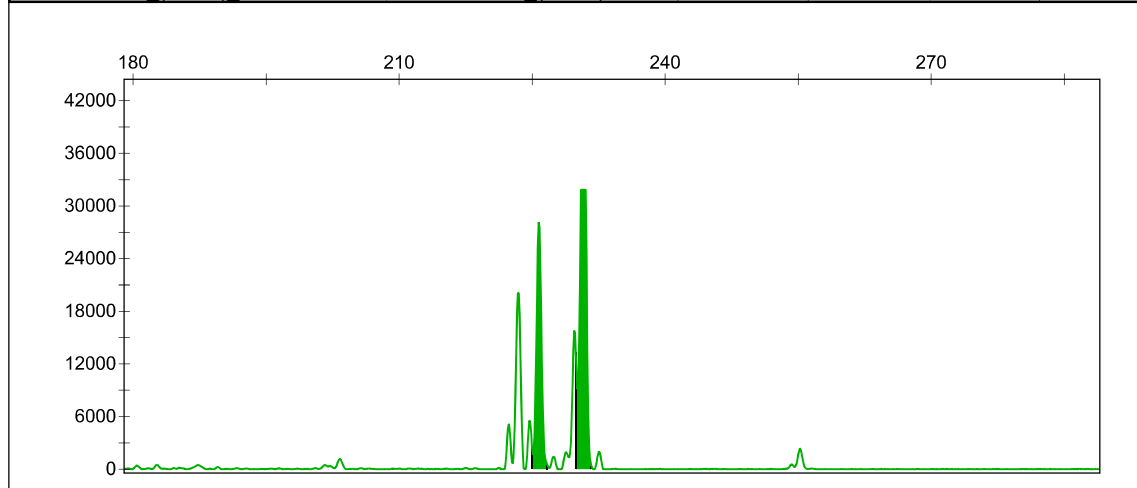
71102731546_(14-9)_F10.fsa	71102731546_(14-9)	None			
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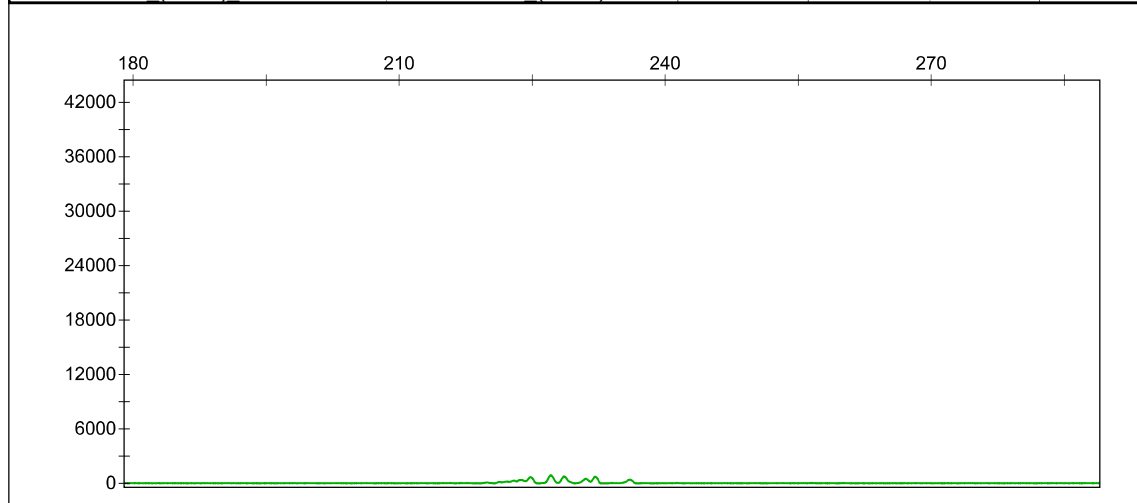
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731547_(14-10)_G10.fsa	71102731547_(14-10)	None		▲	■



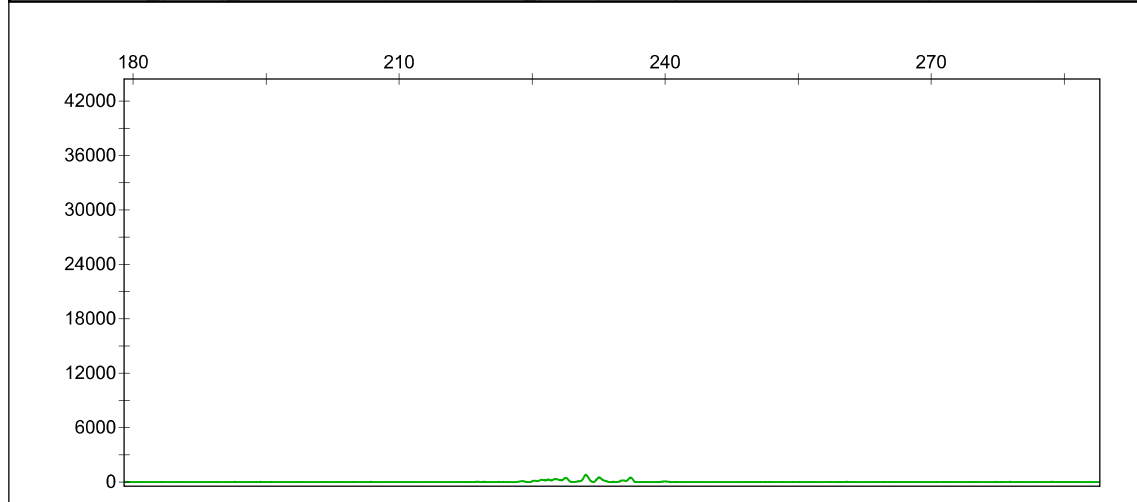
71102731548_(14-12)_H10.fsa	71102731548_(14-12)	None		▲	■
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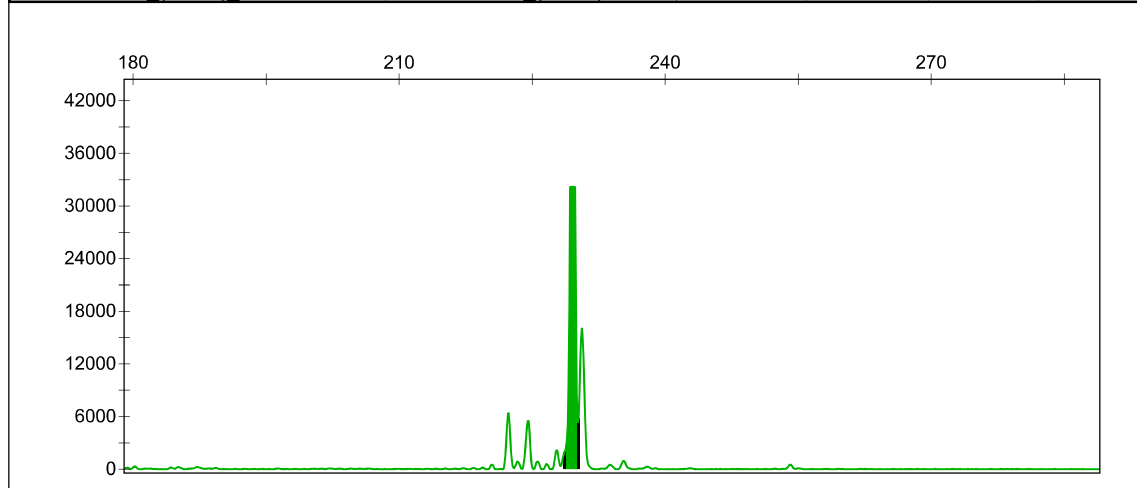
71102731549_(14-13)_A11.fsa	71102731549_(14-13)	None		■	■
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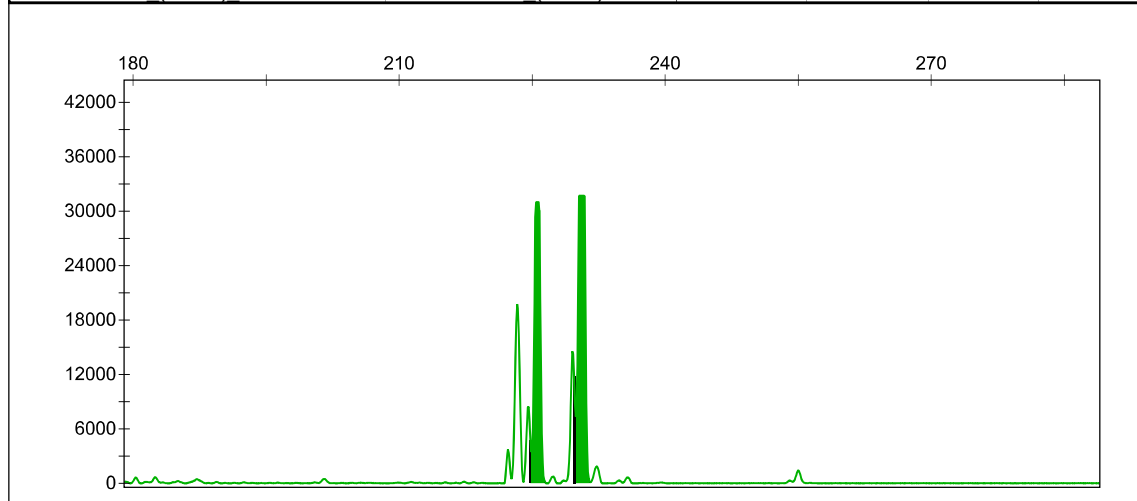
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731550_(14-14)_B11.fsa	71102731550_(14-14)	None		■	■



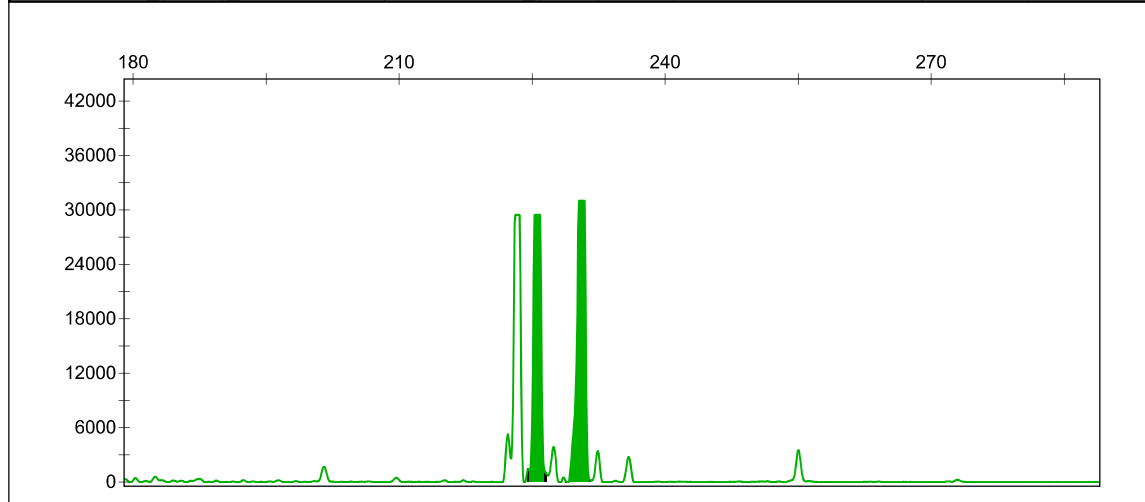
71102731551_(14-15)_C11.fsa	71102731551_(14-15)	None		▲	■
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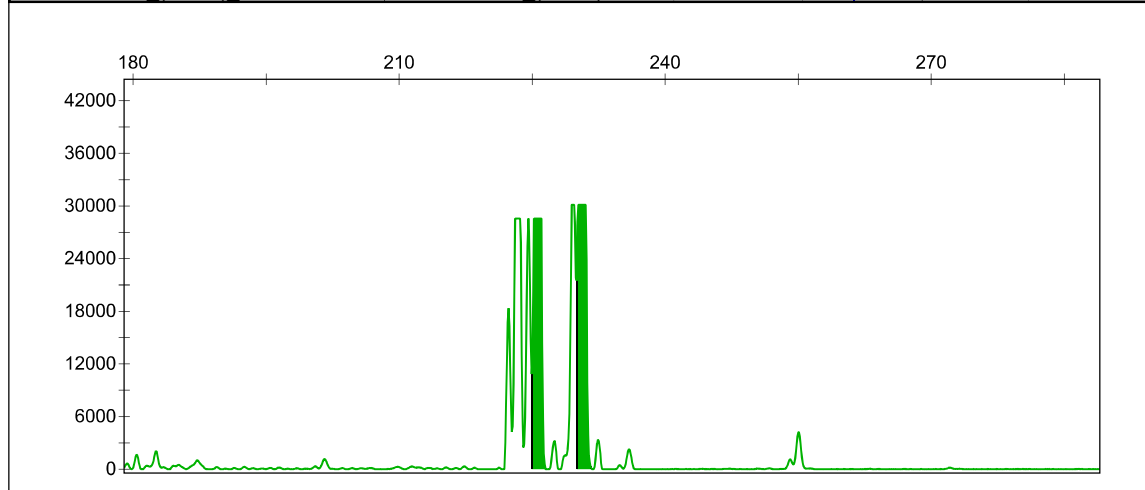
71102731552_(14-16)_D11.fsa	71102731552_(14-16)	None		▲	■
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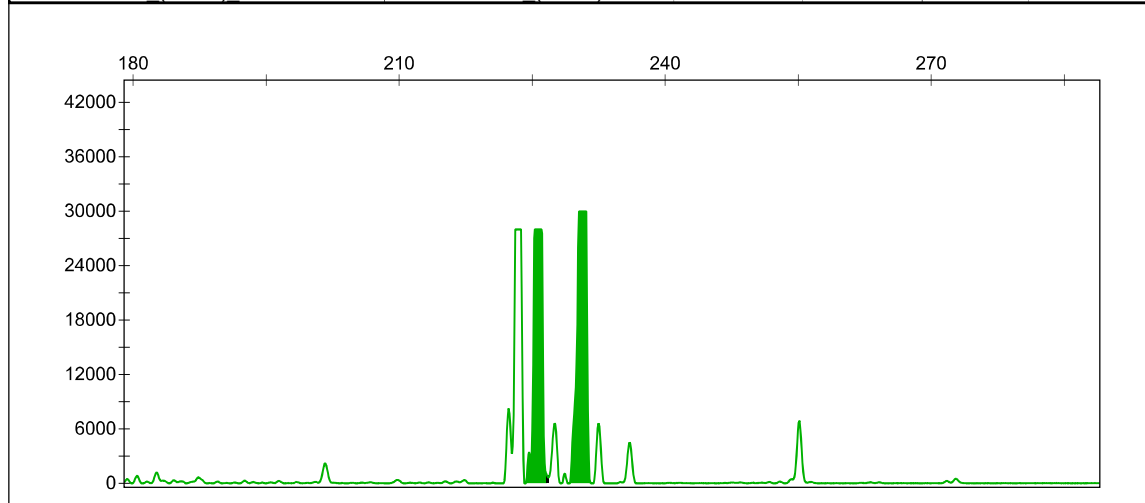
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731553 (14-17)_E11.fsa	71102731553 (14-17)	None		▲	■



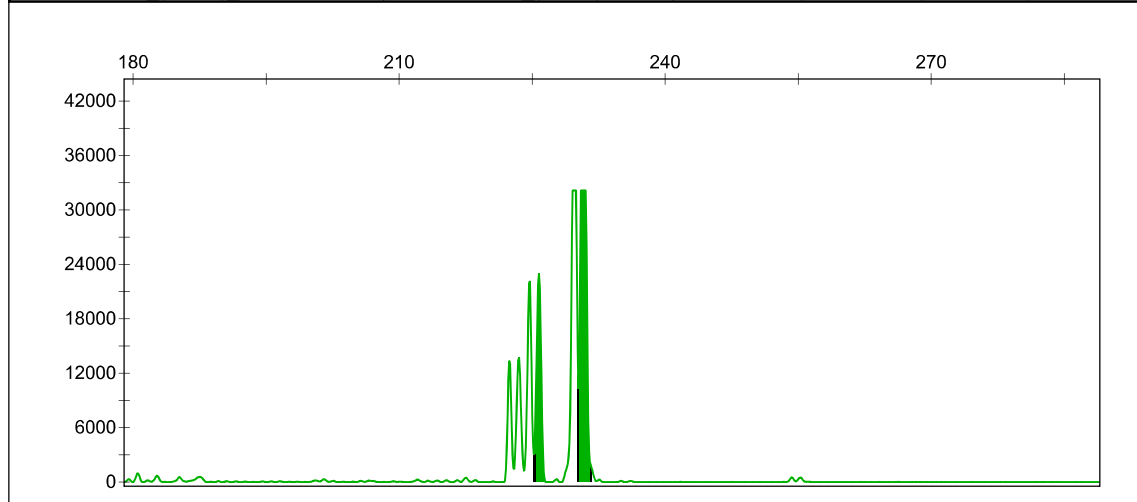
71102731554 (14-18)_F11.fsa	71102731554 (14-18)	None	✗	▲	■
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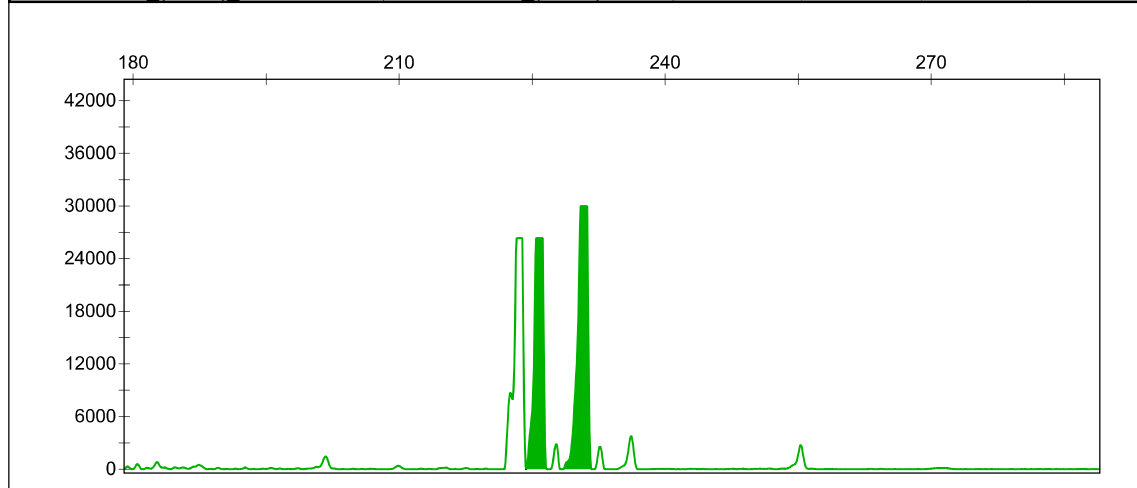
71102731555 (14-19)_G11.fsa	71102731555 (14-19)	None		▲	▲
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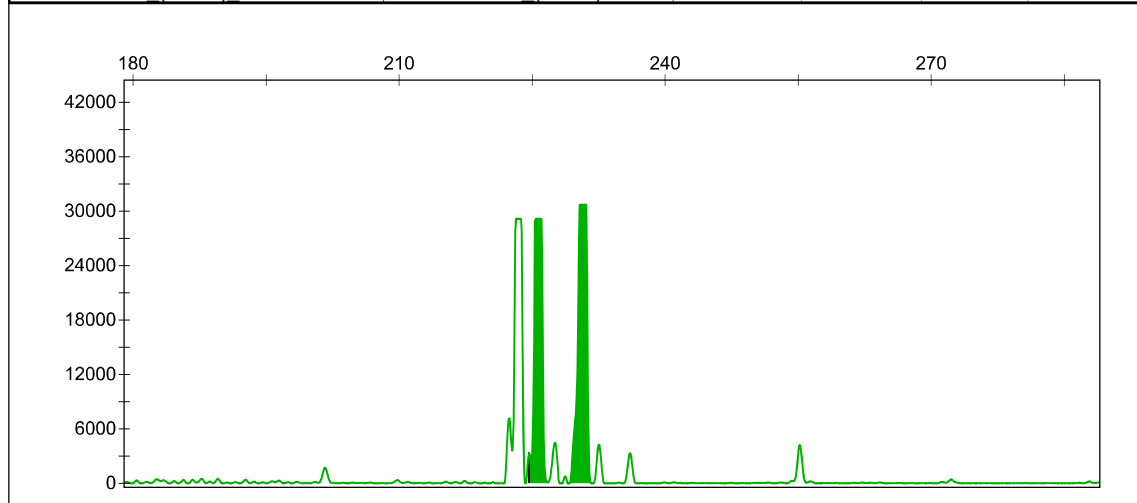
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731556_(14-20)_H11.fsa	71102731556_(14-20)	None		▲	■



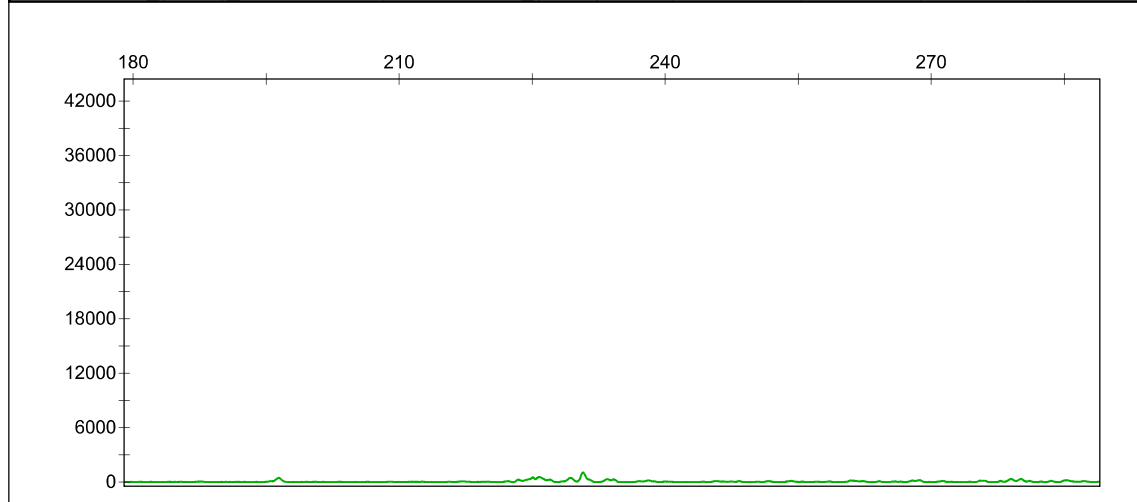
71102731557_(14-21)_A12.fsa	71102731557_(14-21)	None		▲	▲
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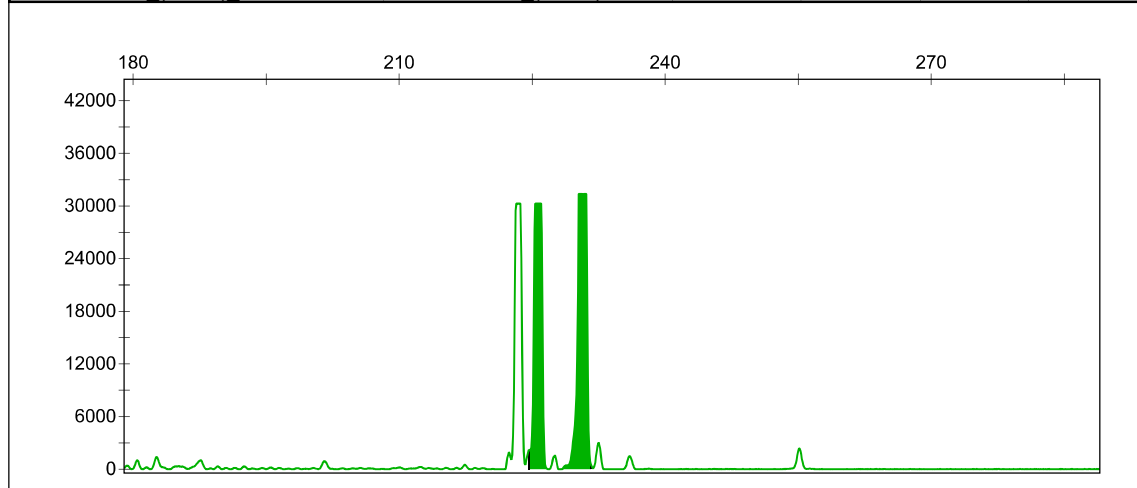
71102731558_(14-22)_B12.fsa	71102731558_(14-22)	None		▲	▲
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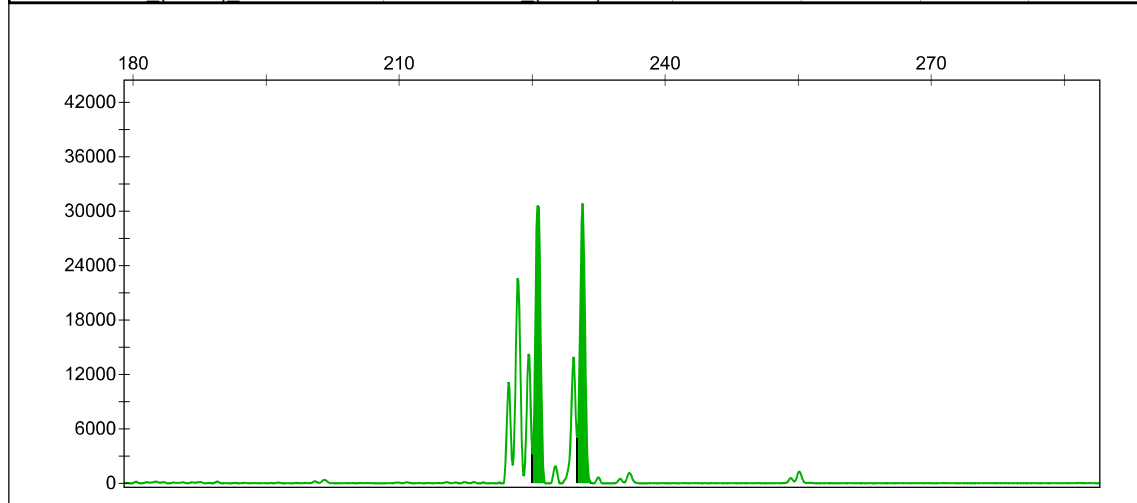
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731559_ (14-23)_C12.fsa	71102731559_ (14-23)	None		■	■



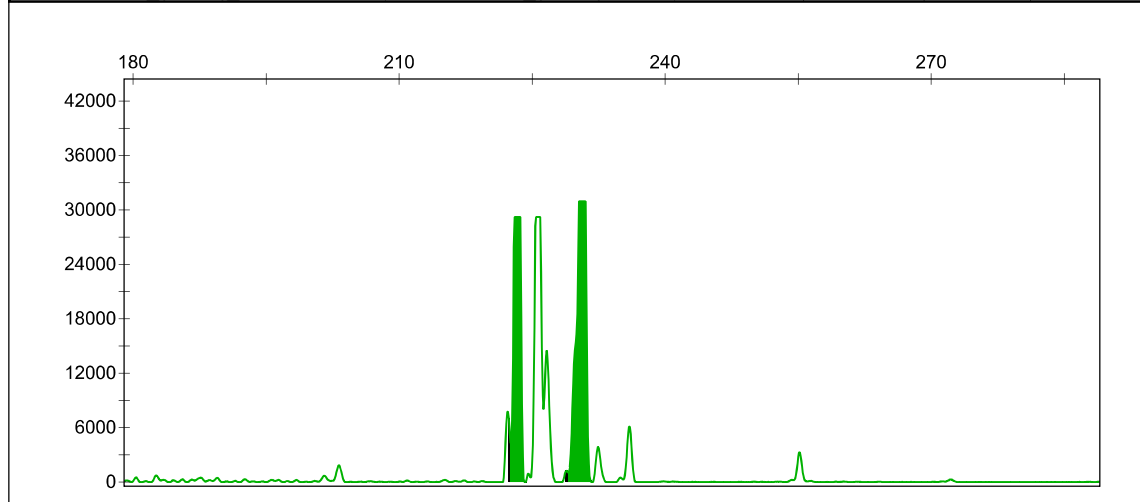
71102731560_ (14-24)_D12.fsa	71102731560_ (14-24)	None		▲	▲
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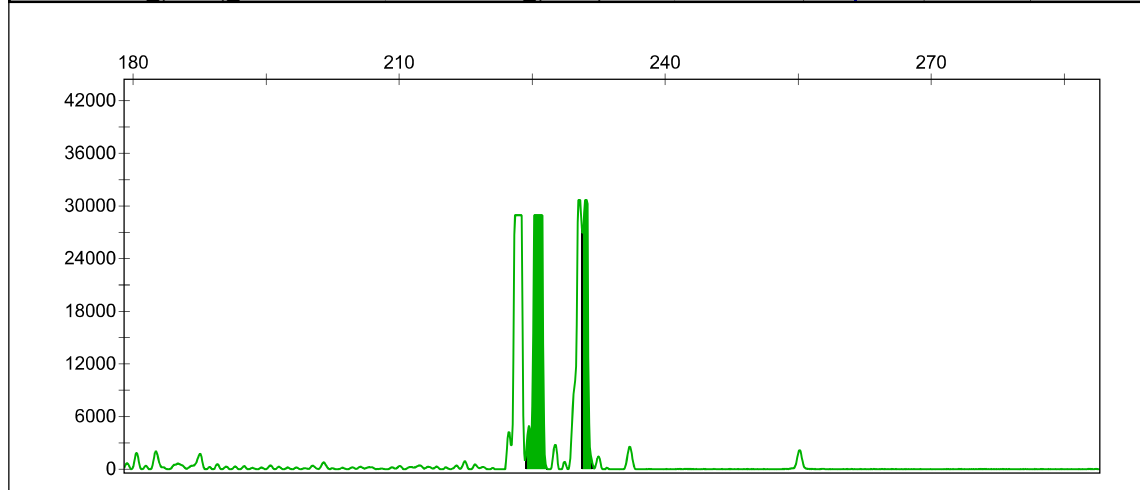
71102731561_ (14-25)_E12.fsa	71102731561_ (14-25)	None		▲	■
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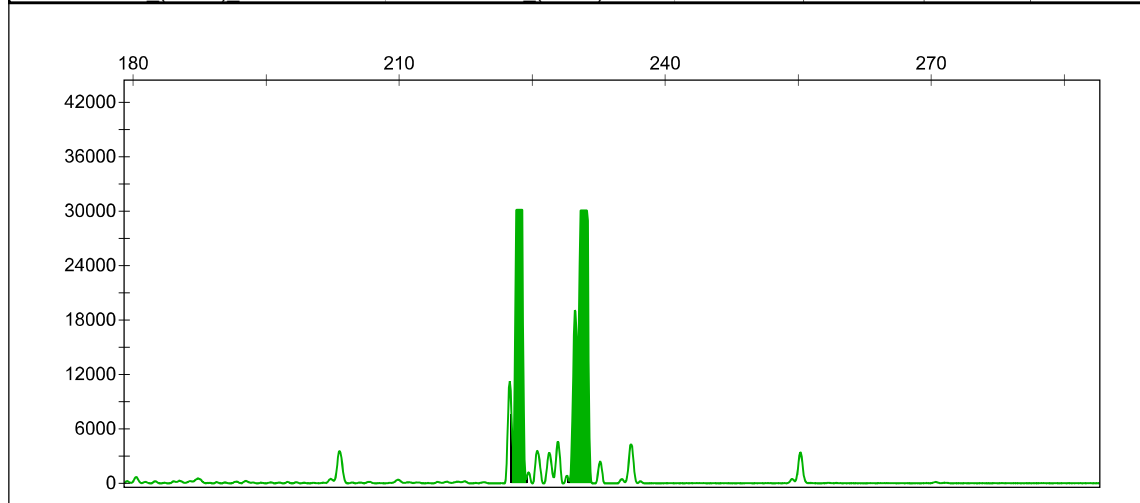
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731562_(14-26)_F12.fsa	71102731562_(14-26)	None		▲	■



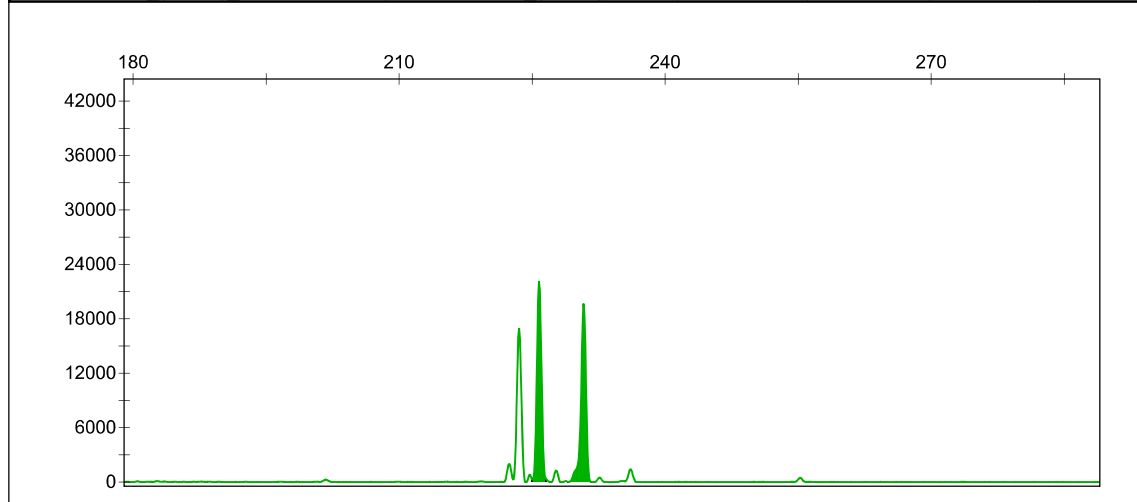
71102731563_(14-27)_G12.fsa	71102731563_(14-27)	None	✕	▲	■
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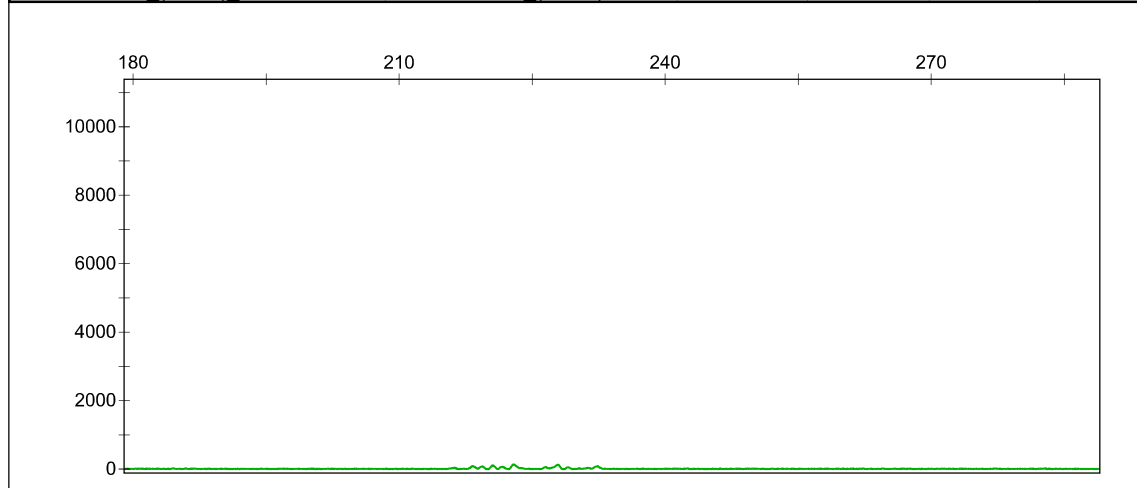
71102731564_(14-29)_H12.fsa	71102731564_(14-29)	None		▲	■
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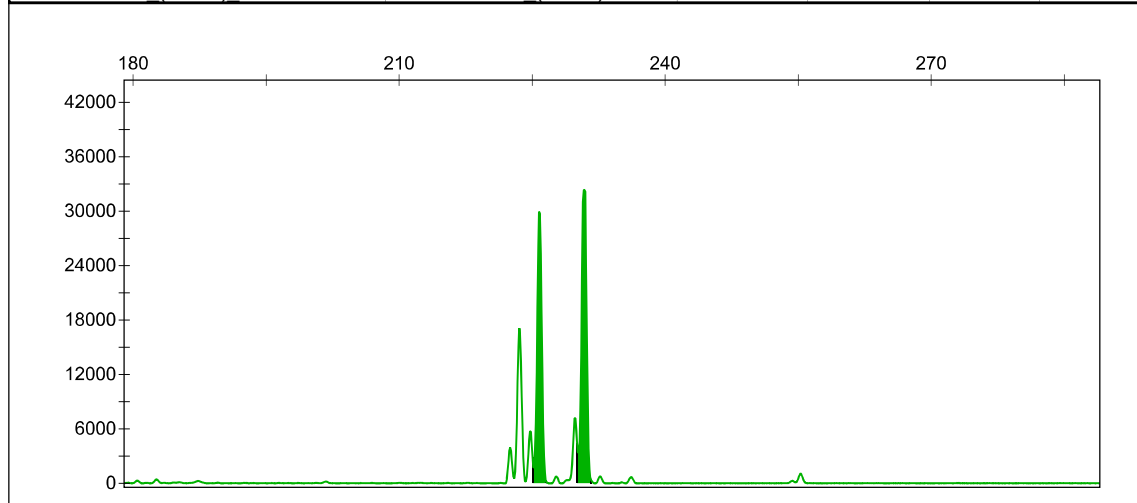
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731565_(14-30)_A01.fsa	71102731565_(14-30)	None		■	■



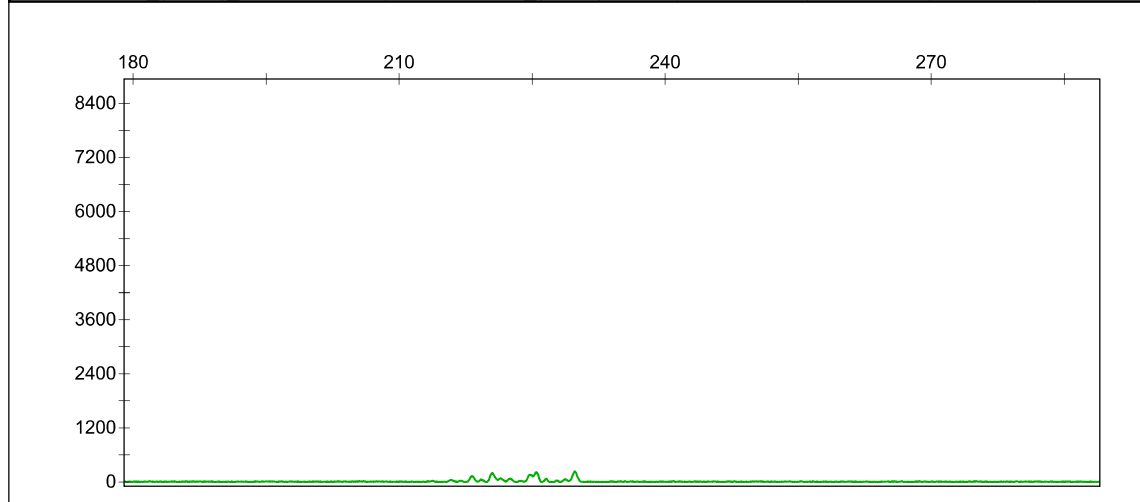
71102731566_(14-31)_B01.fsa	71102731566_(14-31)	None		■	■
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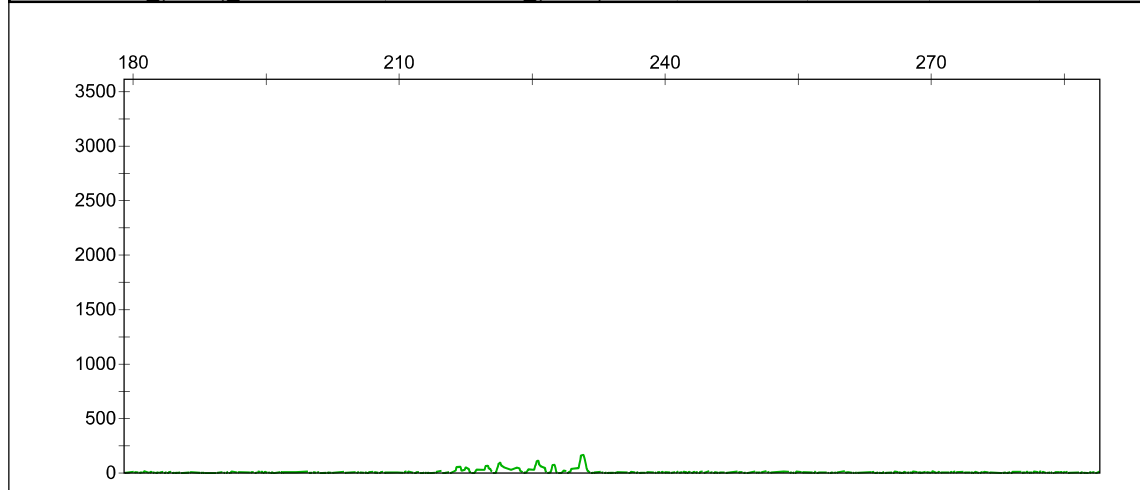
71102731567_(14-33)_C01.fsa	71102731567_(14-33)	None		▲	■
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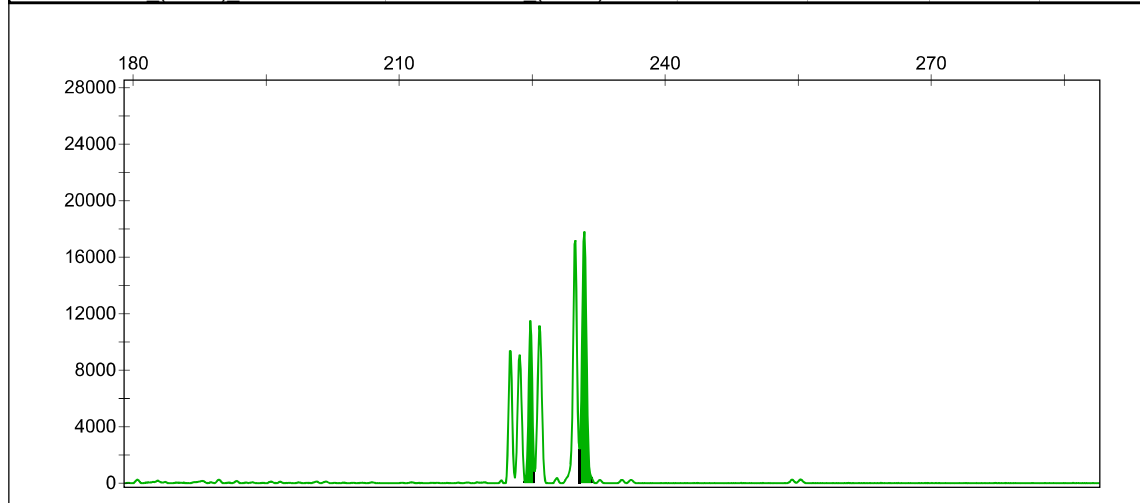
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731568_(14-34)_D01.fsa	71102731568_(14-34)	None			



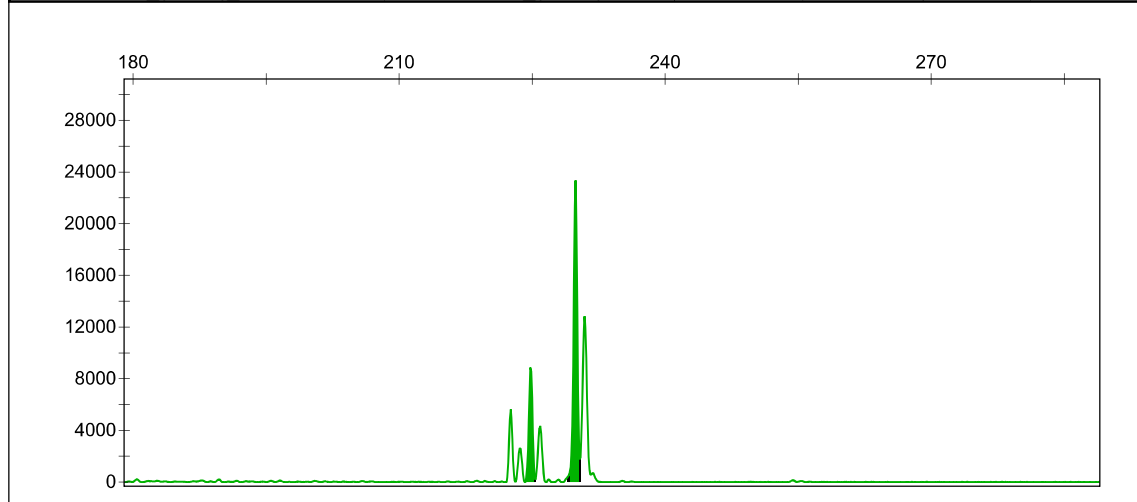
71102731569_(14-35)_E01.fsa	71102731569_(14-35)	None			
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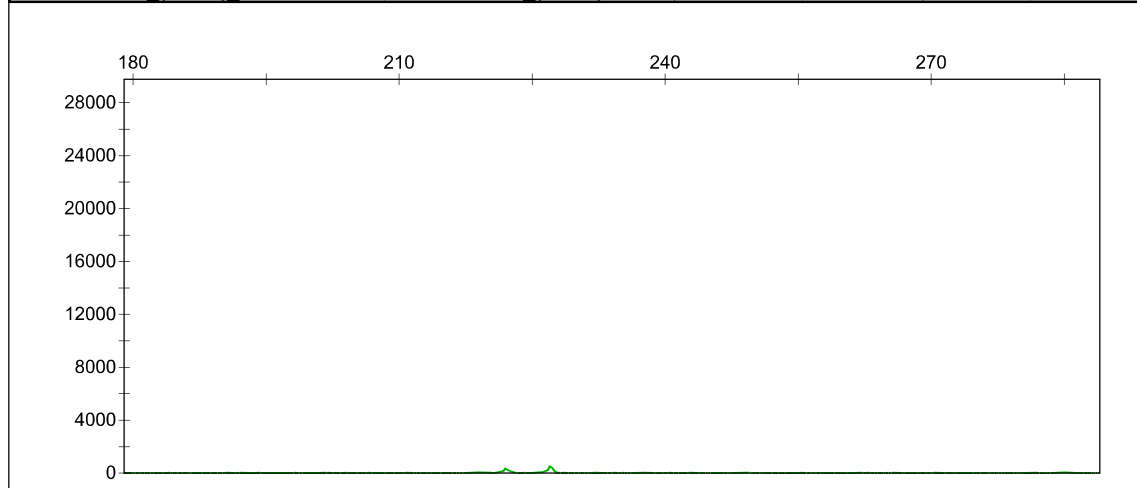
71102731570_(14-36)_F01.fsa	71102731570_(14-36)	None			
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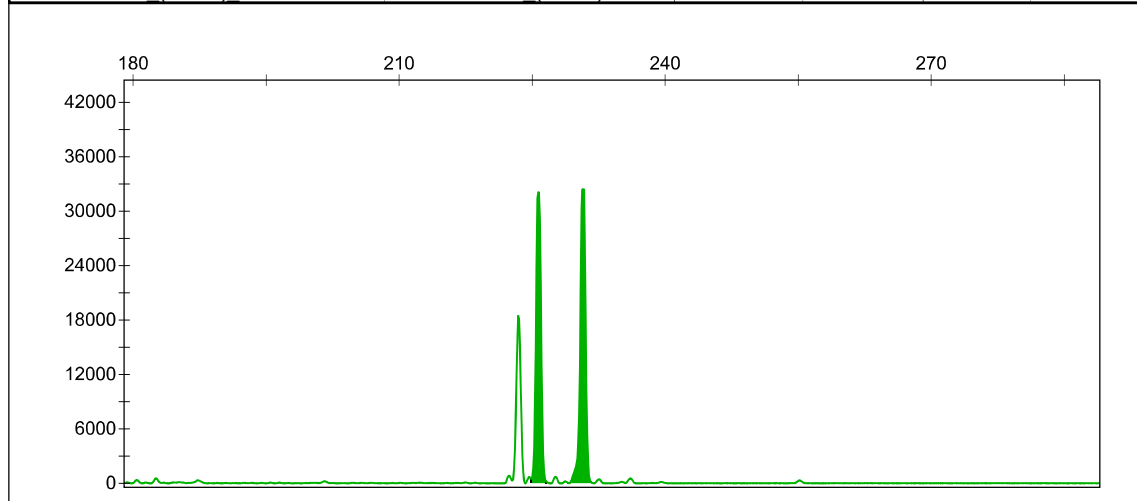
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731571_(14-37)_G01.fsa	71102731571_(14-37)	None		■	■



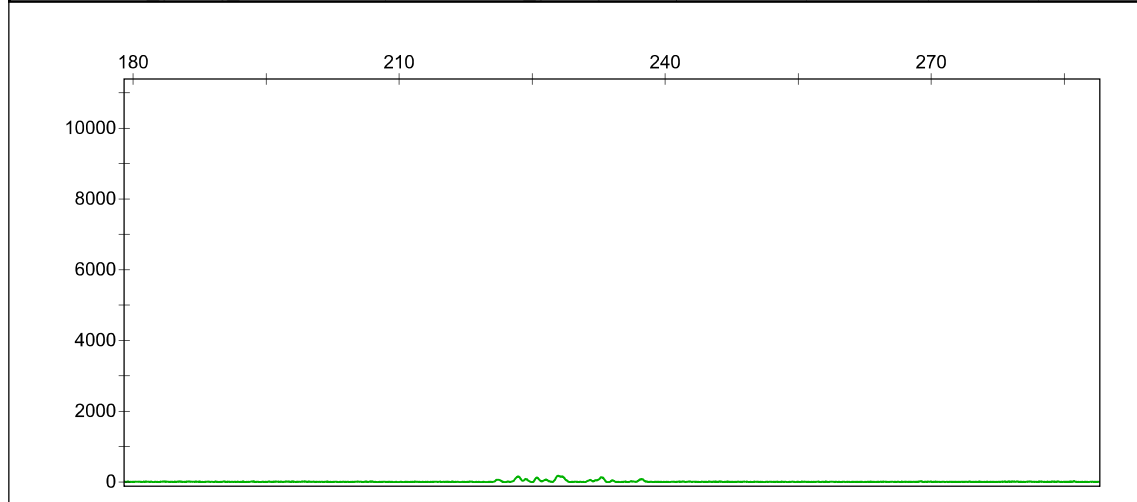
71102731572_(14-38)_H01.fsa	71102731572_(14-38)	None		■	■
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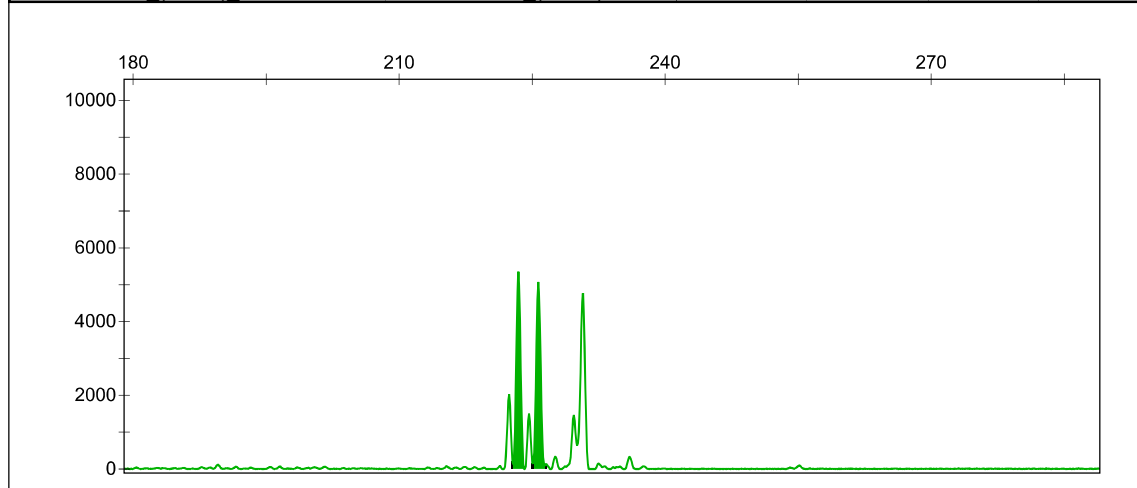
71102731573_(14-39)_A02.fsa	71102731573_(14-39)	None		▲	■
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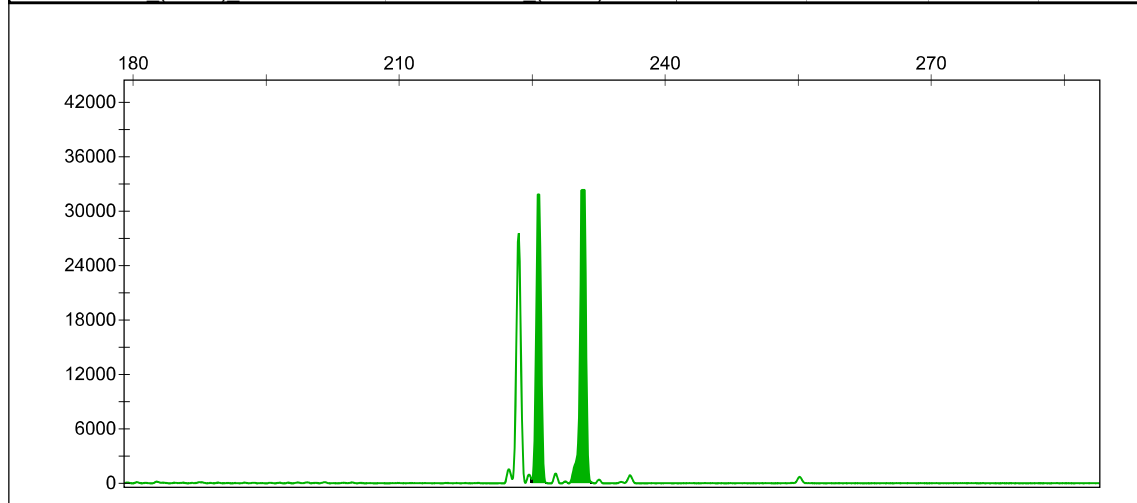
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731574_(14-40)_B02.fsa	71102731574_(14-40)	None		■	■



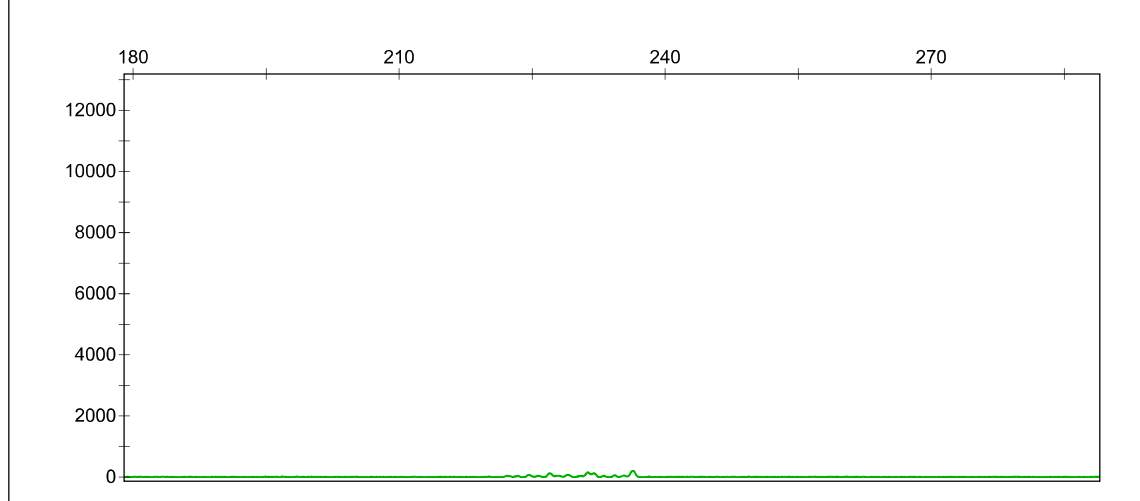
71102731575_(14-41)_C02.fsa	71102731575_(14-41)	None		■	■
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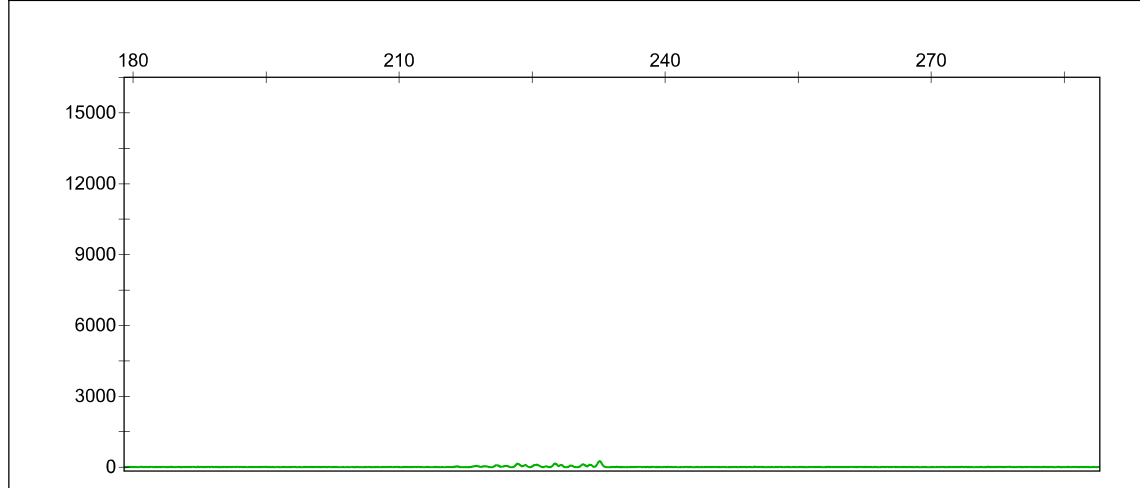
71102731576_(14-42)_D02.fsa	71102731576_(14-42)	None		▲	■
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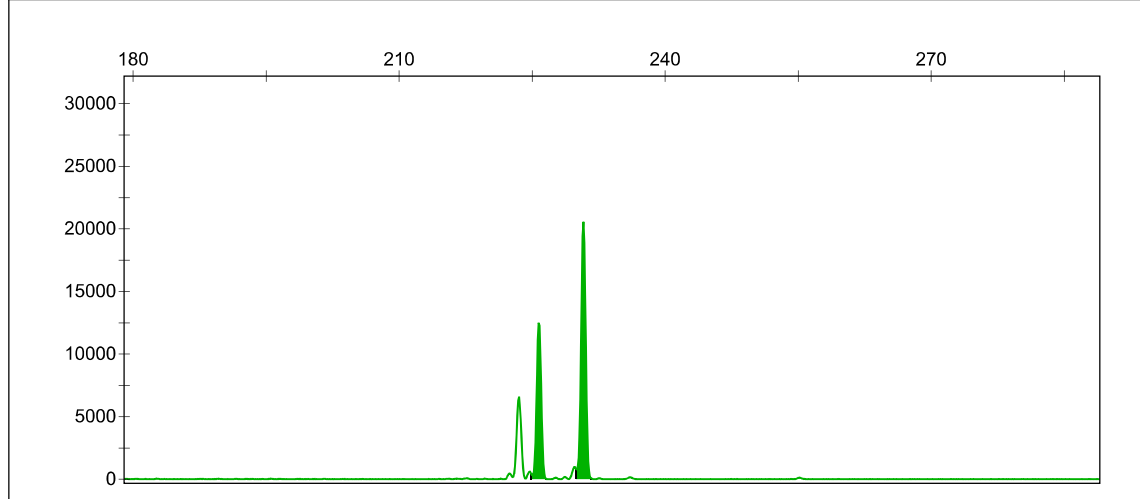
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731577_(14-43)_E02.fsa	71102731577_(14-43)	None		■	■



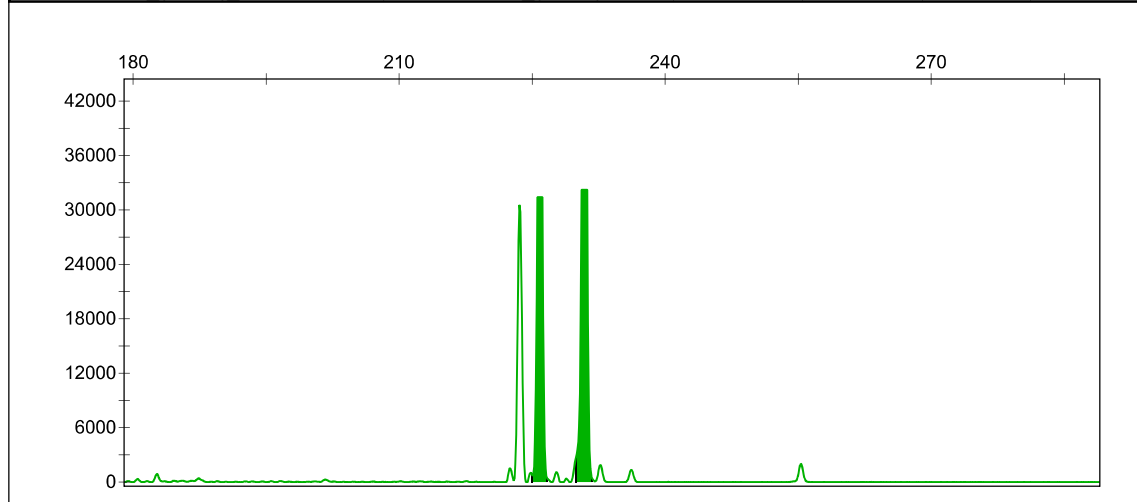
71102731578_(14-45)_F02.fsa	71102731578_(14-45)	None		■	■
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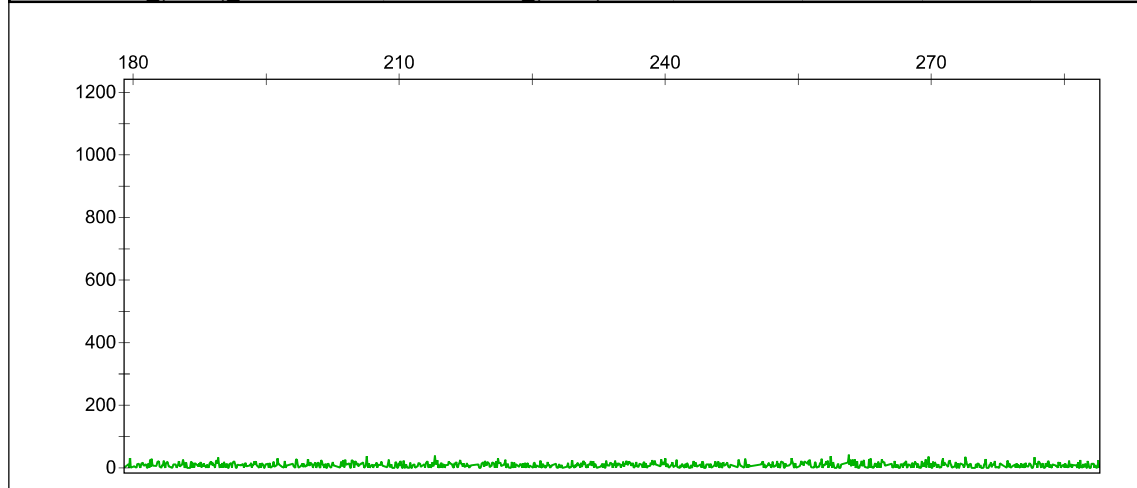
71102731579_(14-46)_G02.fsa	71102731579_(14-46)	None		■	■
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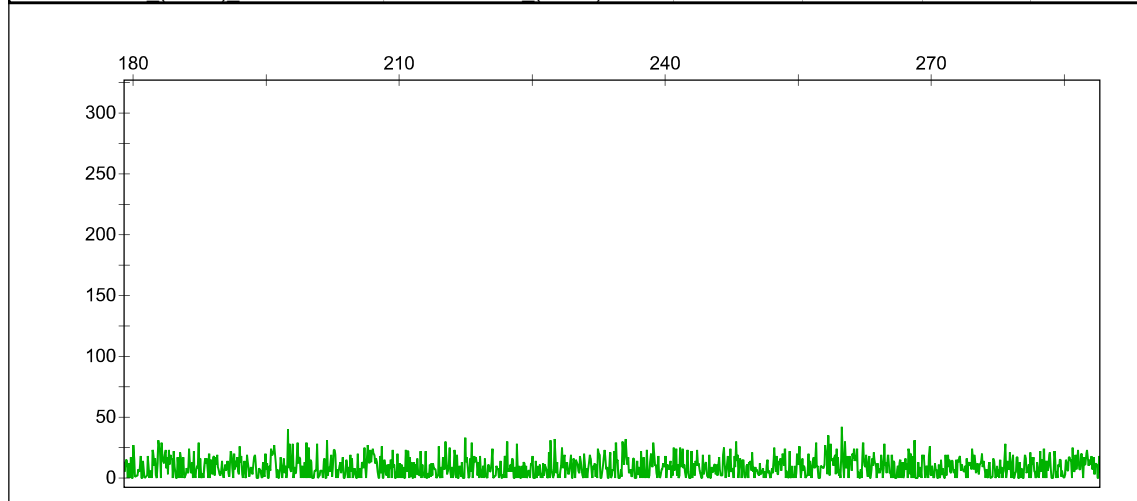
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731580_(14-47)_H02.fsa	71102731580_(14-47)	None		▲	■



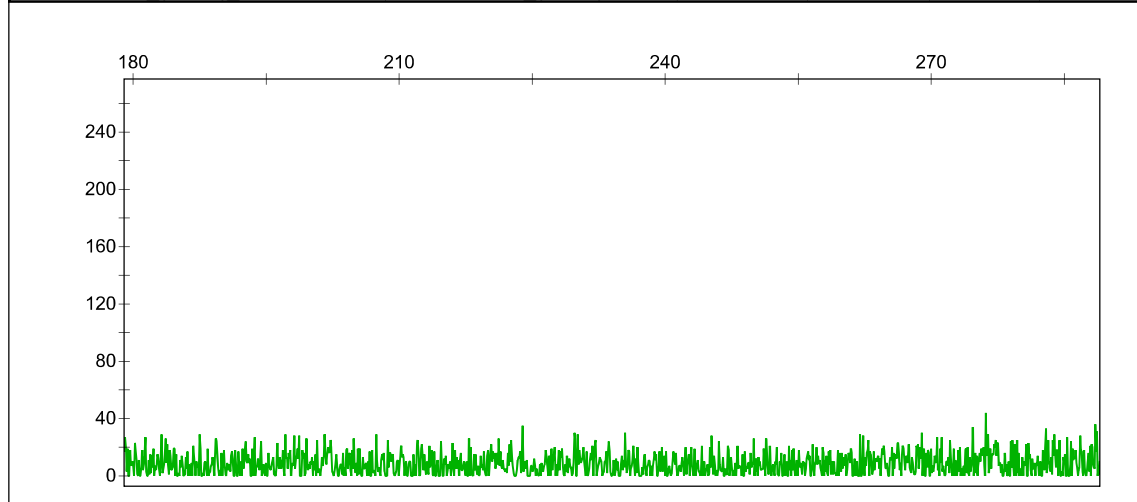
71102731581_(14-48)_A12.fsa	71102731581_(14-48)	None		■	■
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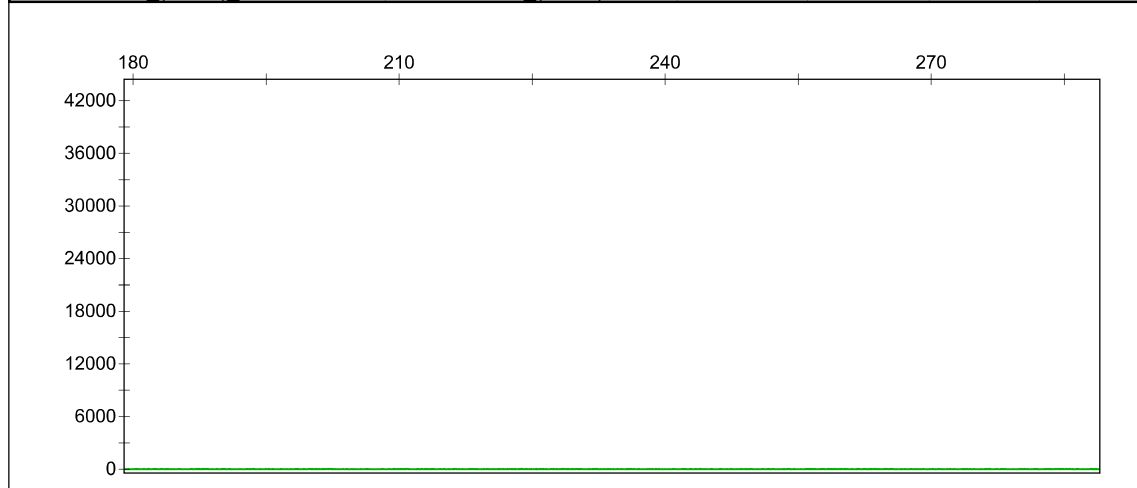
71102731582_(14-49)_B12.fsa	71102731582_(14-49)	None		■	■
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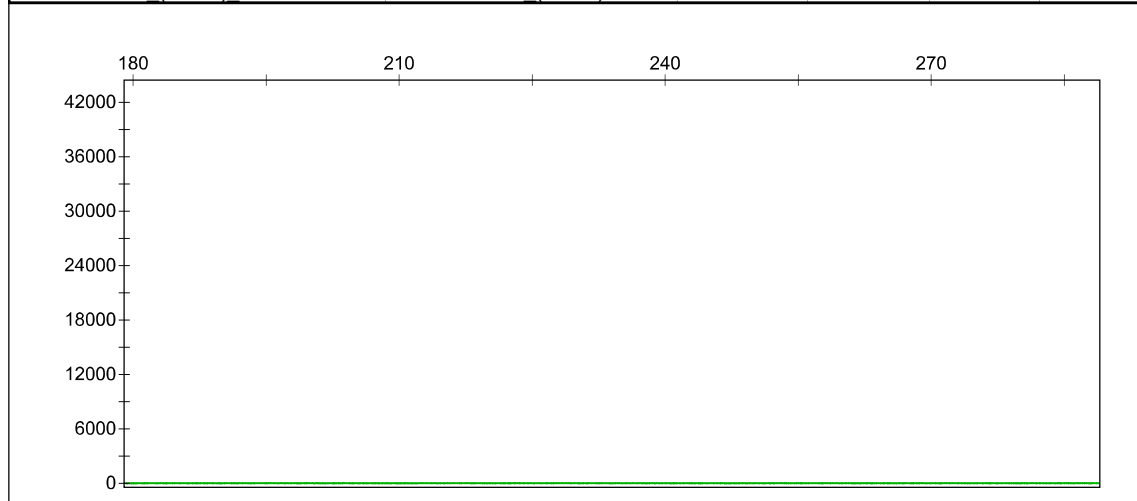
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731583_(14-50)_C12.fsa	71102731583_(14-50)	None			



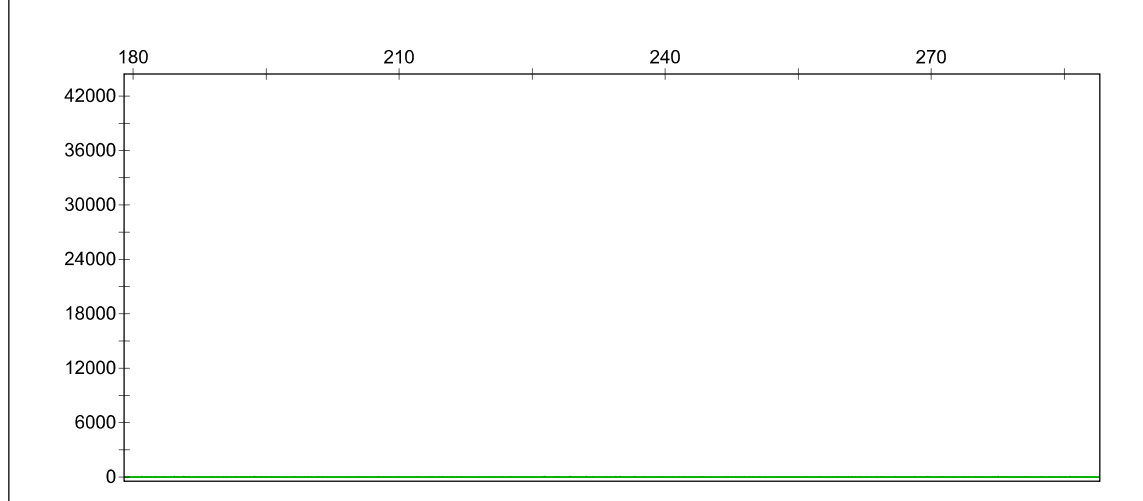
71102731584_(14-51)_D12.fsa	71102731584_(14-51)	None			
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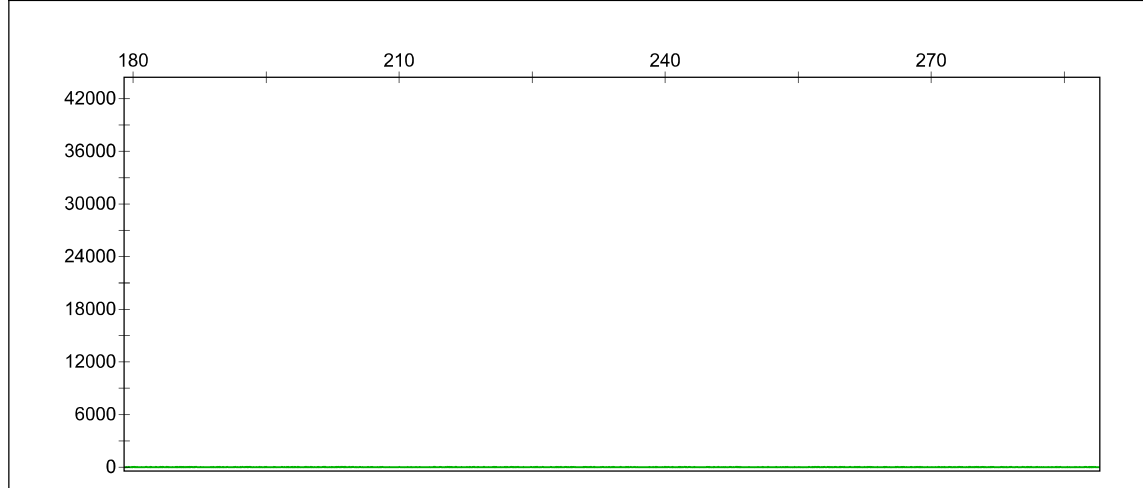
71102731585_(14-53)_E12.fsa	71102731585_(14-53)	None			
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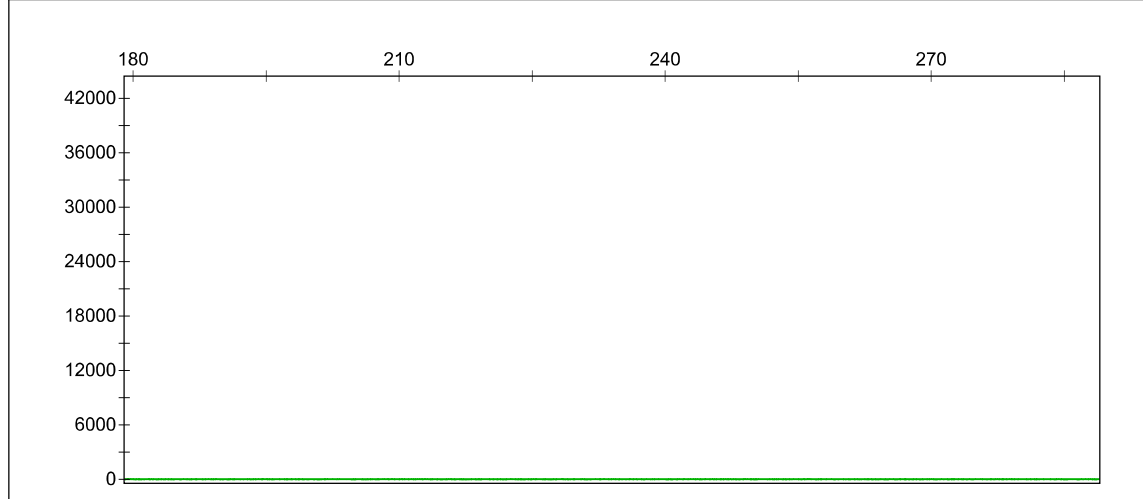
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731586_(14-54)_F12.fsa	71102731586_(14-54)	None			



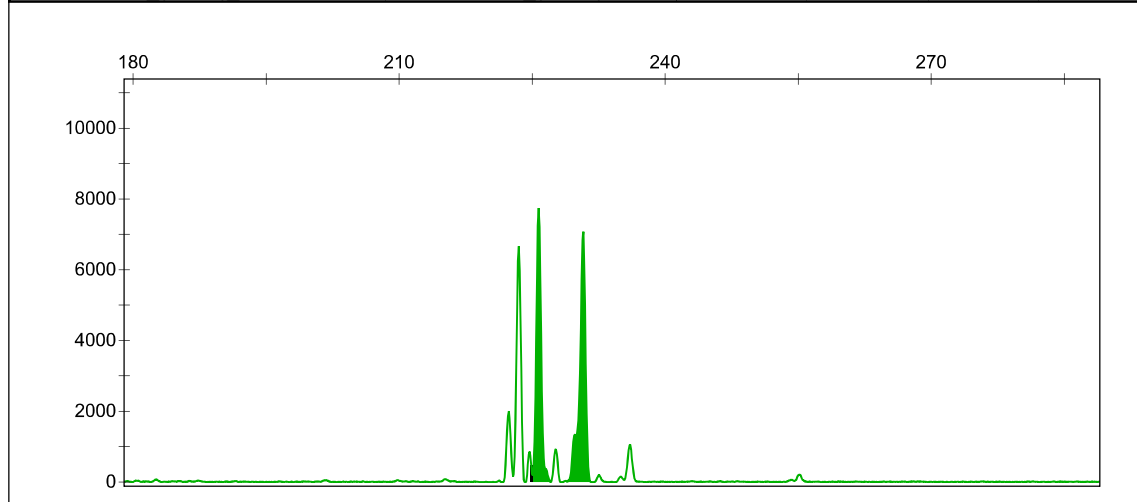
71102731587_(14-55)_G12.fsa	71102731587_(14-55)	None			
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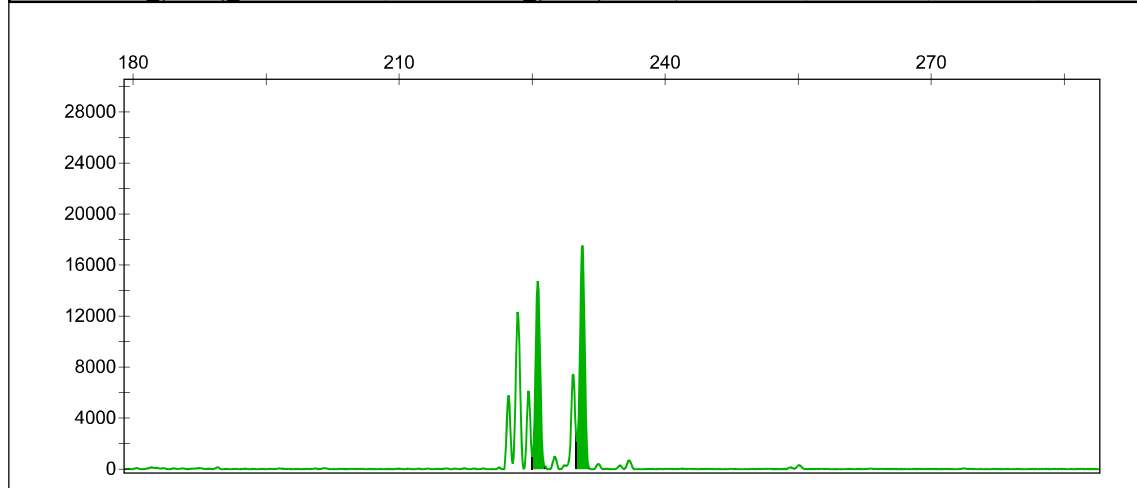
71102731588_(14-56)_H12.fsa	71102731588_(14-56)	None			
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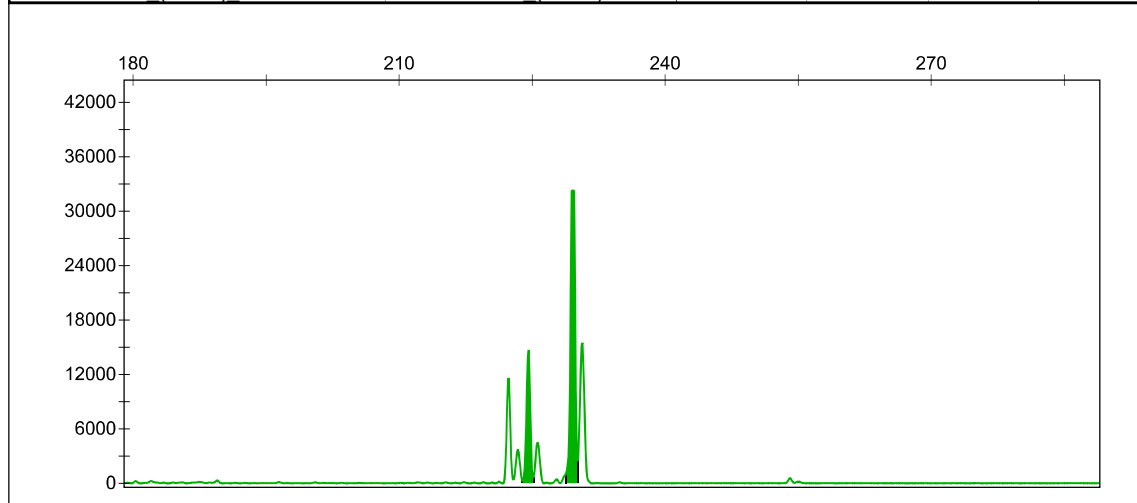
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731589_(14-57)_A03.fsa	71102731589_(14-57)	None		■	■



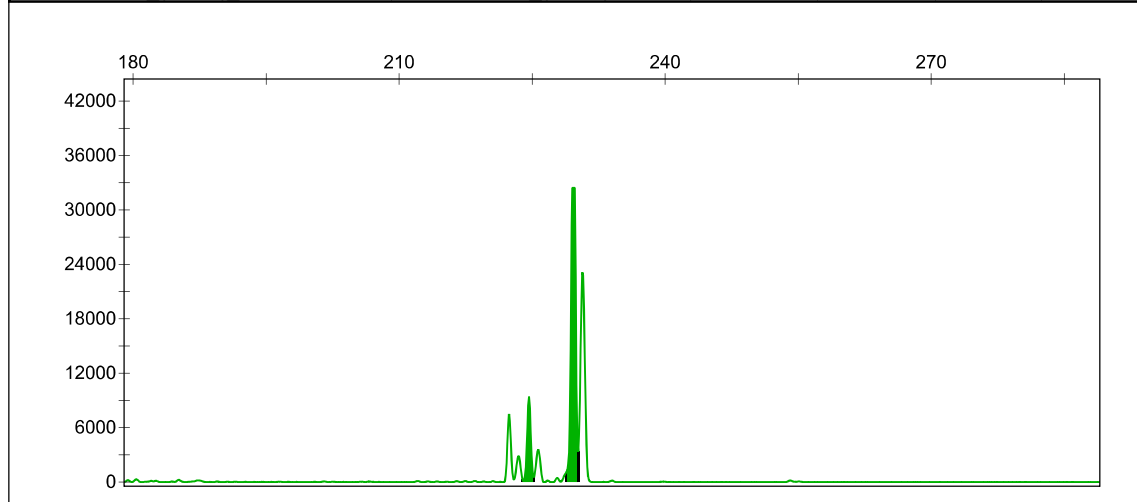
71102731590_(14-58)_B03.fsa	71102731590_(14-58)	None		■	■
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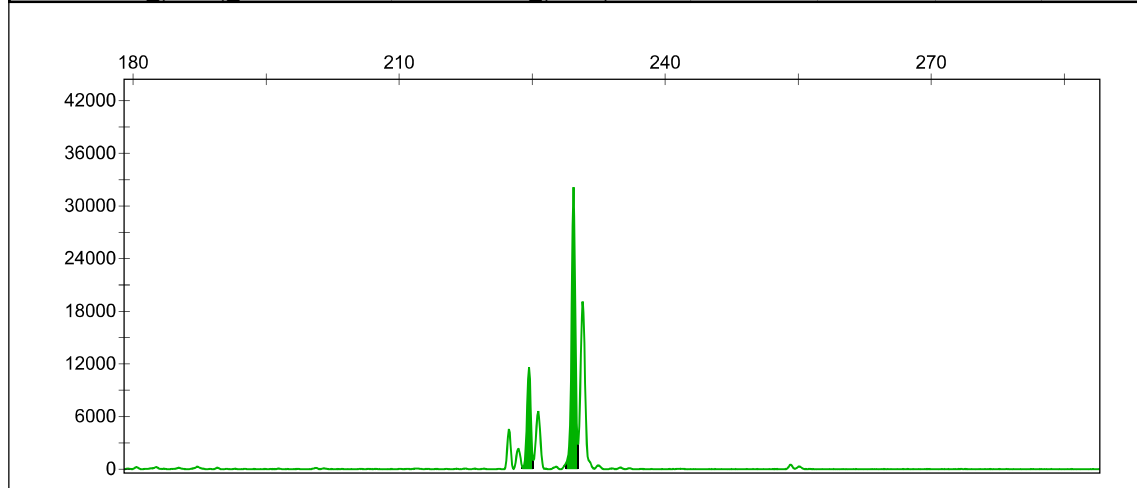
71102731591_(14-59)_C03.fsa	71102731591_(14-59)	None		▲	■
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Sample File	Sample Name	Panel	SQO	OS	SQ
71102731592_ (14-60)_D03.fsa	71102731592_ (14-60)	None		▲	■



71102731593_ (14-61)_E03.fsa	71102731593_ (14-61)	None		■	■
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71102731594_ (14-100)_F03.fsa	71102731594_ (14-100)	None		■	■
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