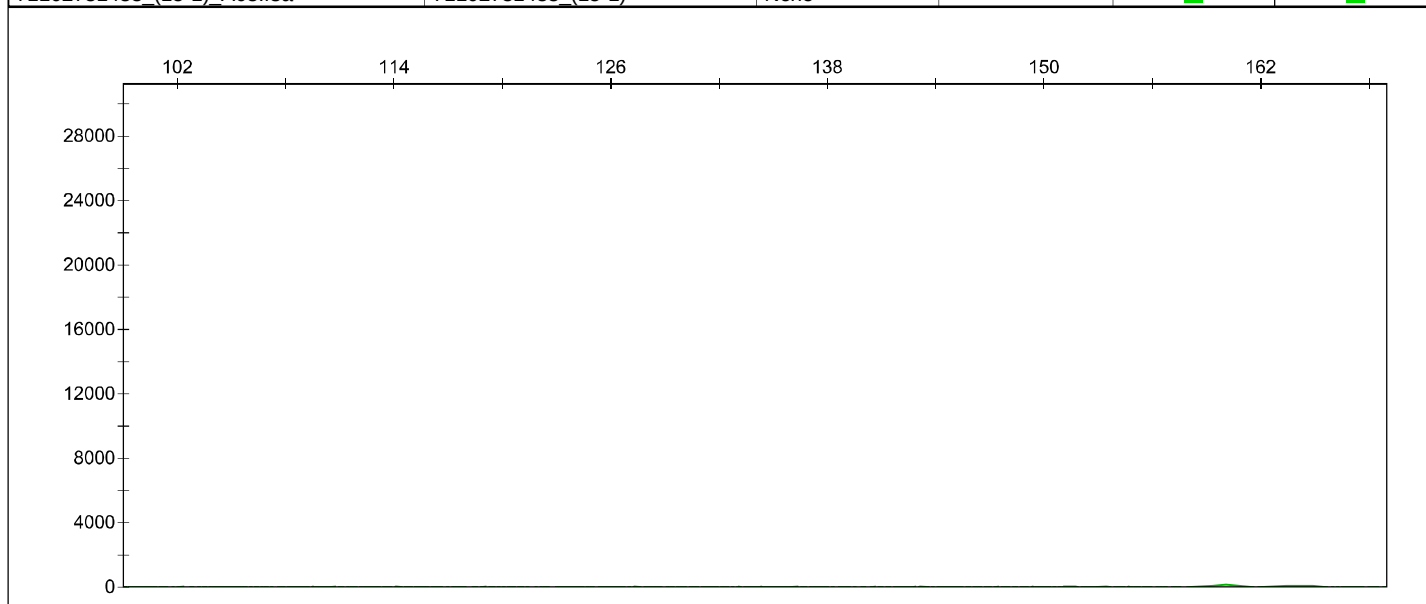
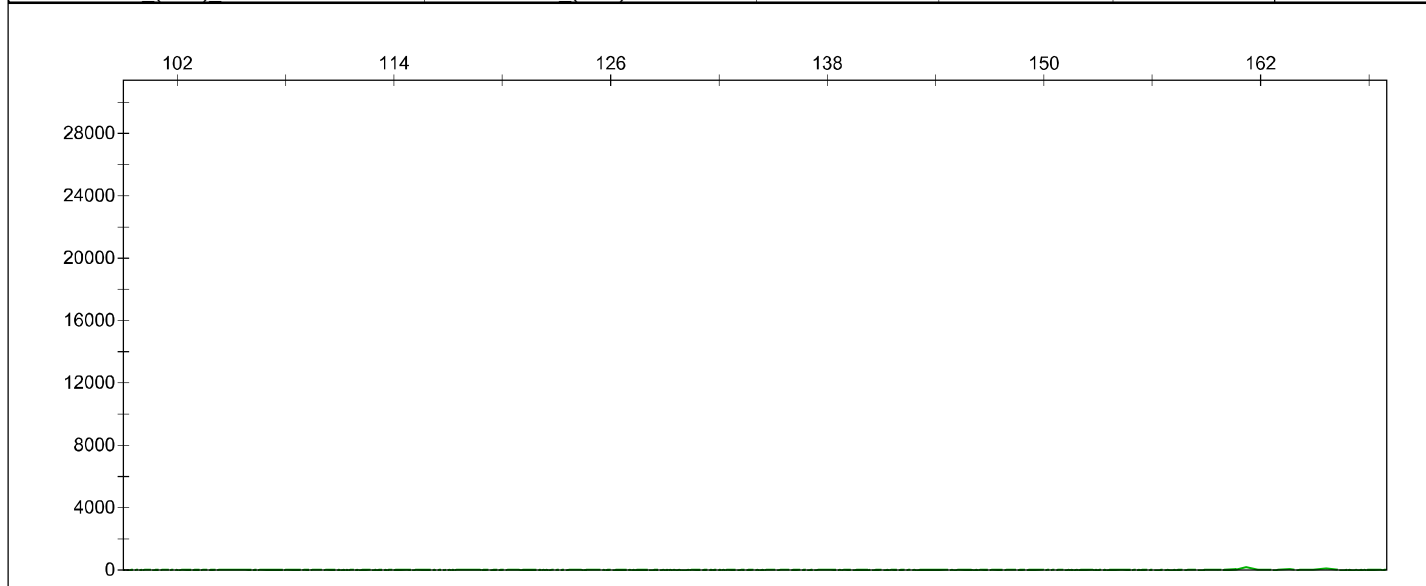


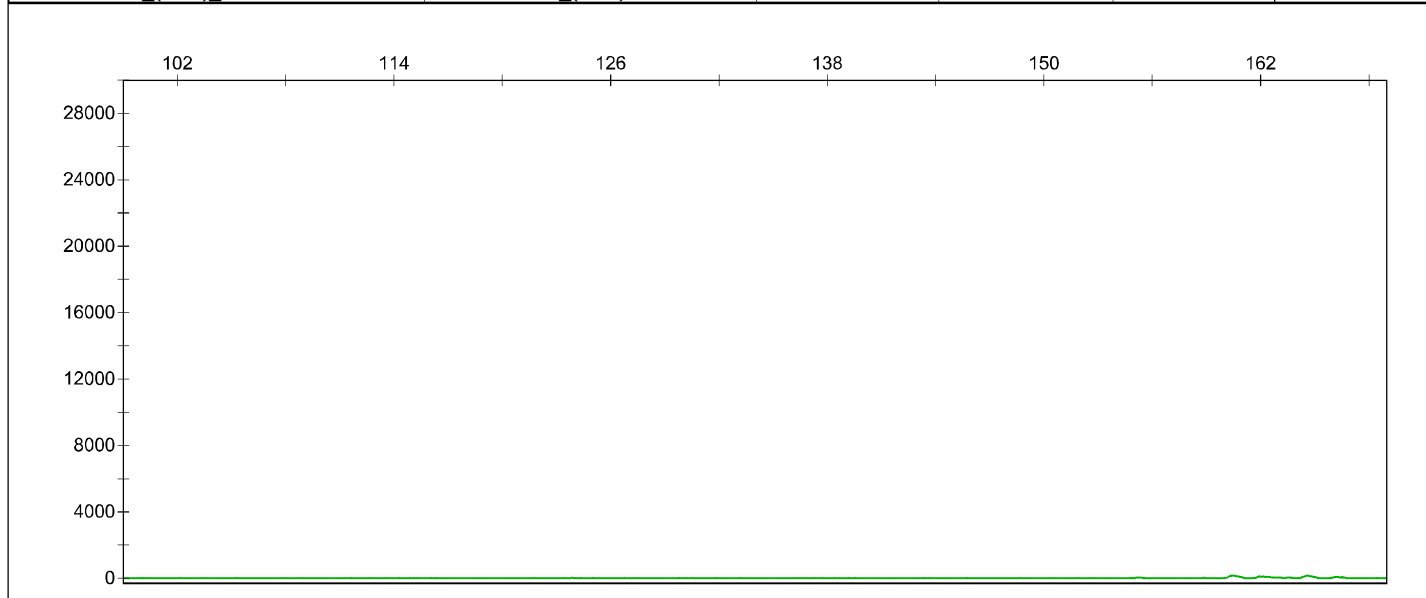
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731433_(15-1)_A08.fsa	71102731433_(15-1)	None			



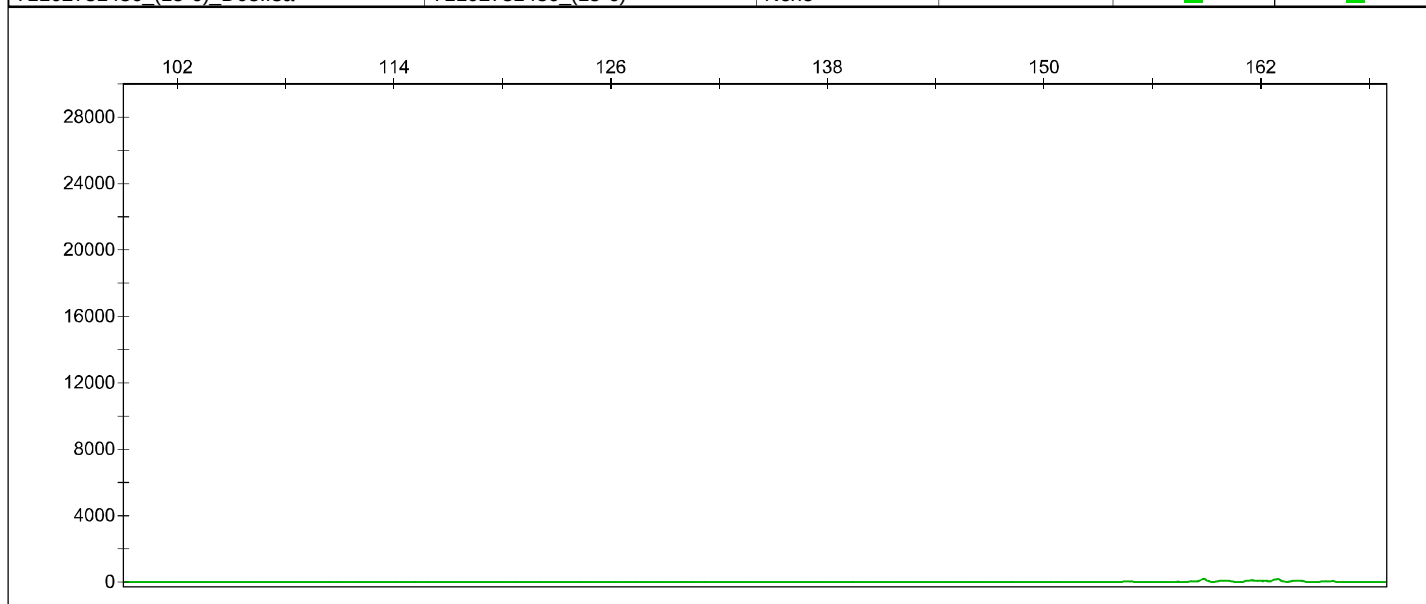
71102731434_(15-3)_B08.fsa	71102731434_(15-3)	None			
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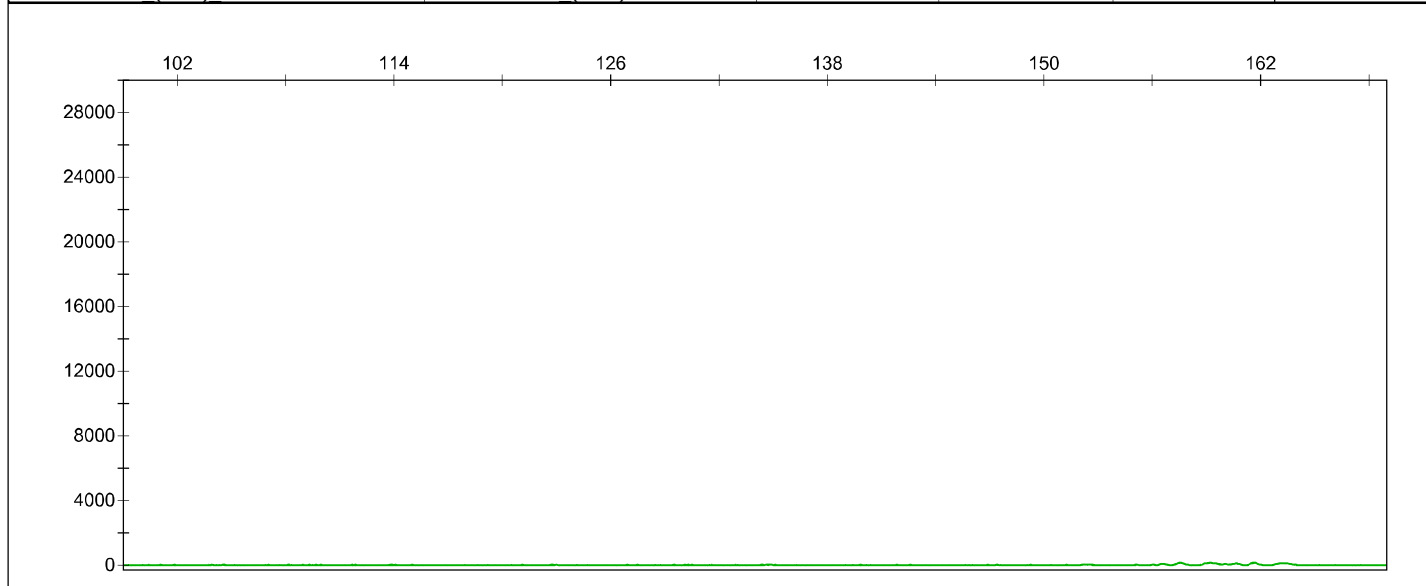
71102731435_(15-5)_C08.fsa	71102731435_(15-5)	None			
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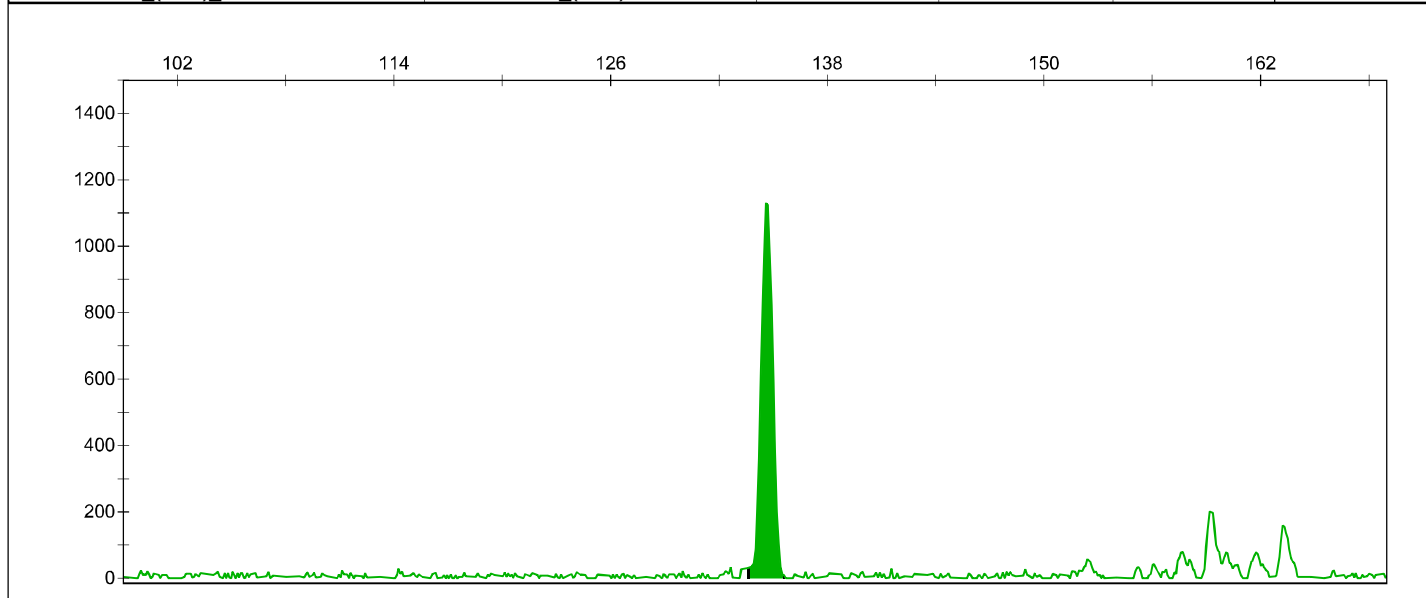
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731436_(15-6)_D08.fsa	71102731436_(15-6)	None			



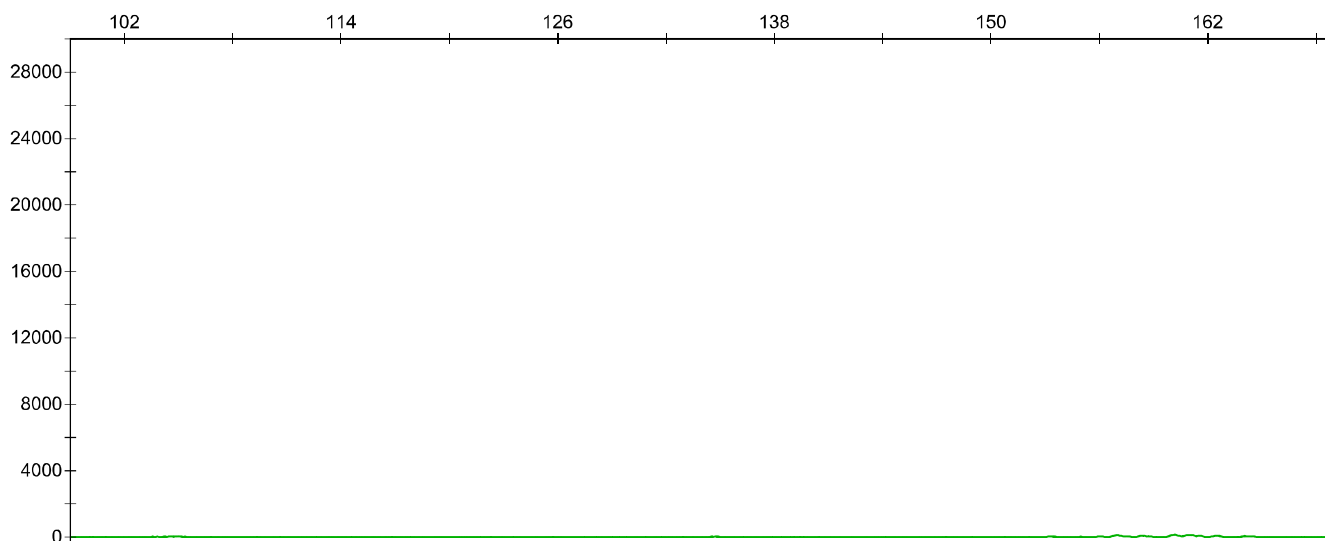
71102731437_(15-7)_E08.fsa	71102731437_(15-7)	None			
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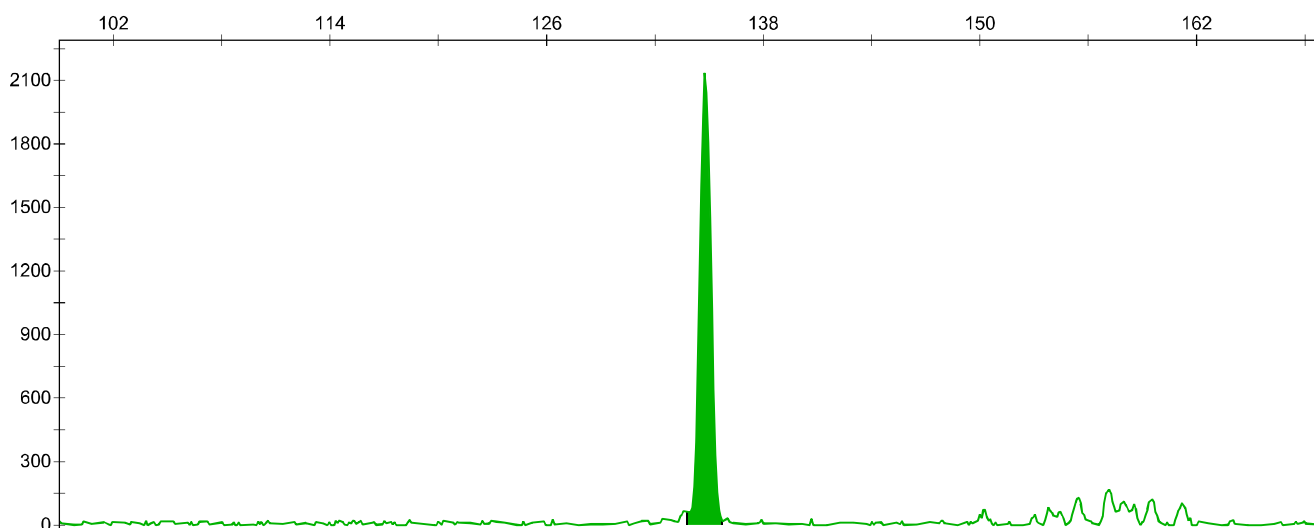
71102731438_(15-9)_F08.fsa	71102731438_(15-9)	None			
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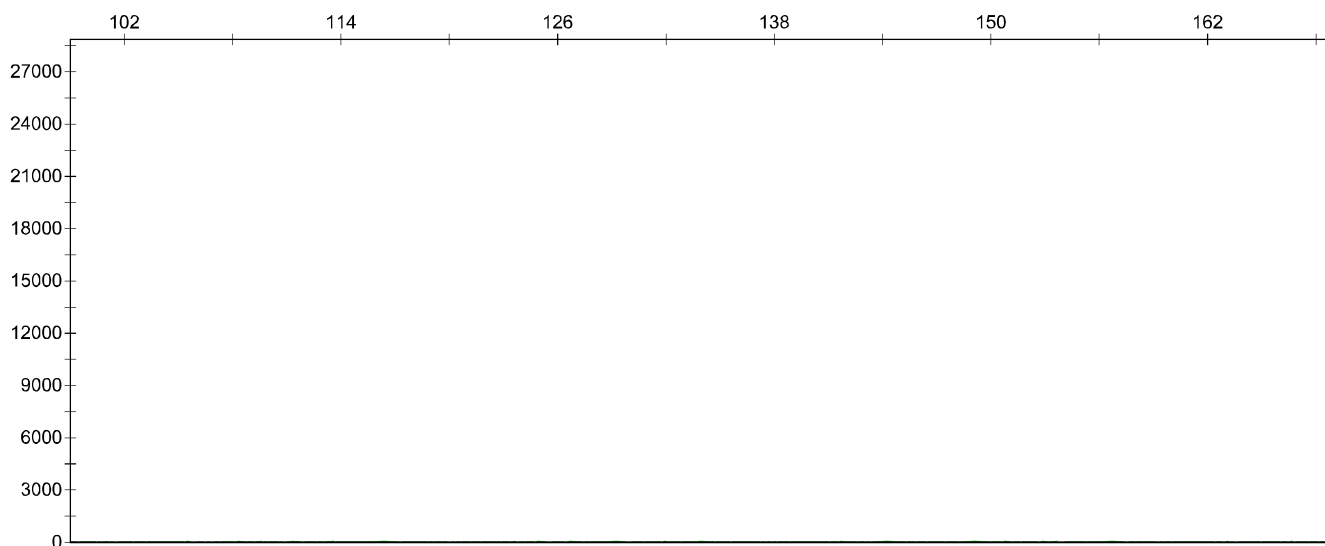
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731439 (15-10) G08.fsa	71102731439 (15-10)	None			



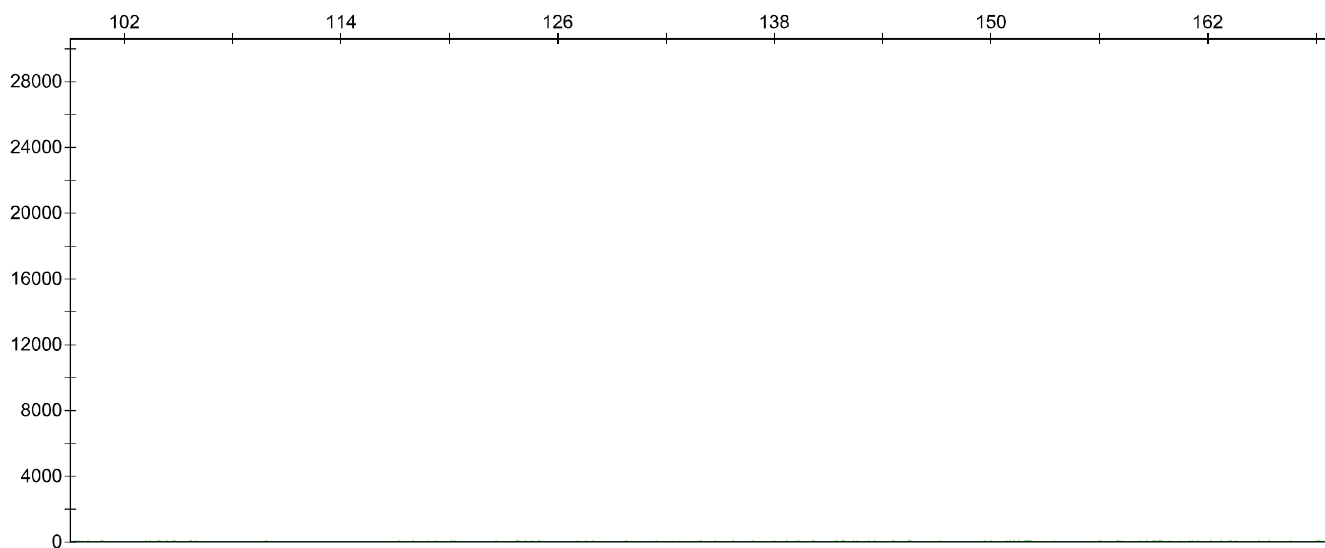
71102731440 (15-12) H08.fsa	71102731440 (15-12)	None			
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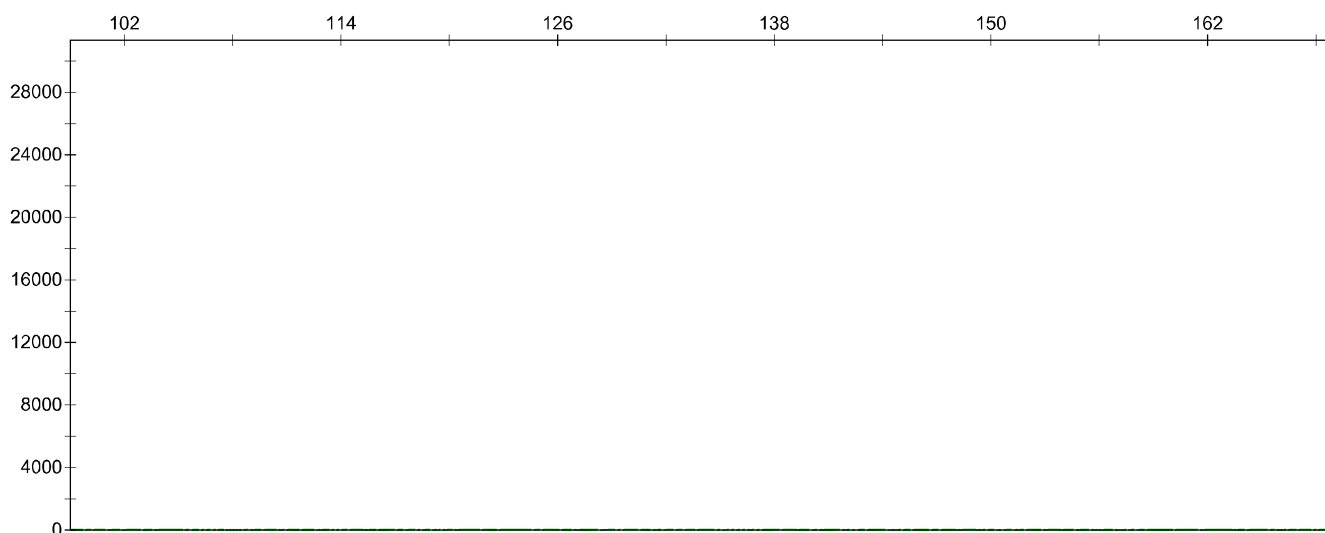
71102731441 (15-13) A09.fsa	71102731441 (15-13)	None			
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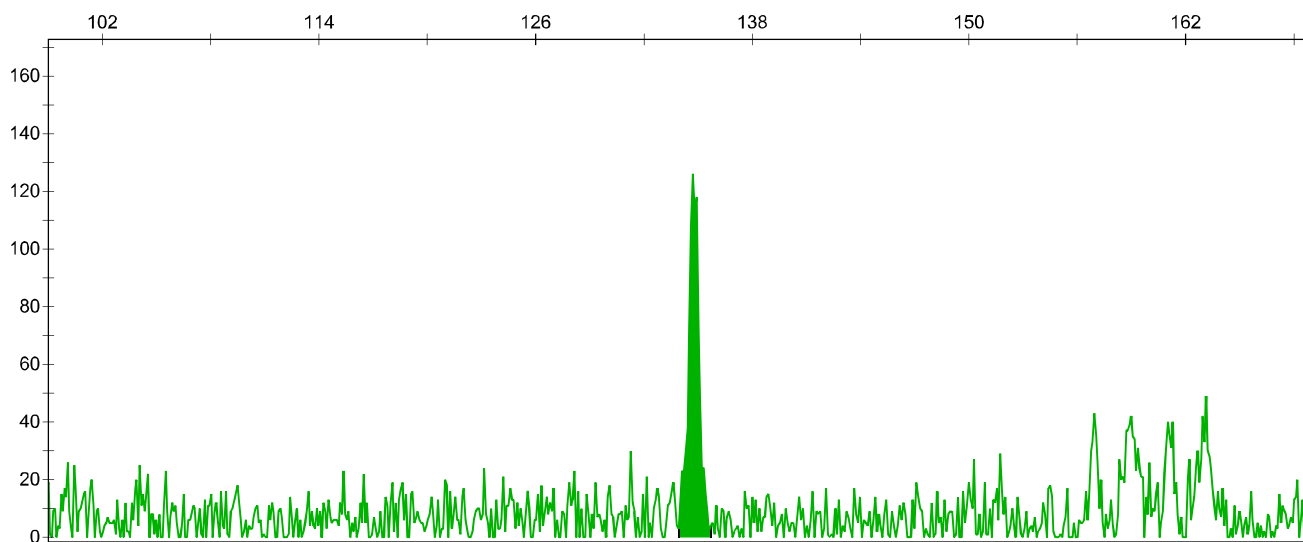
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731442_(15-14)_B09.fsa	71102731442_(15-14)	None		■	■



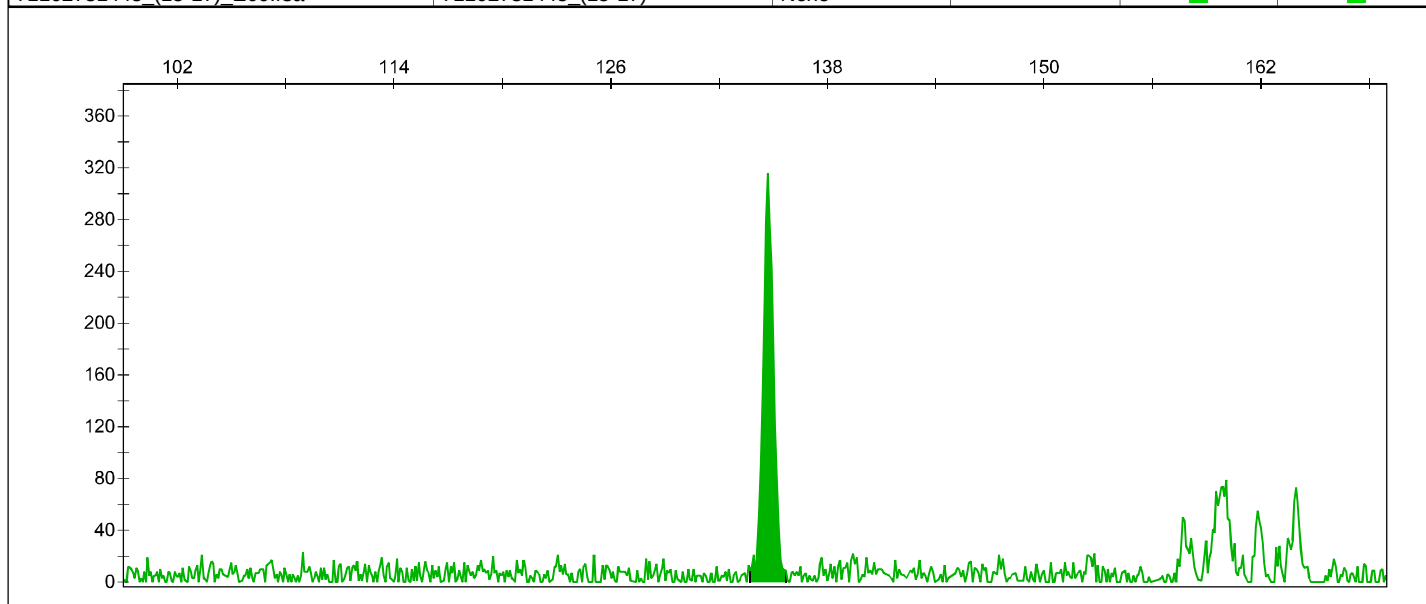
71102731443_(15-15)_C09.fsa	71102731443_(15-15)	None		■	■
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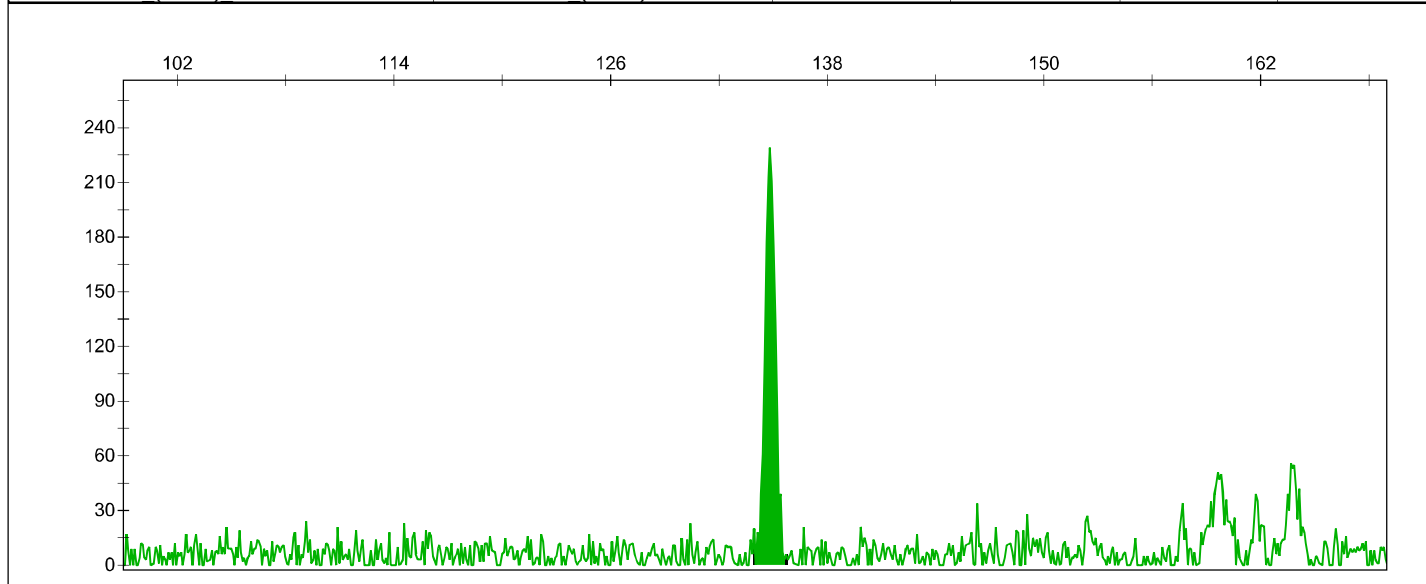
71102731444_(15-16)_D09.fsa	71102731444_(15-16)	None		■	■
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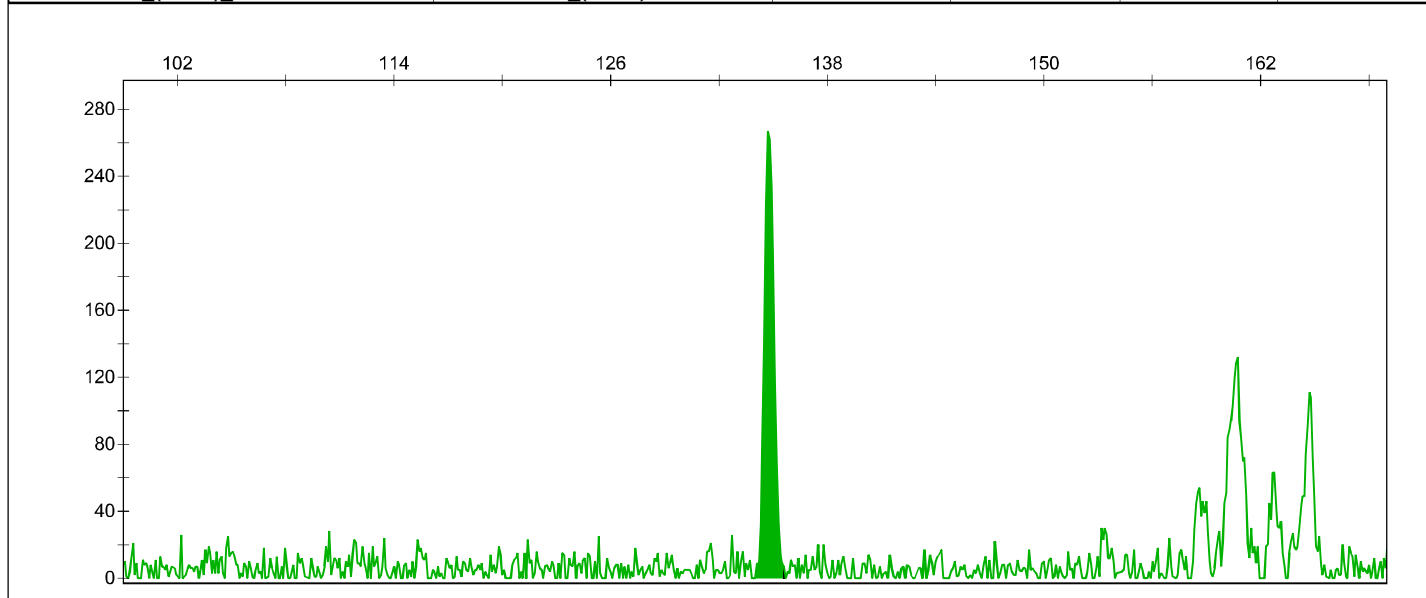
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731445_(15-17)_E09.fsa	71102731445_(15-17)	None		■	■



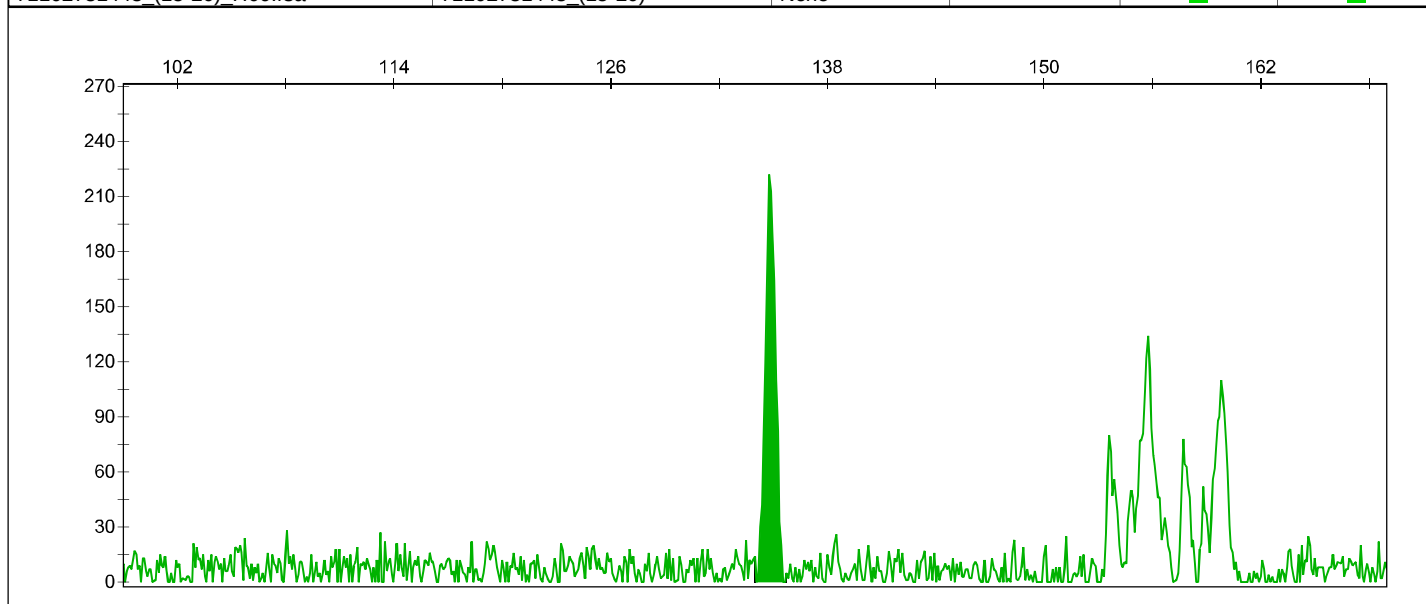
71102731446_(15-18)_F09.fsa	71102731446_(15-18)	None		■	■
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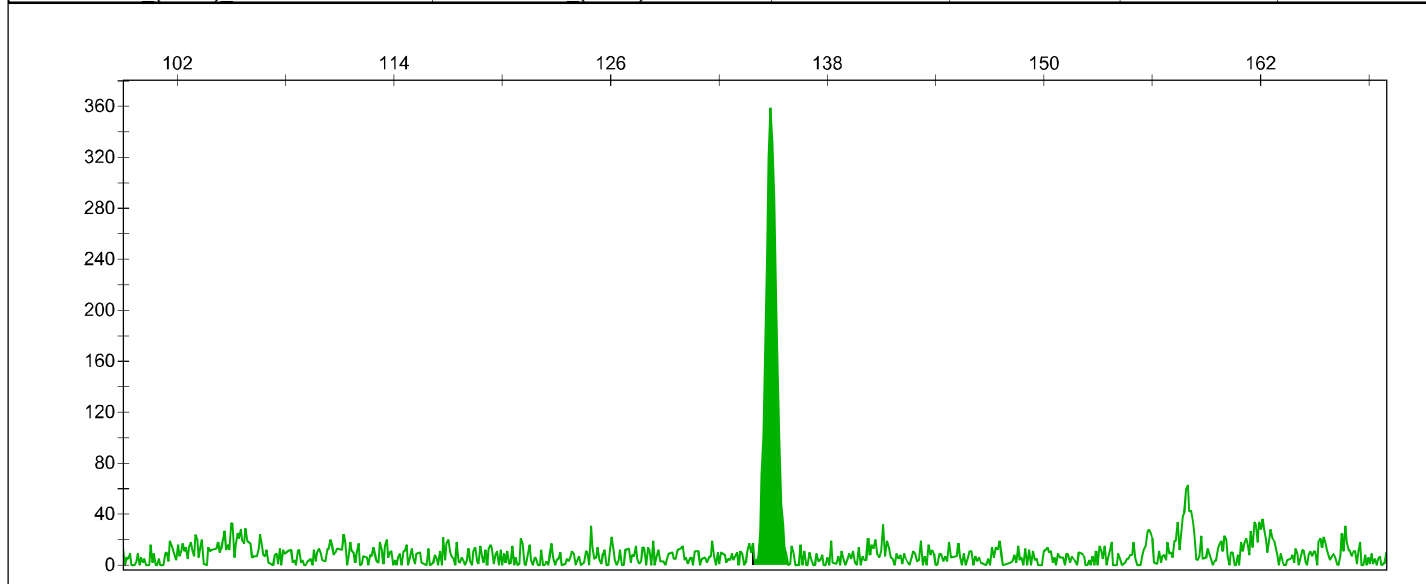
71102731447_(15-19)_G09.fsa	71102731447_(15-19)	None		■	■
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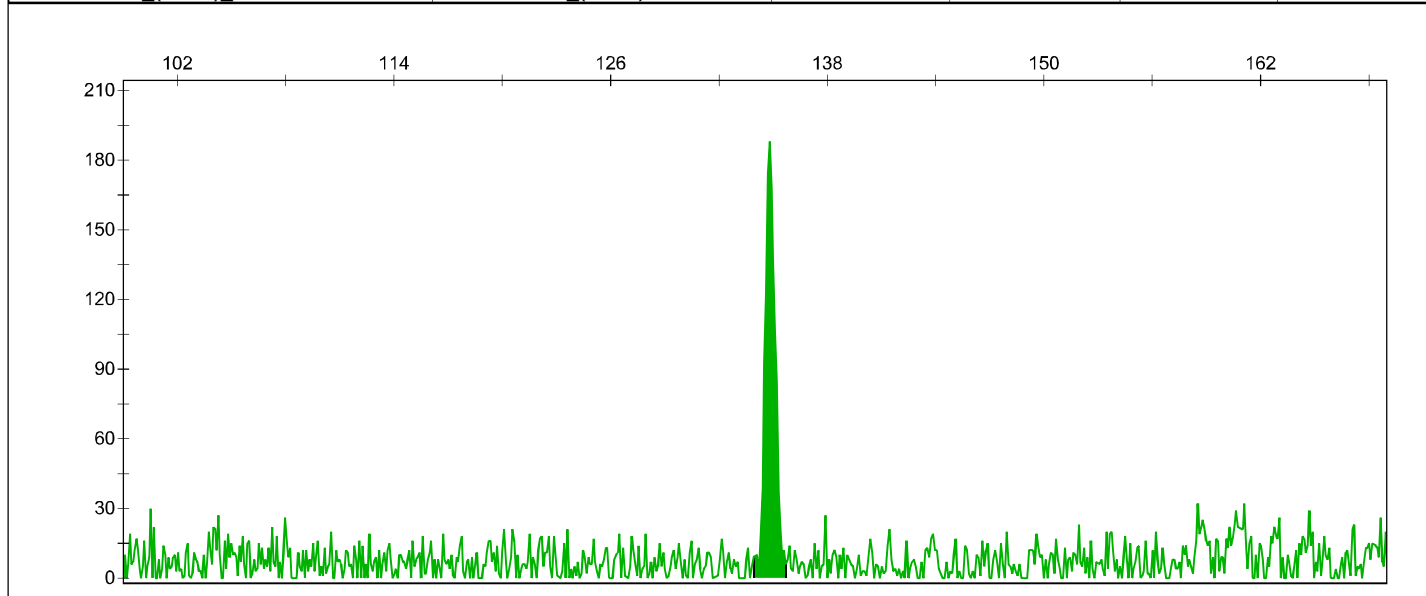
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731448_(15-20)_H09.fsa	71102731448_(15-20)	None		■	■



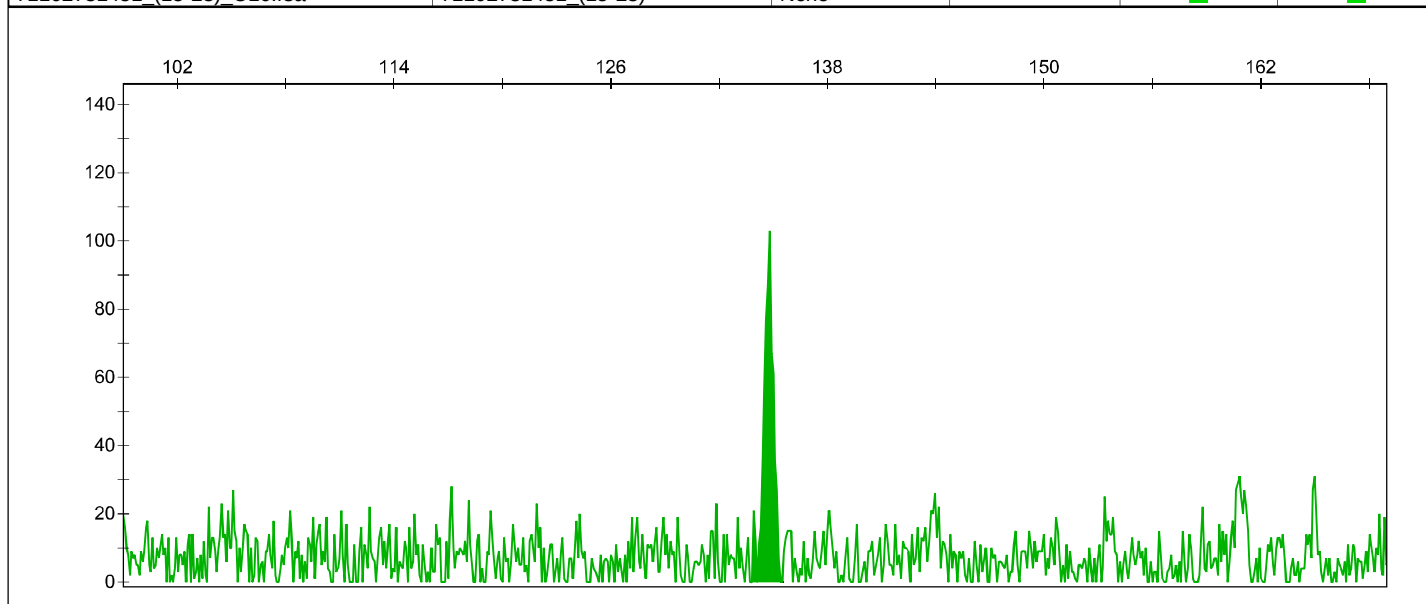
71102731449_(15-21)_A10.fsa	71102731449_(15-21)	None		■	■
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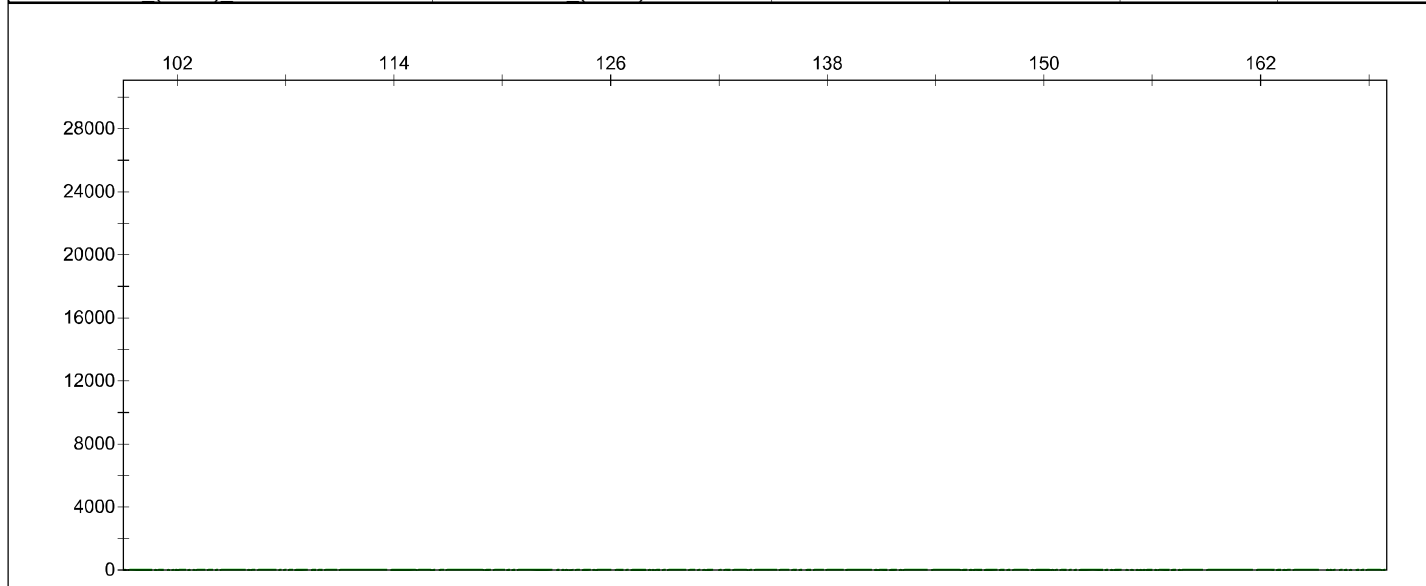
71102731450_(15-22)_B10.fsa	71102731450_(15-22)	None		■	■
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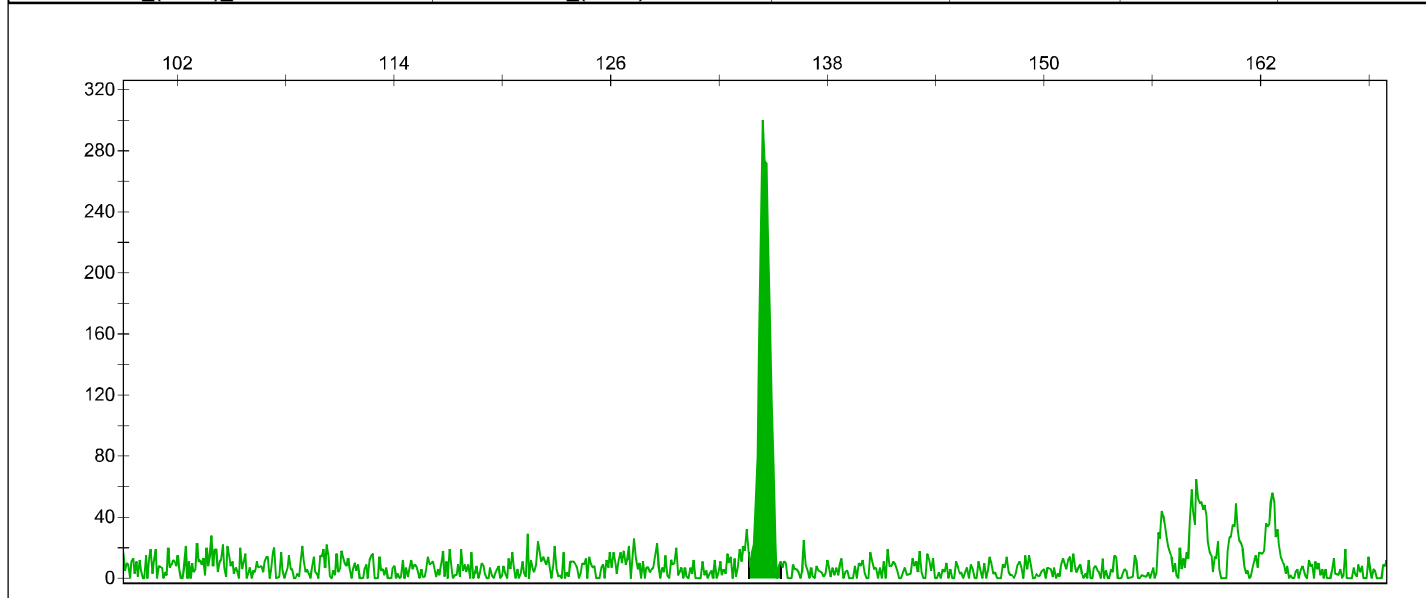
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731451_ (15-23)_ C10.fsa	71102731451_ (15-23)	None		■	■



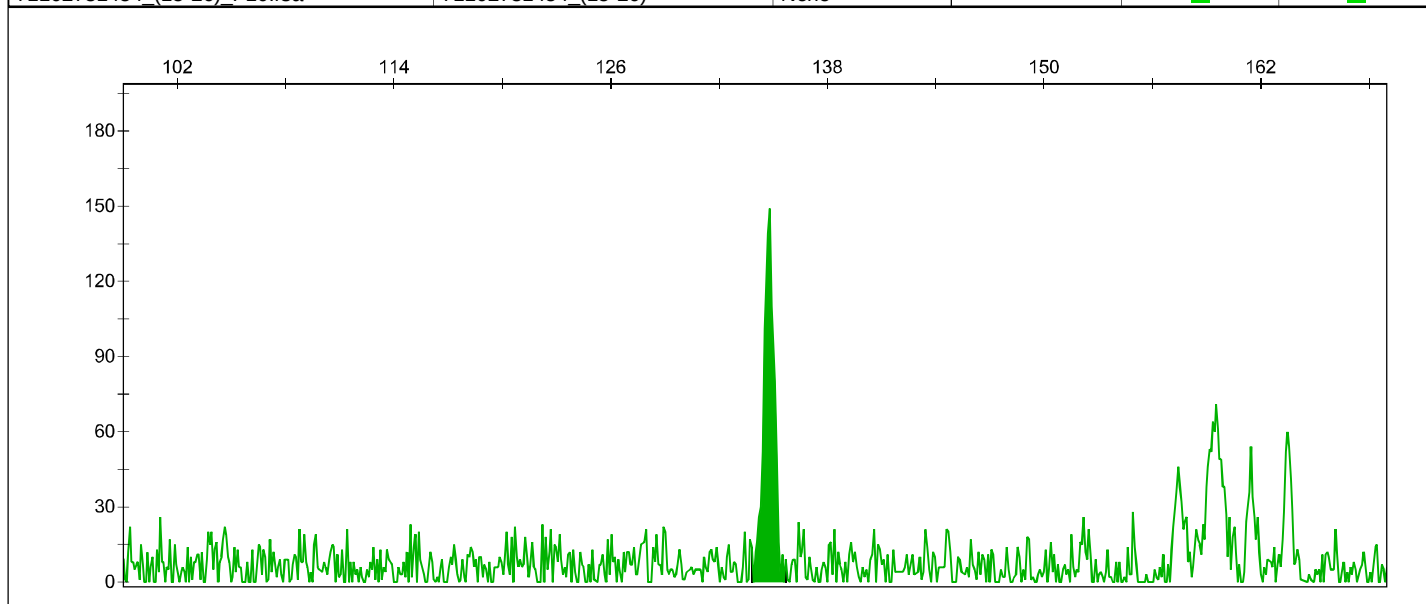
71102731452_ (15-24)_ D10.fsa	71102731452_ (15-24)	None		■	■
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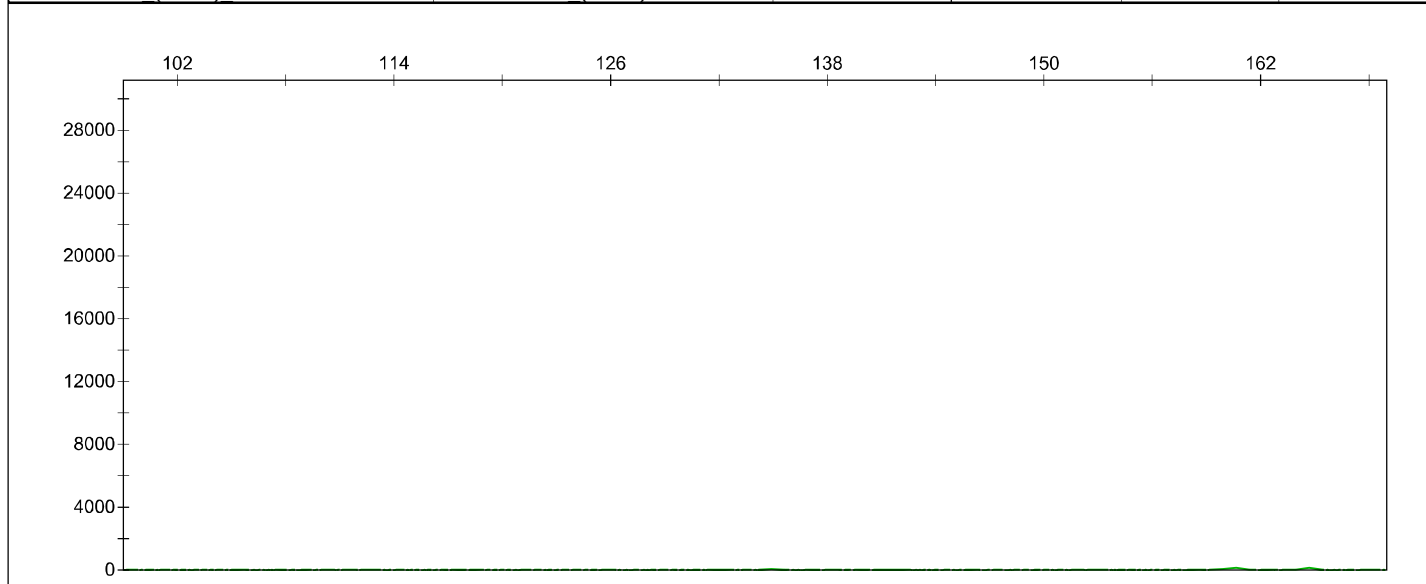
71102731453_ (15-25)_ E10.fsa	71102731453_ (15-25)	None		■	■
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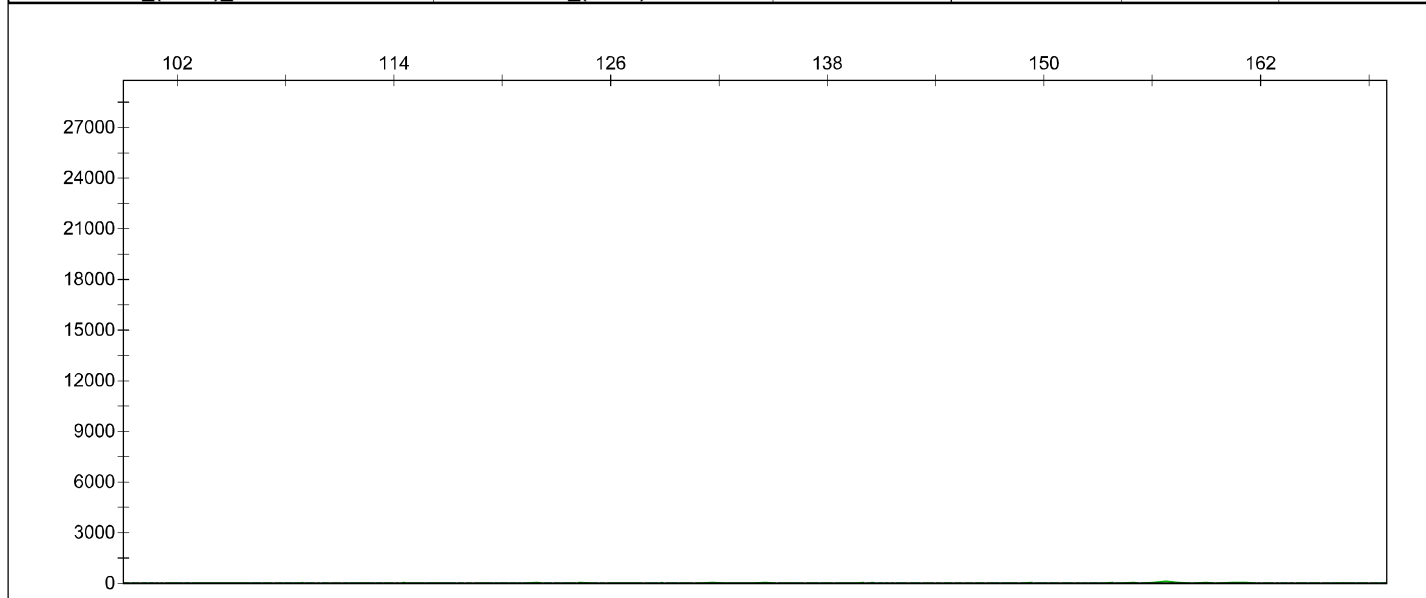
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731454_(15-26)_F10.fsa	71102731454_(15-26)	None			



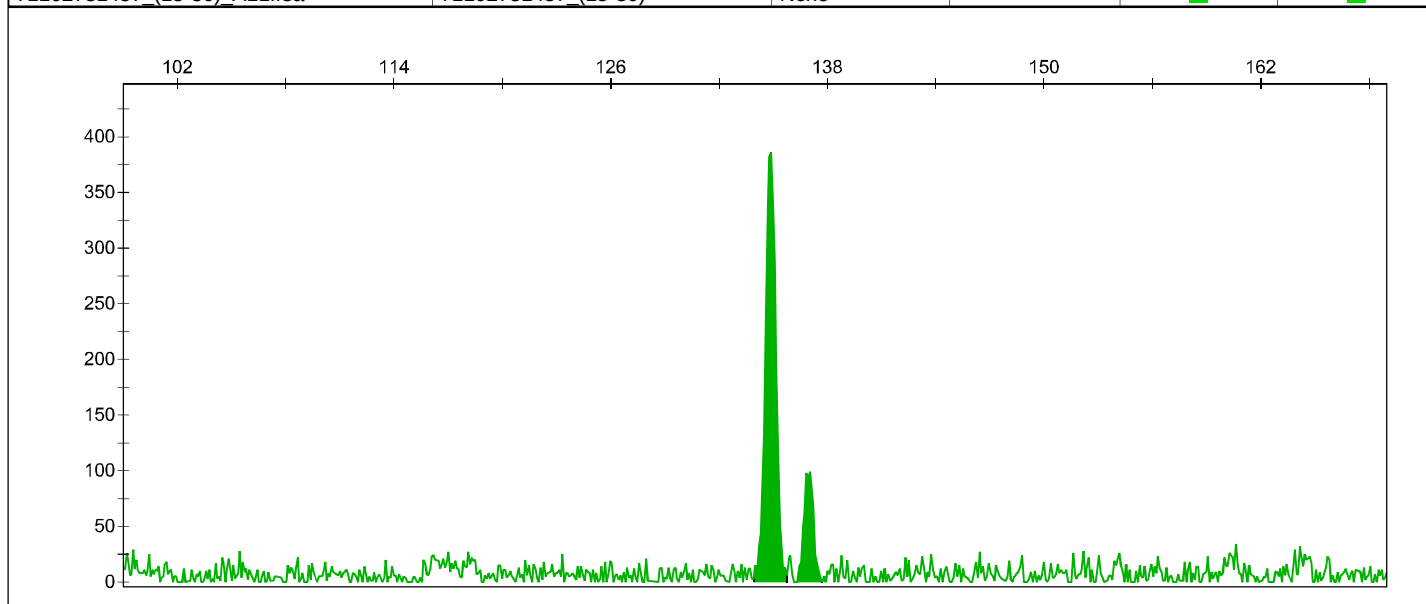
71102731455_(15-27)_G10.fsa	71102731455_(15-27)	None			
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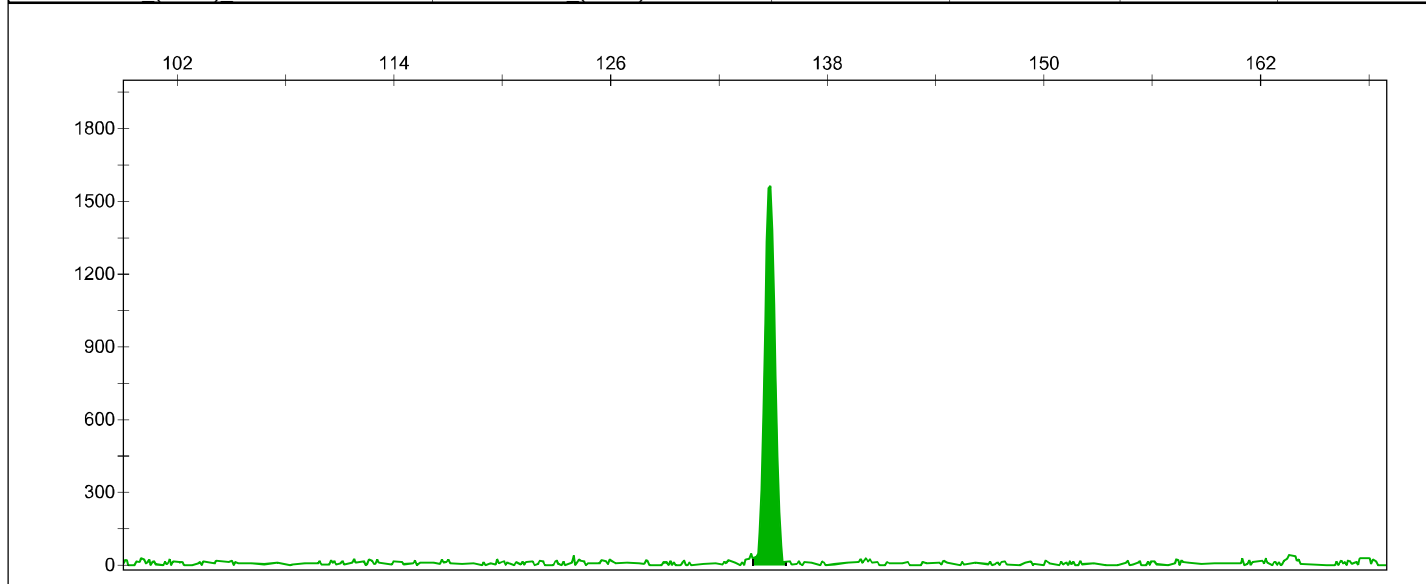
71102731456_(15-29)_H10.fsa	71102731456_(15-29)	None			
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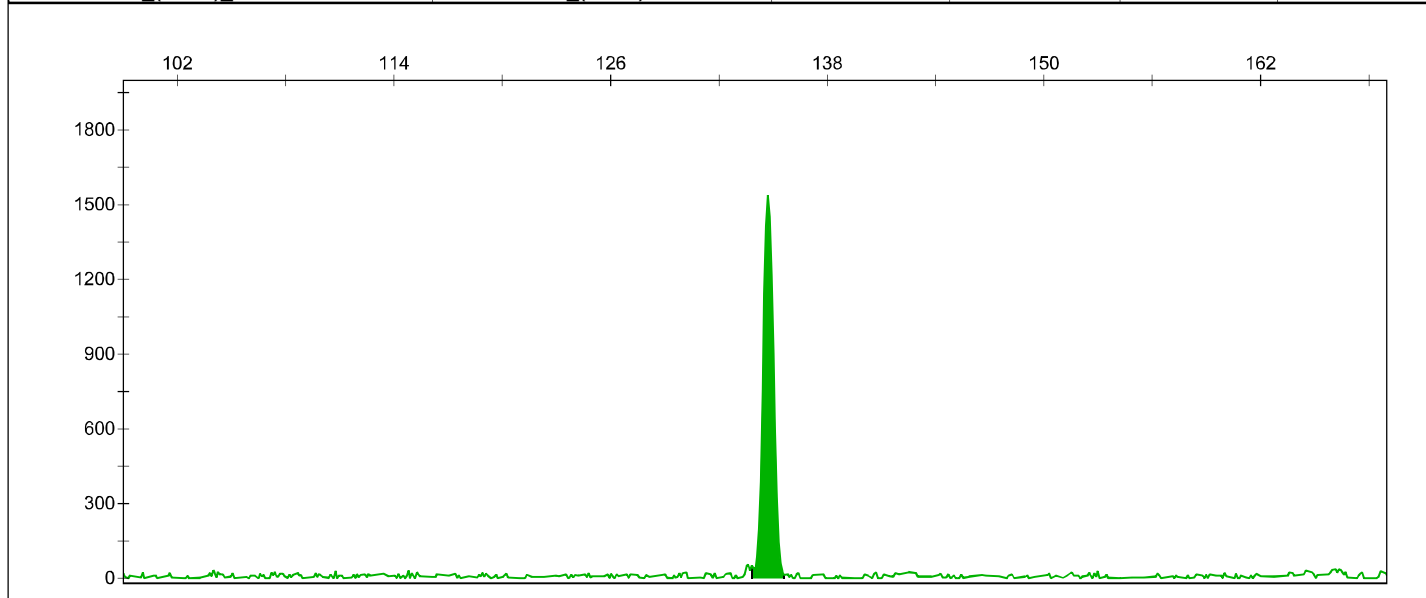
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731457_(15-30)_A11.fsa	71102731457_(15-30)	None		■	■



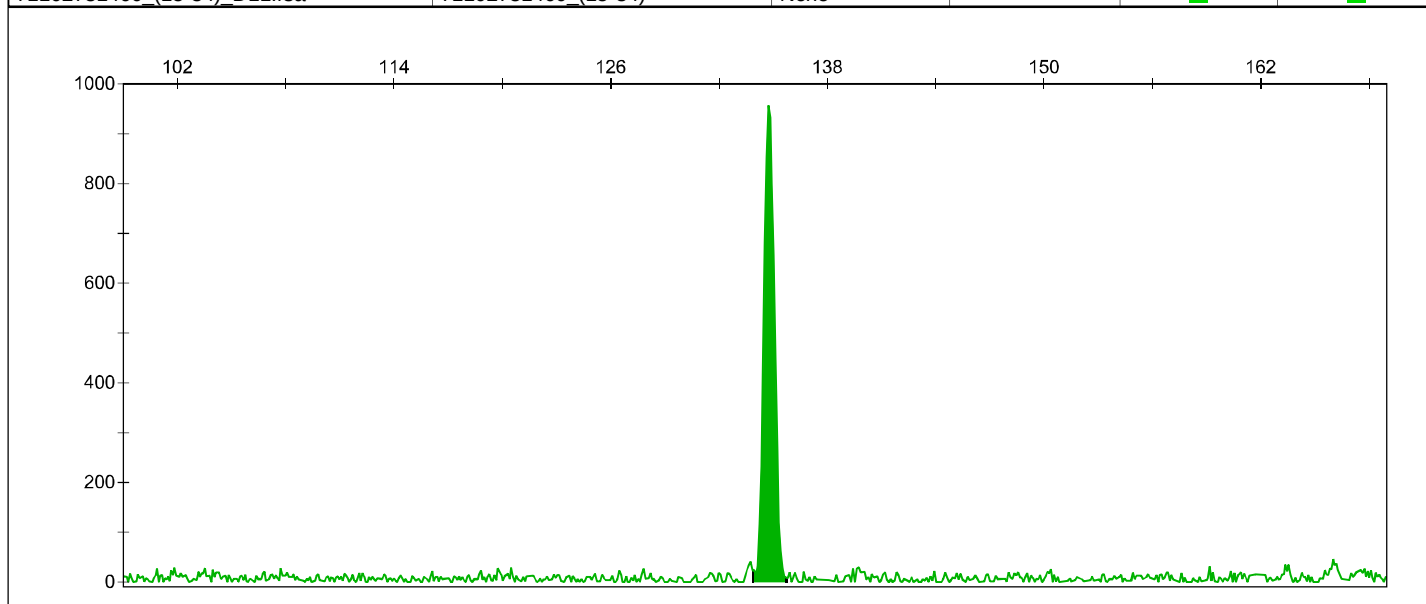
71102731458_(15-31)_B11.fsa	71102731458_(15-31)	None		■	■
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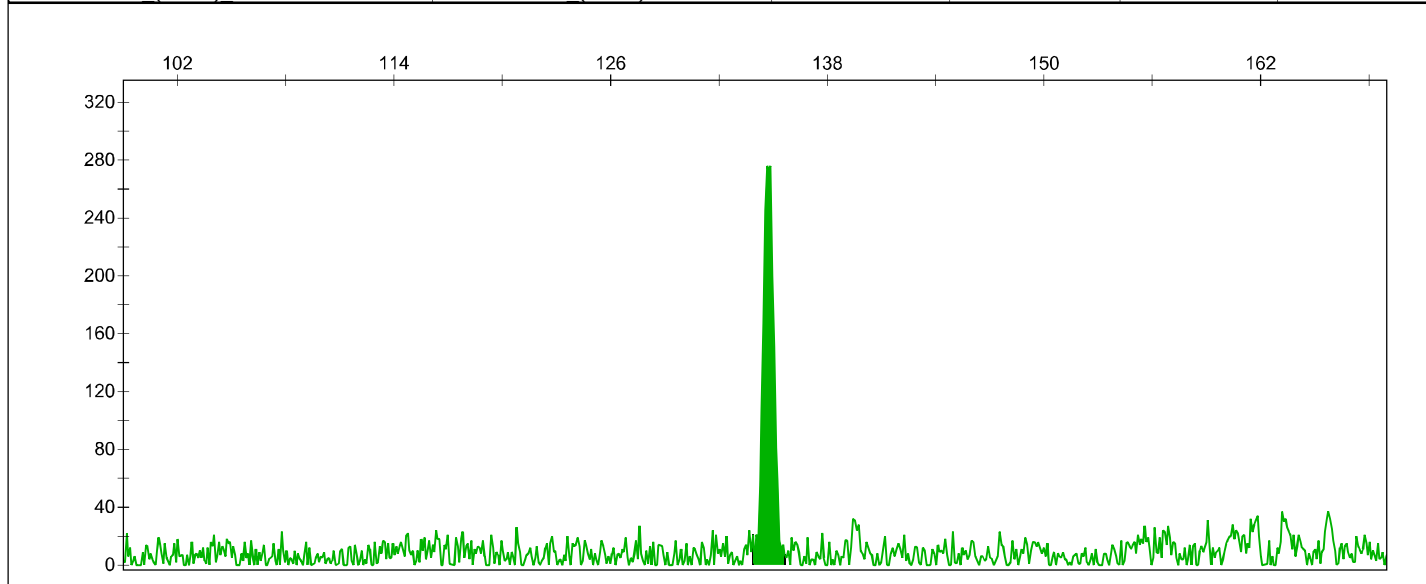
71102731459_(15-33)_C11.fsa	71102731459_(15-33)	None		■	■
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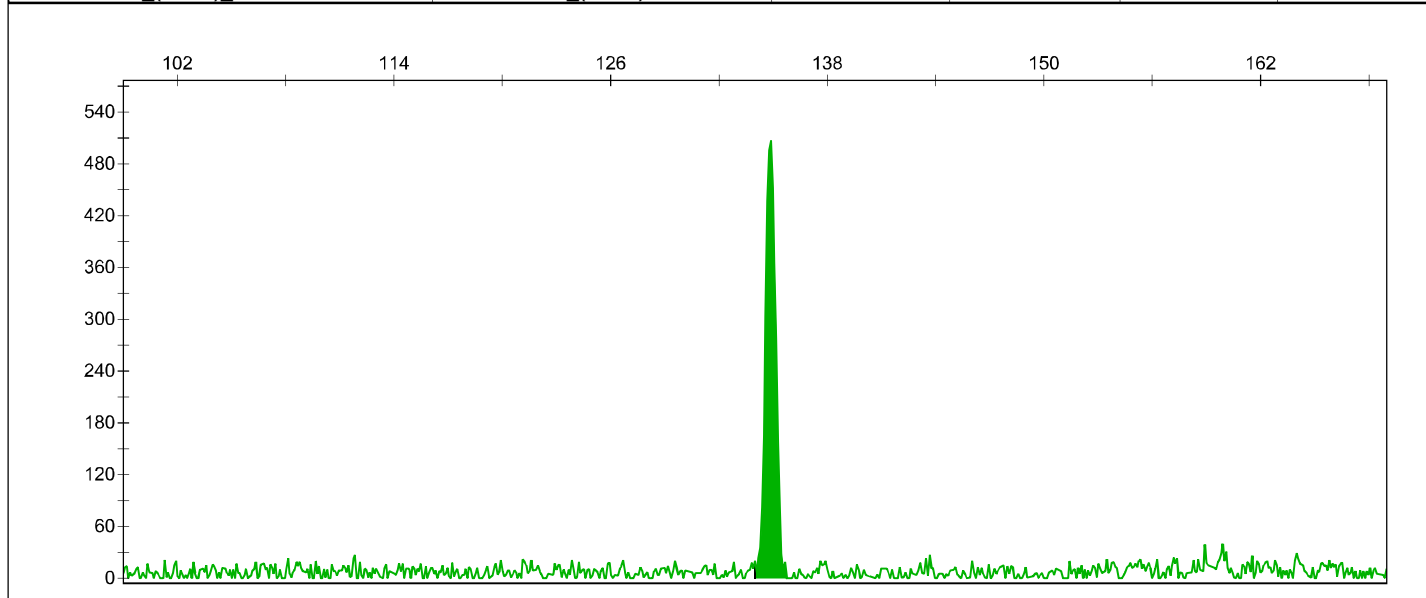
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731460_(15-34)_D11.fsa	71102731460_(15-34)	None		■	■



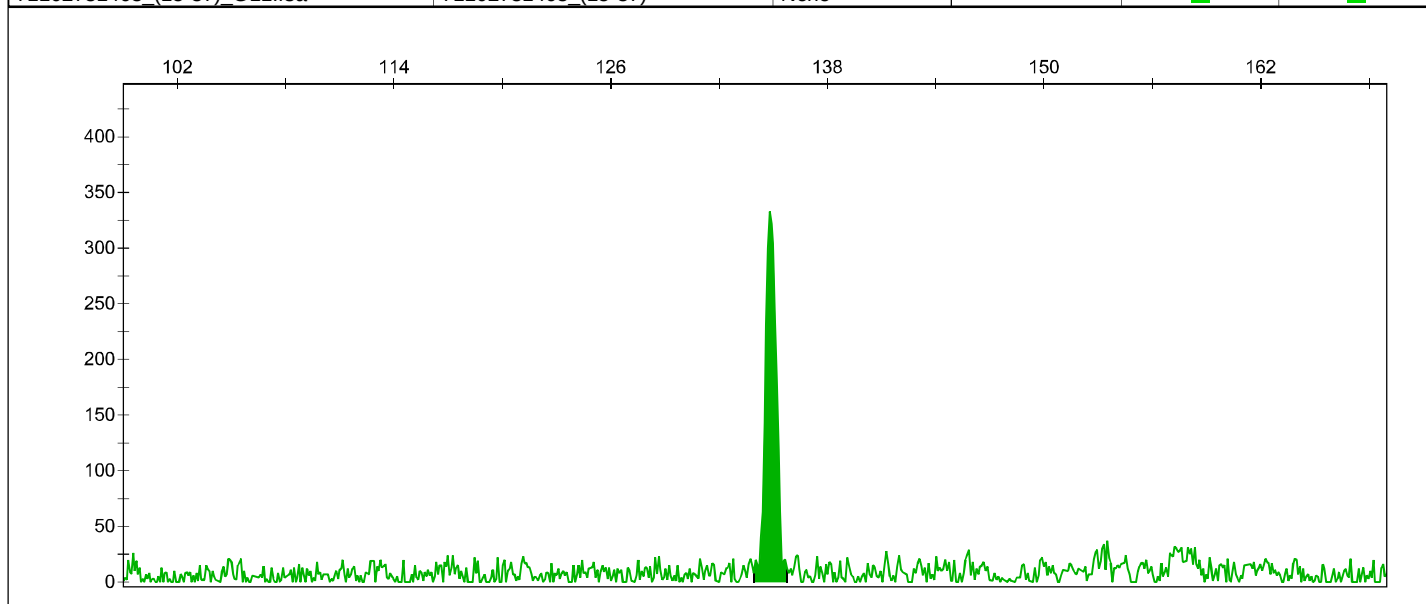
71102731461_(15-35)_E11.fsa	71102731461_(15-35)	None		■	■
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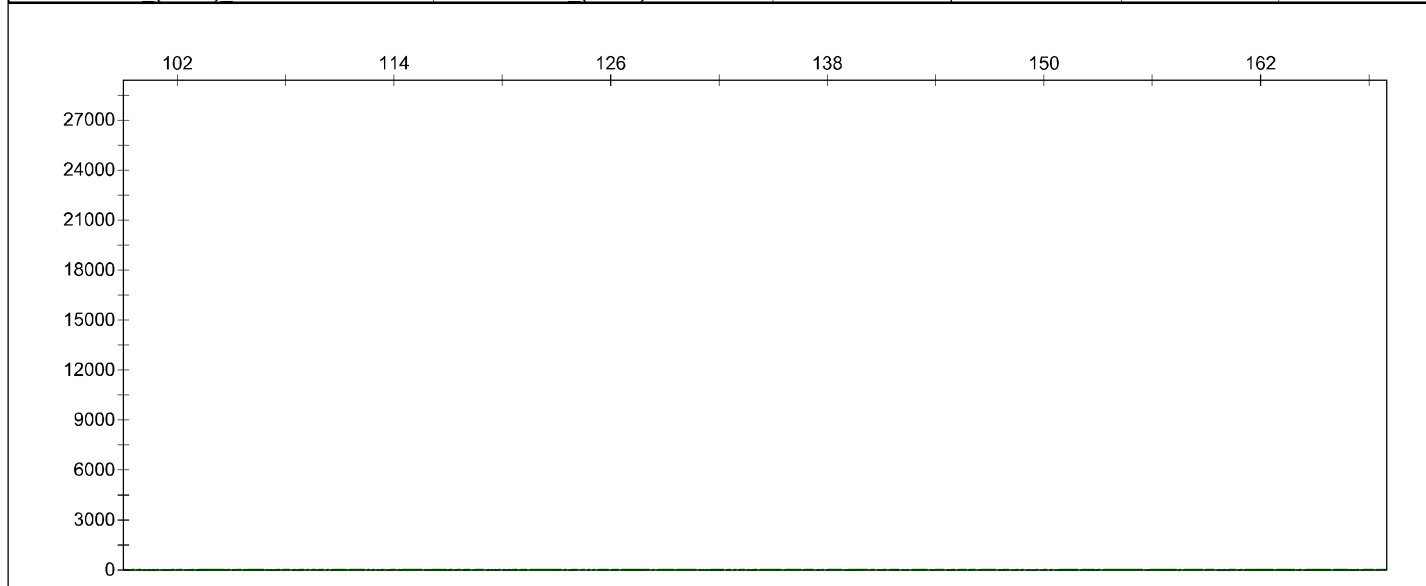
71102731462_(15-36)_F11.fsa	71102731462_(15-36)	None		■	■
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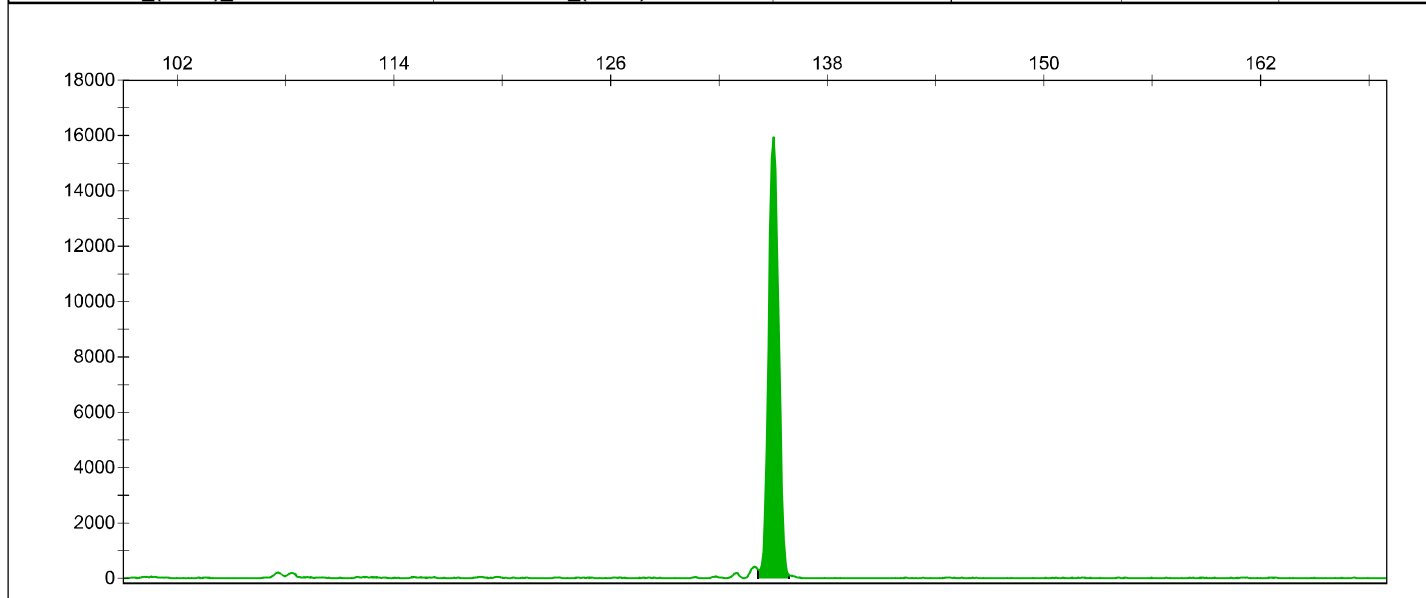
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731463 (15-37) G11.fsa	71102731463 (15-37)	None			



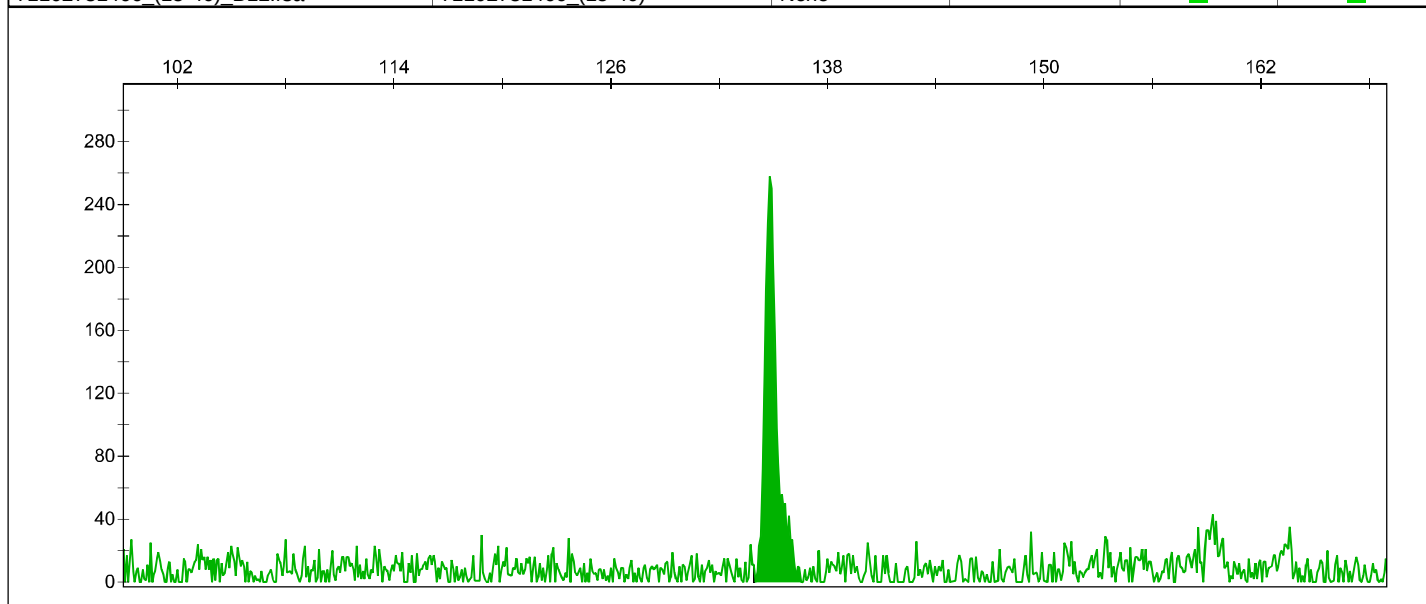
71102731464 (15-38) H11.fsa	71102731464 (15-38)	None			
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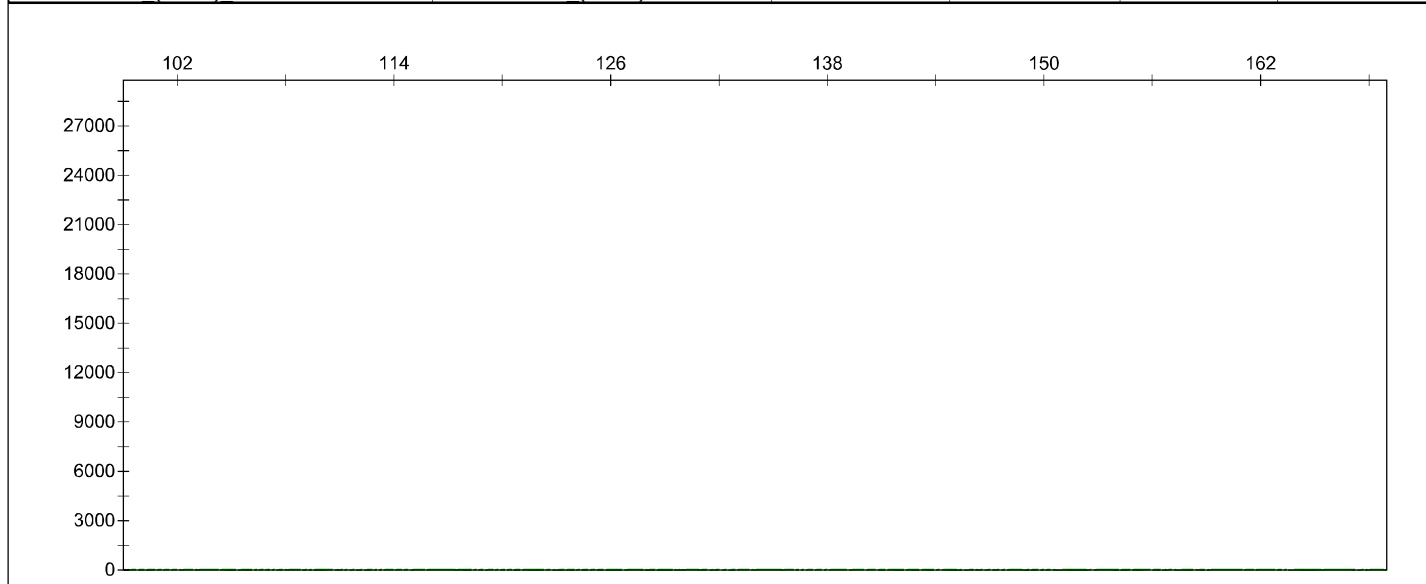
71102731465 (15-39) A12.fsa	71102731465 (15-39)	None			
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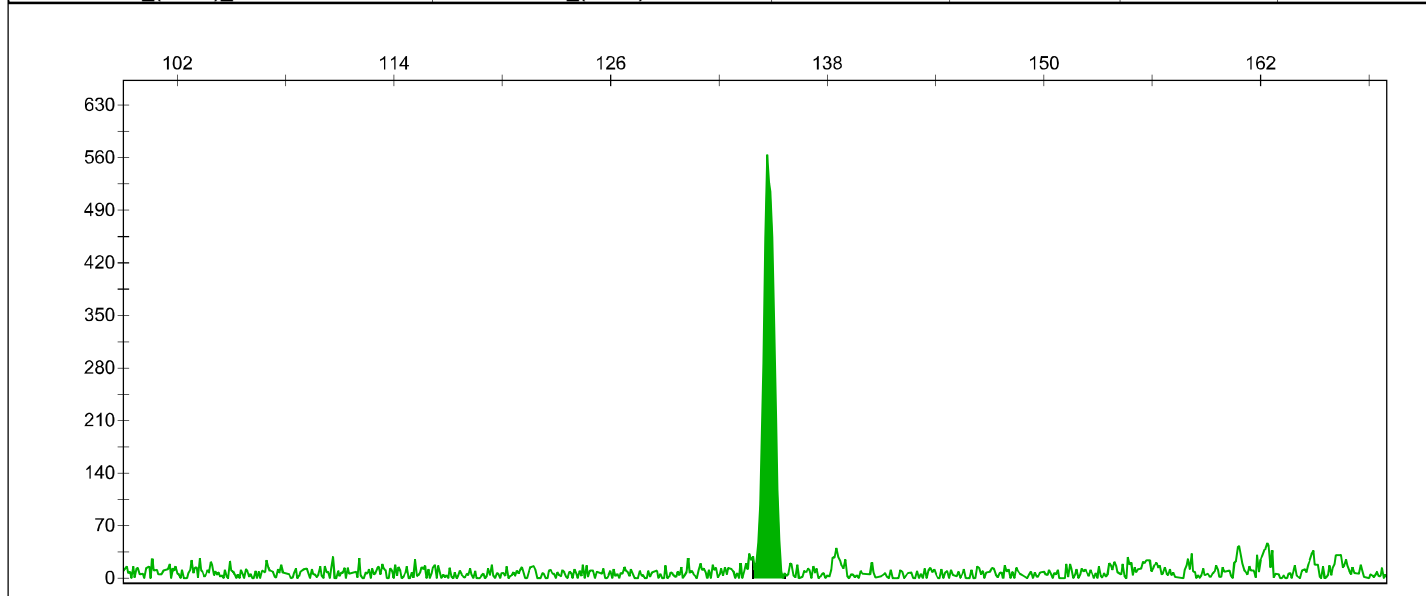
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731466_(15-40)_B12.fsa	71102731466_(15-40)	None		■	■



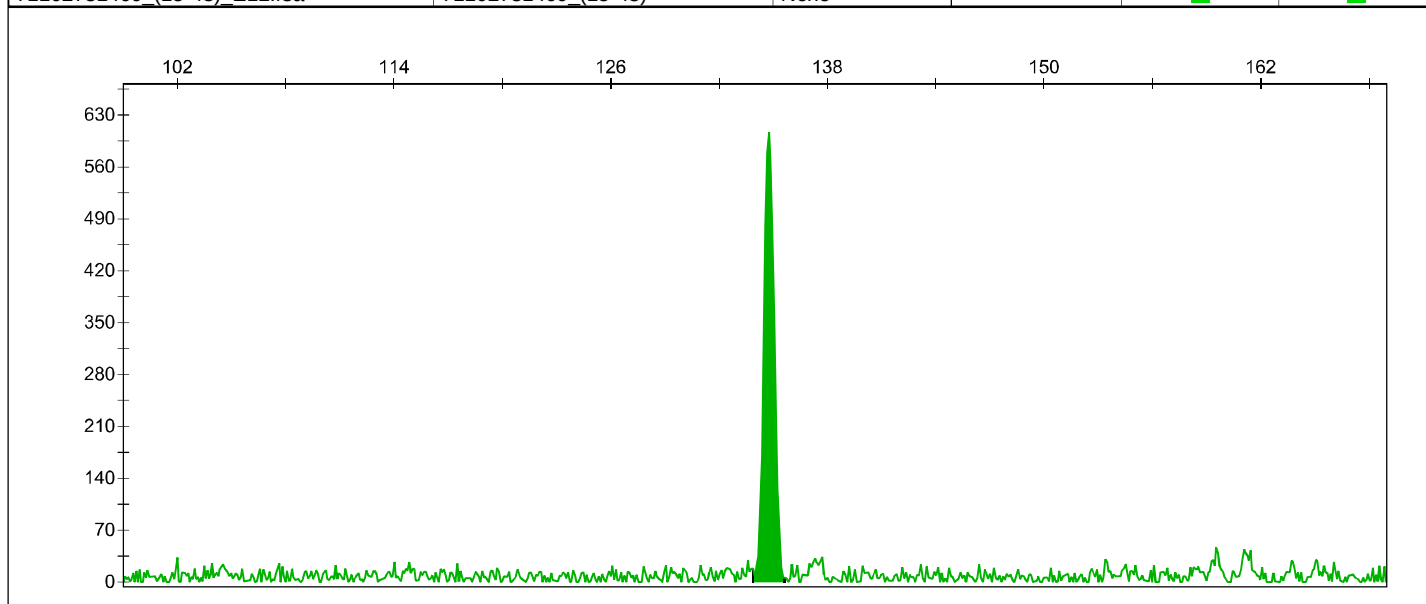
71102731467_(15-41)_C12.fsa	71102731467_(15-41)	None		■	■
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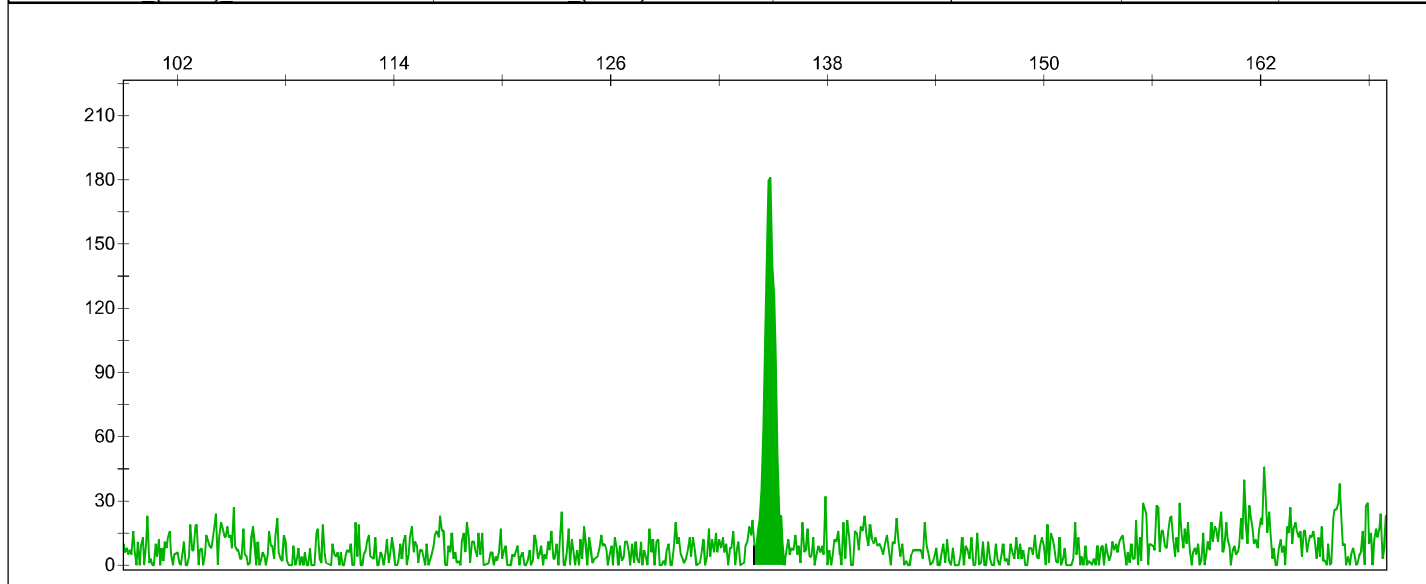
71102731468_(15-42)_D12.fsa	71102731468_(15-42)	None		■	■
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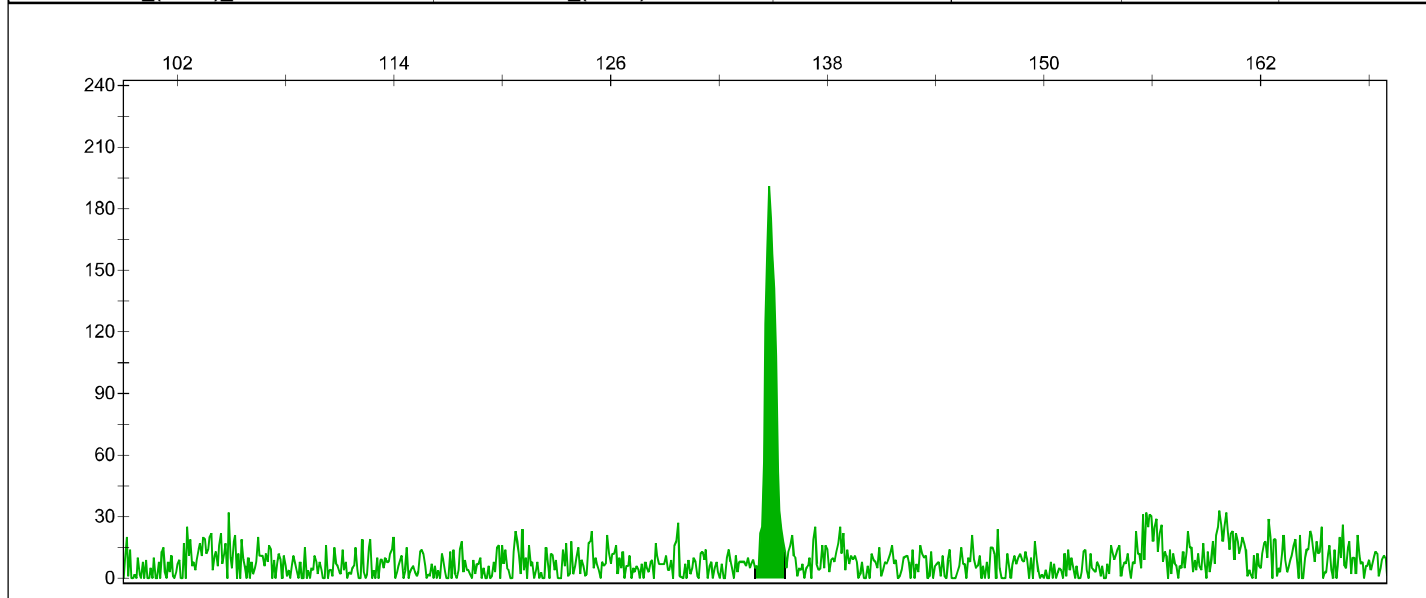
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731469_ (15-43)_ E12.fsa	71102731469_ (15-43)	None		■	■



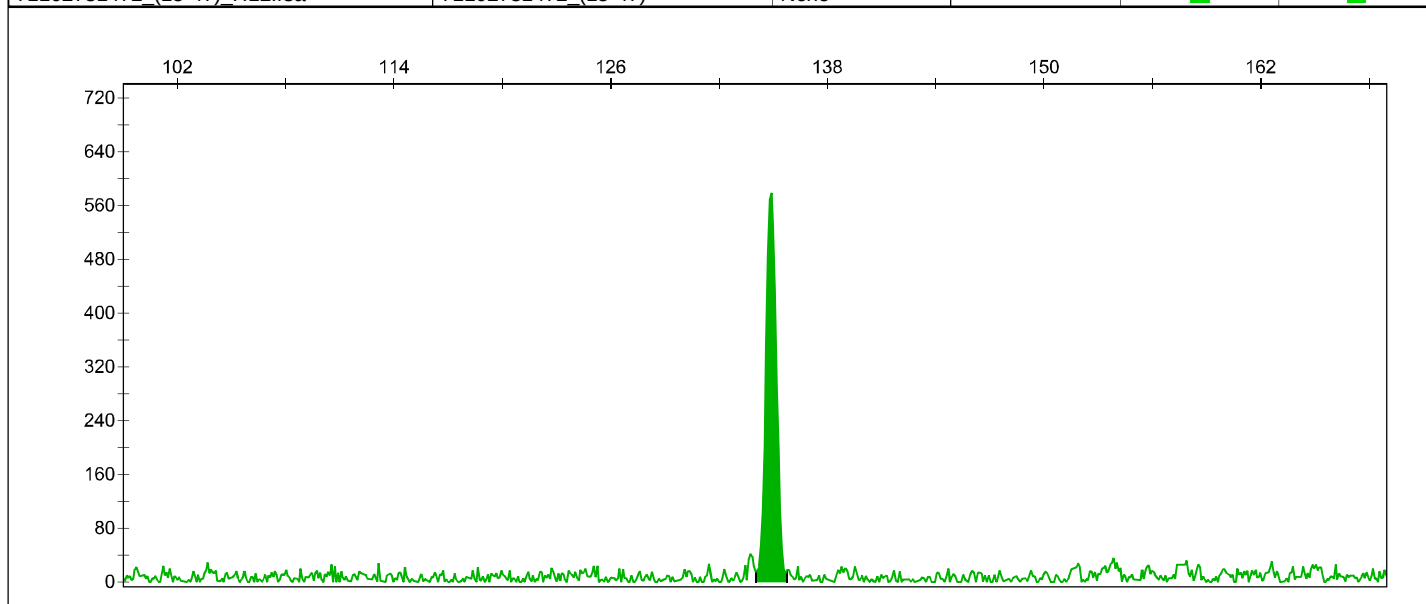
71102731470_ (15-45)_ F12.fsa	71102731470_ (15-45)	None		■	■
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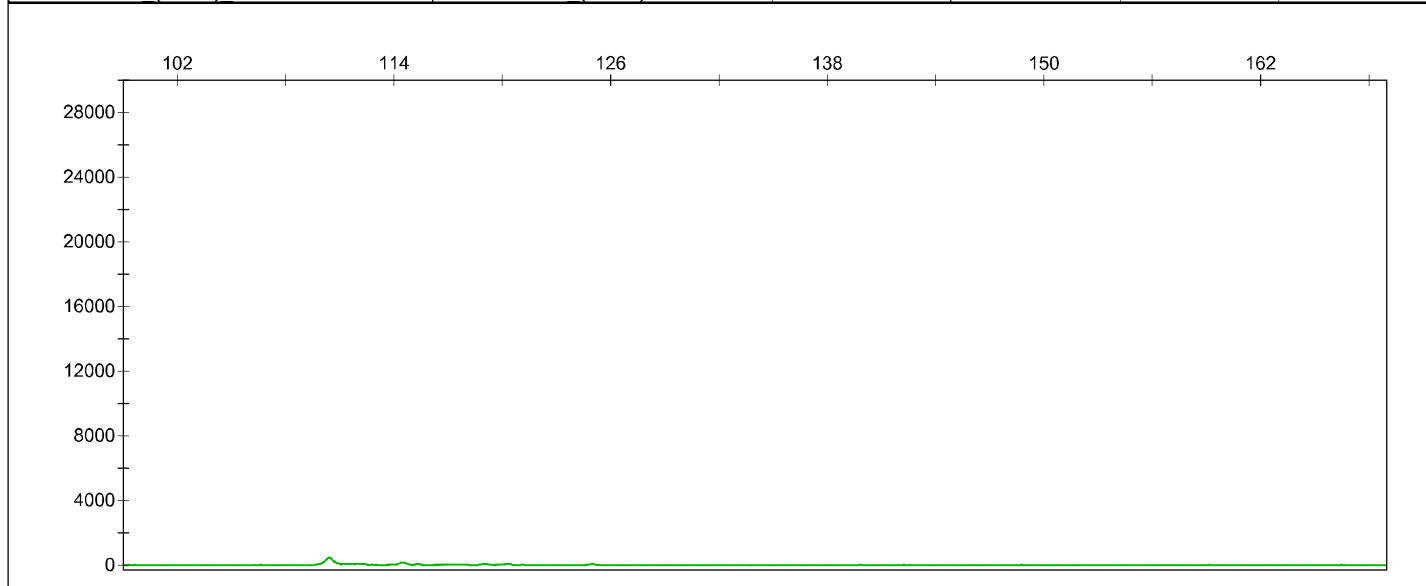
71102731471_ (15-46)_ G12.fsa	71102731471_ (15-46)	None		■	■
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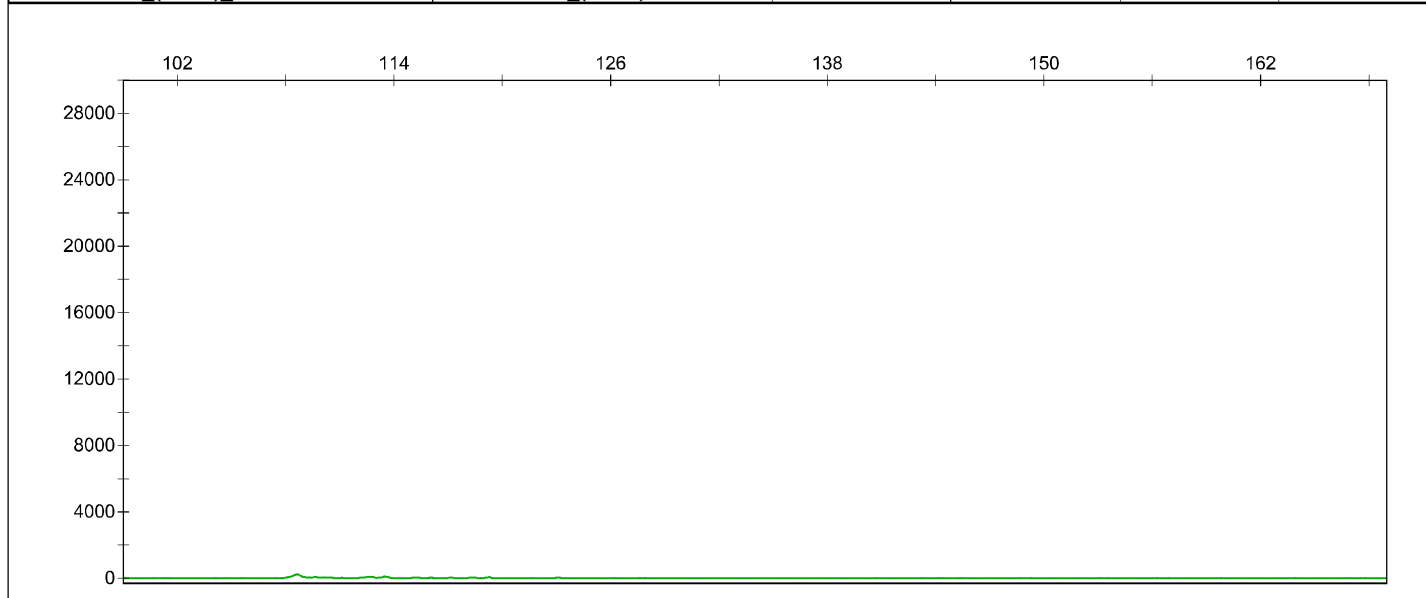
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731472_ (15-47)_ H12.fsa	71102731472_ (15-47)	None			



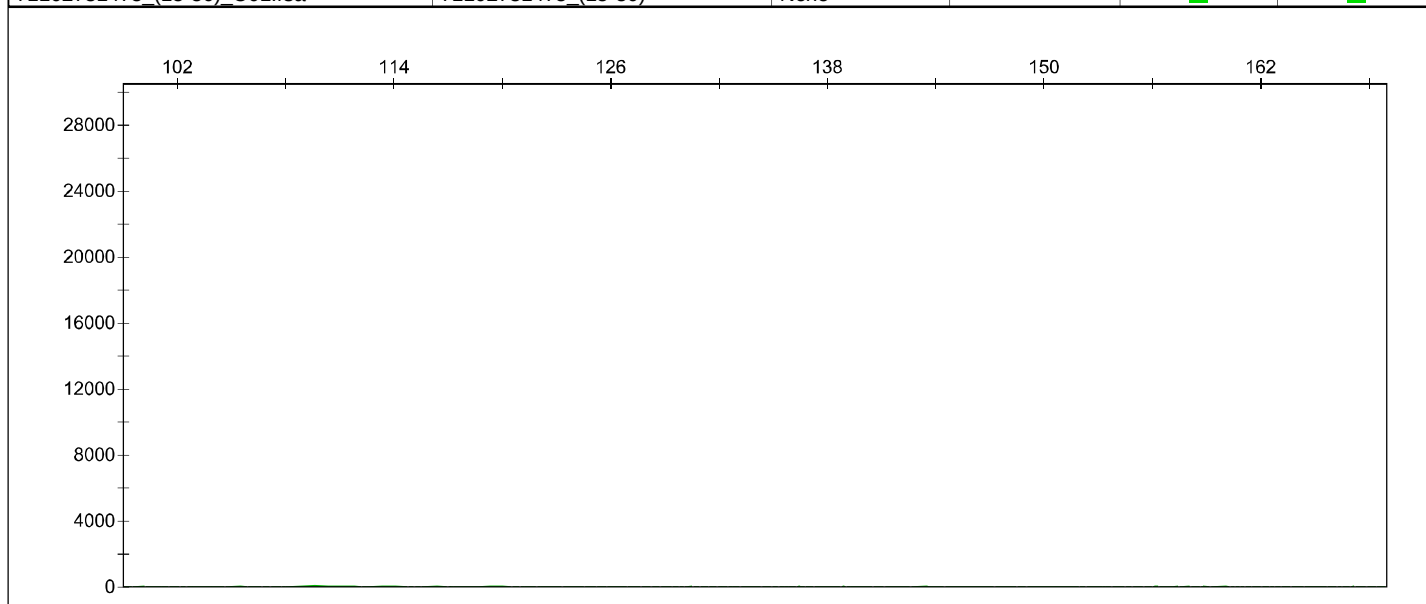
71102731473_ (15-48)_ A01.fsa	71102731473_ (15-48)	None			
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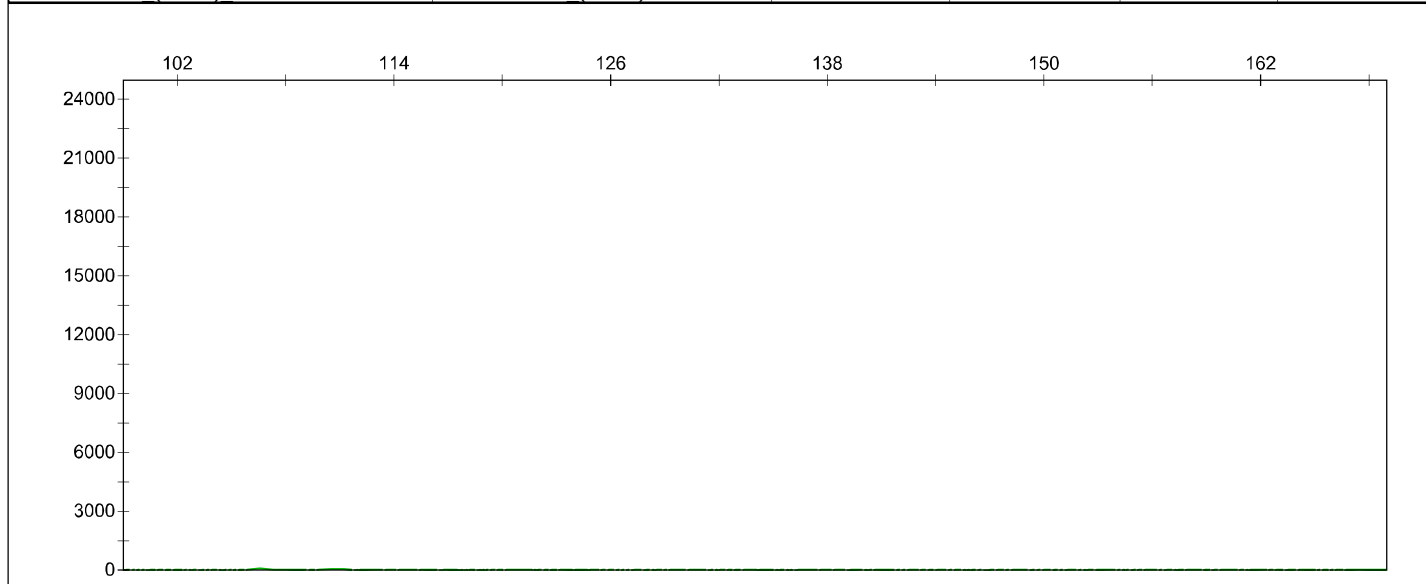
71102731474_ (15-49)_ B01.fsa	71102731474_ (15-49)	None			
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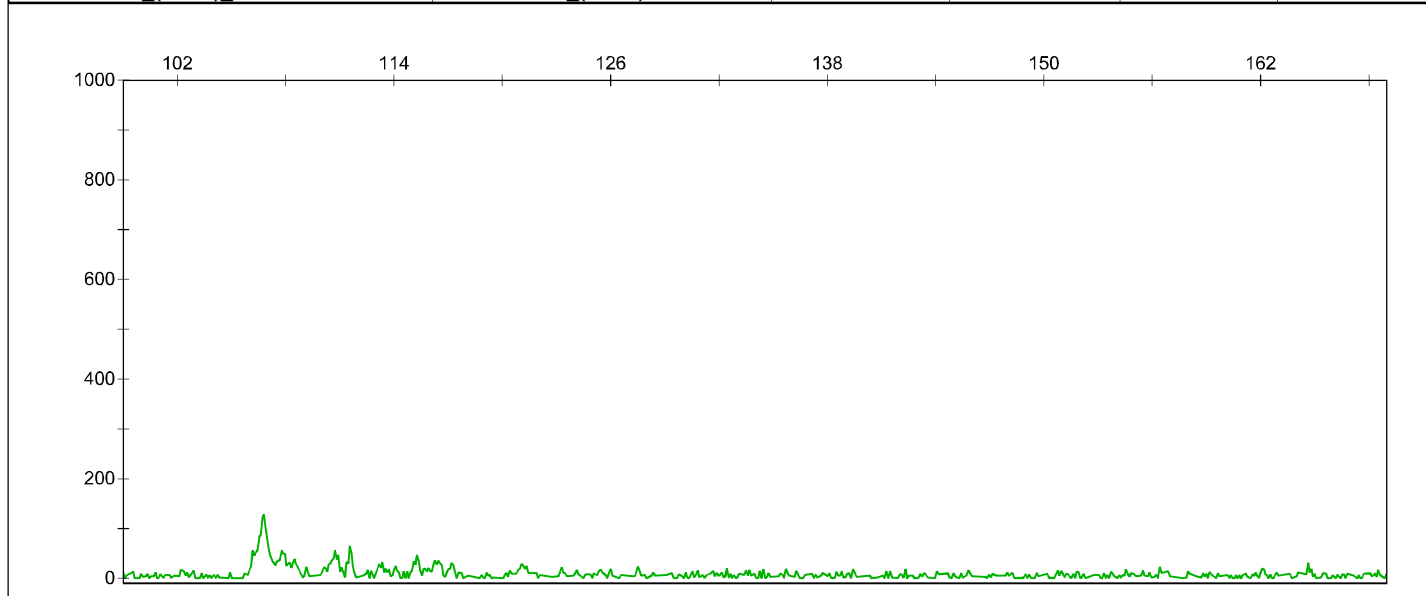
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731475_(15-50)_C01.fsa	71102731475_(15-50)	None		■	■



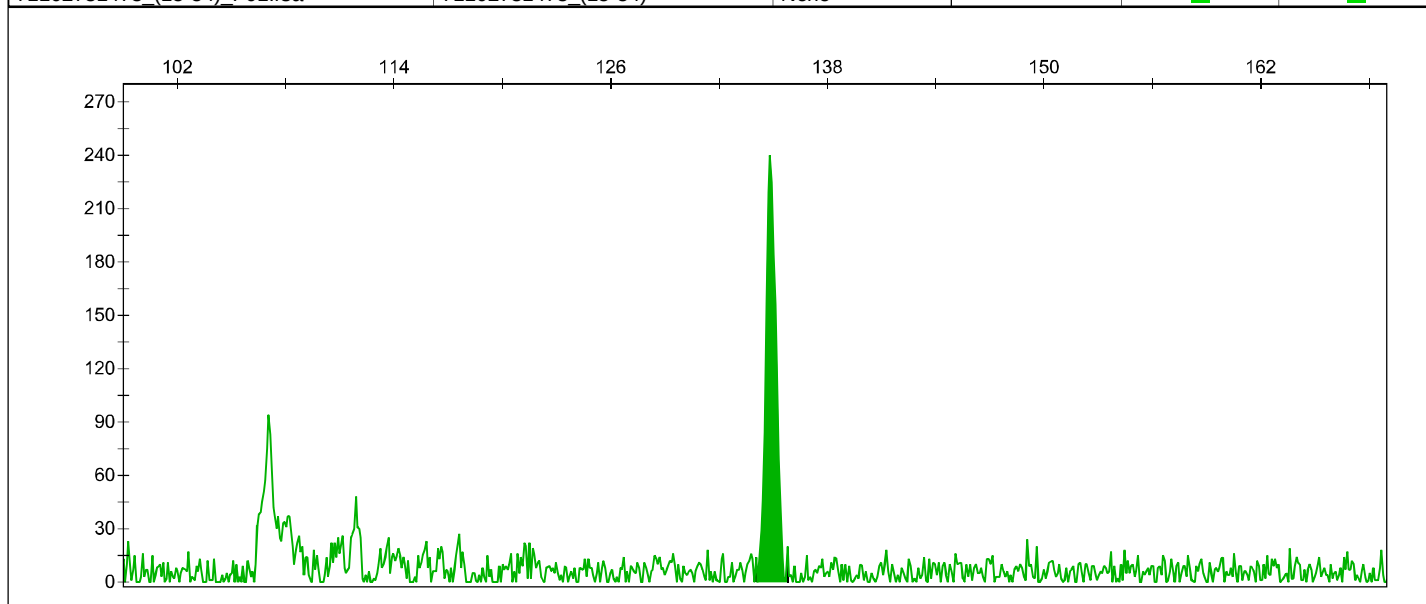
71102731476_(15-51)_D01.fsa	71102731476_(15-51)	None		■	■
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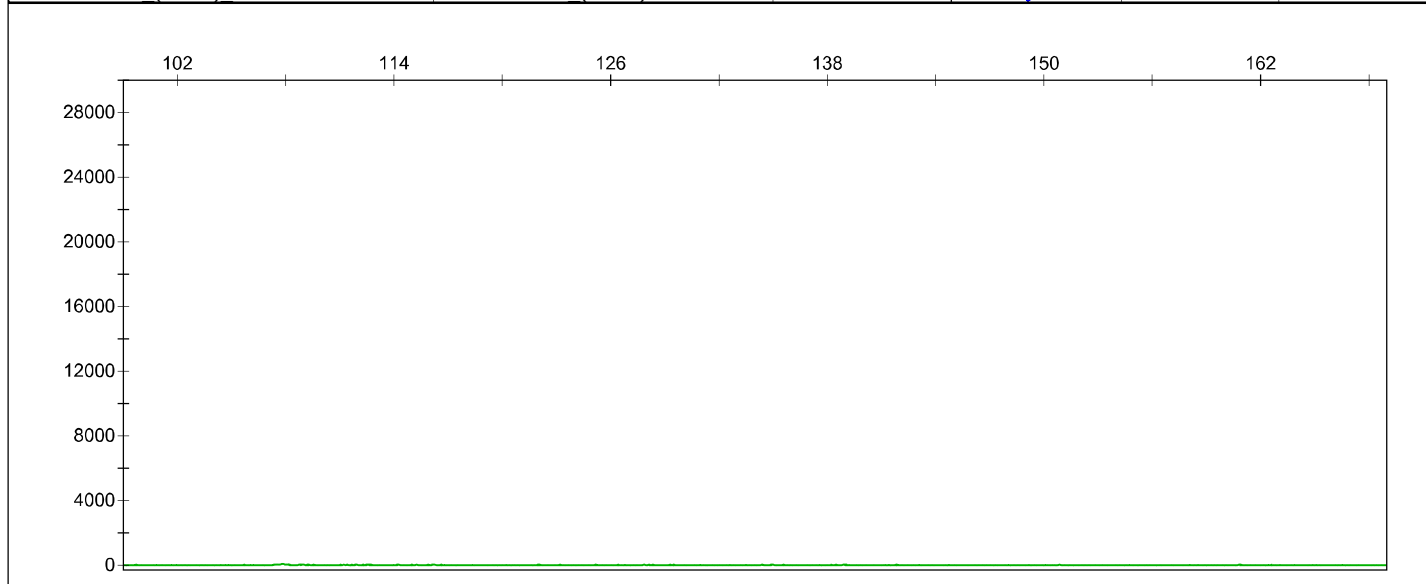
71102731477_(15-53)_E01.fsa	71102731477_(15-53)	None		■	■
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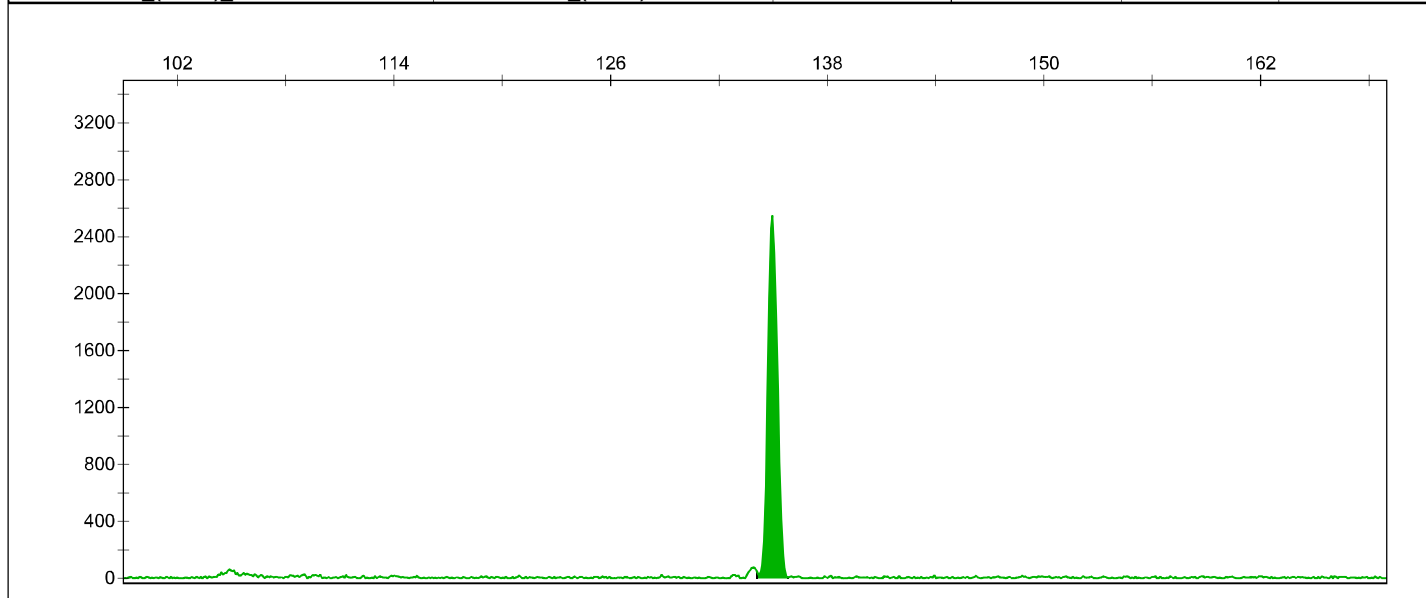
Sample File	Sample Name	Panel	SQO	OS	SQ
71102731478 (15-54) F01.fsa	71102731478 (15-54)	None		■	■



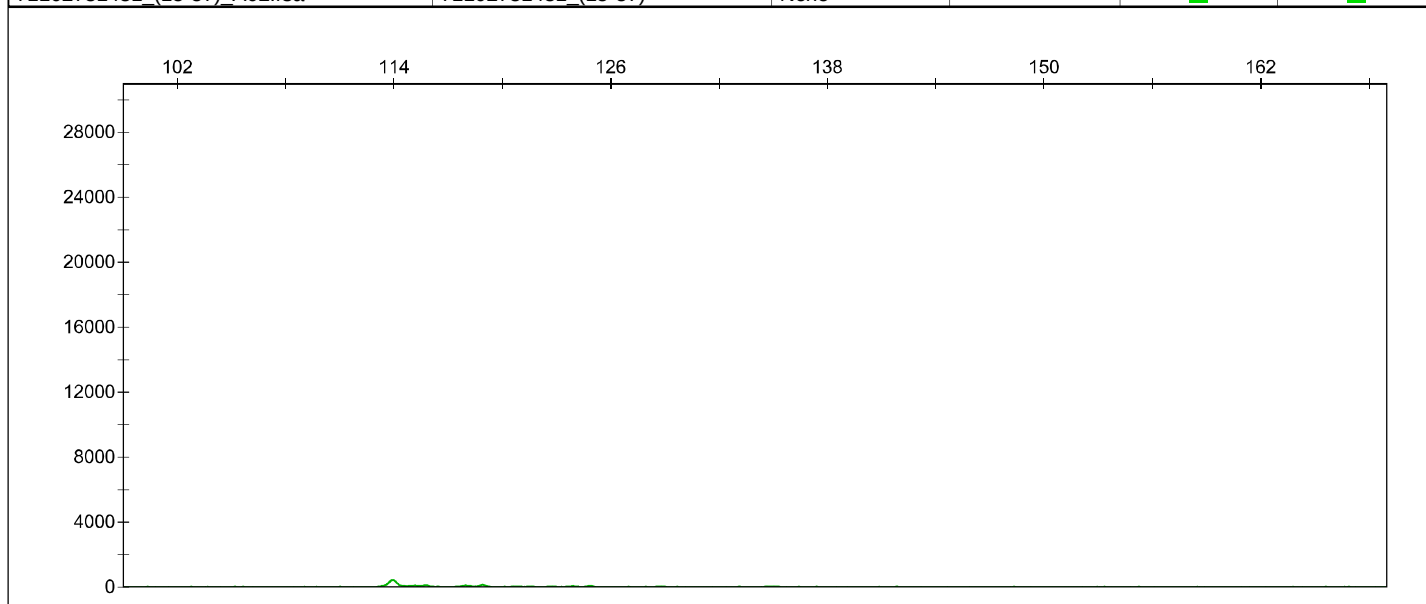
71102731479 (15-55) G01.fsa	71102731479 (15-55)	None	✕	■	■
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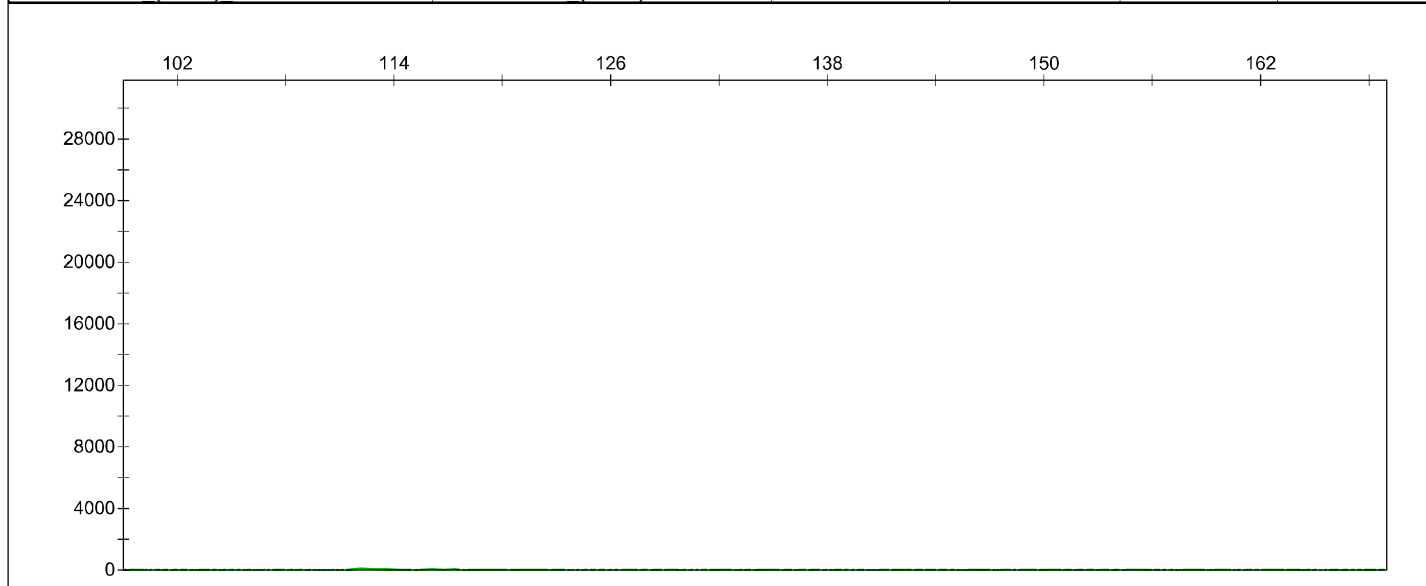
71102731480 (15-56) H01.fsa	71102731480 (15-56)	None		■	■
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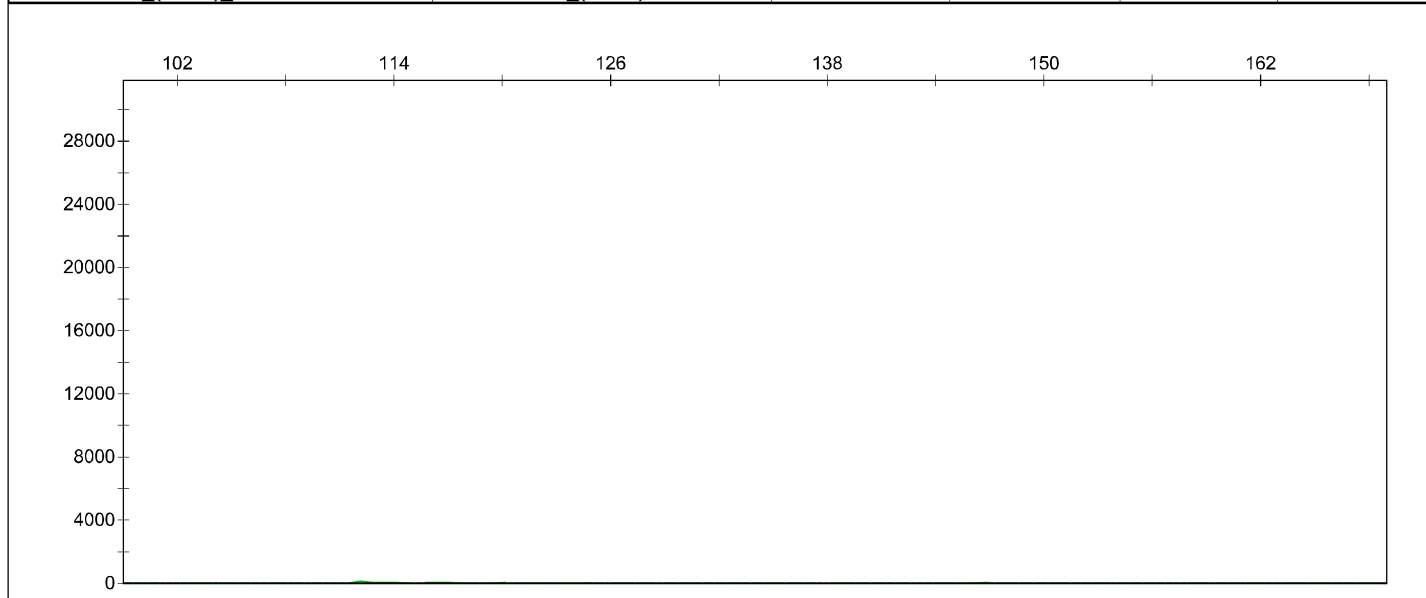
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731481_(15-57)_A02.fsa	71102731481_(15-57)	None		■	■



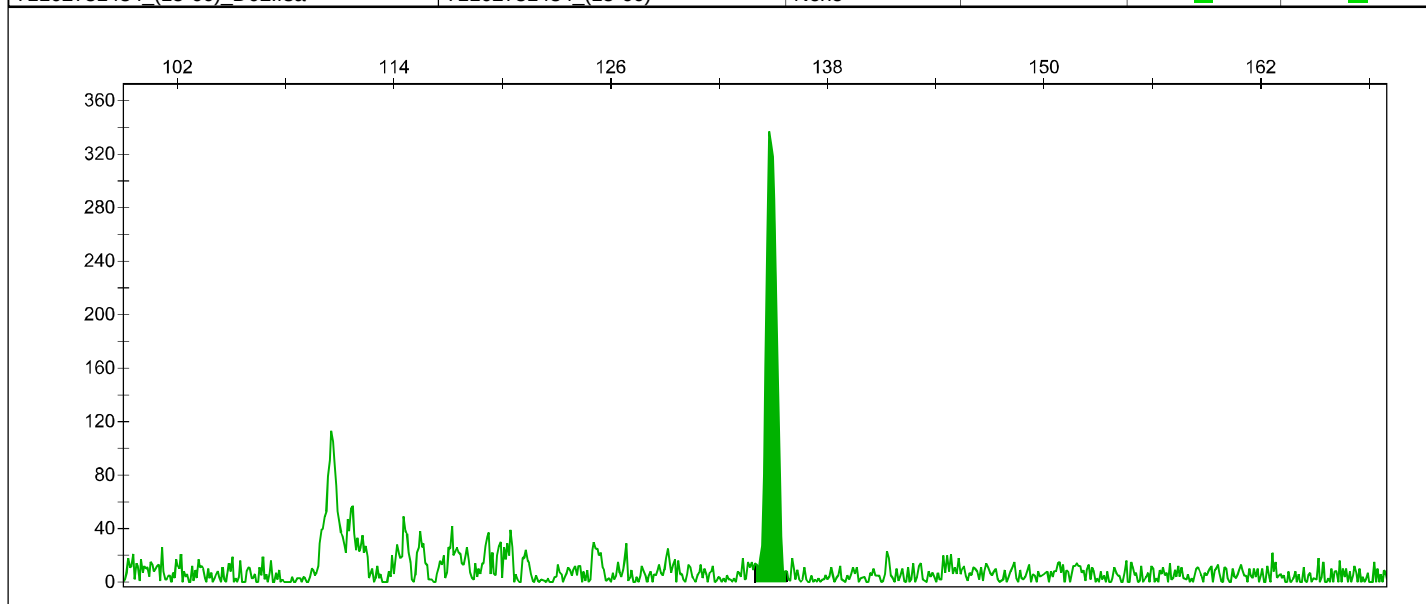
71102731482_(15-58)_B02.fsa	71102731482_(15-58)	None		■	■
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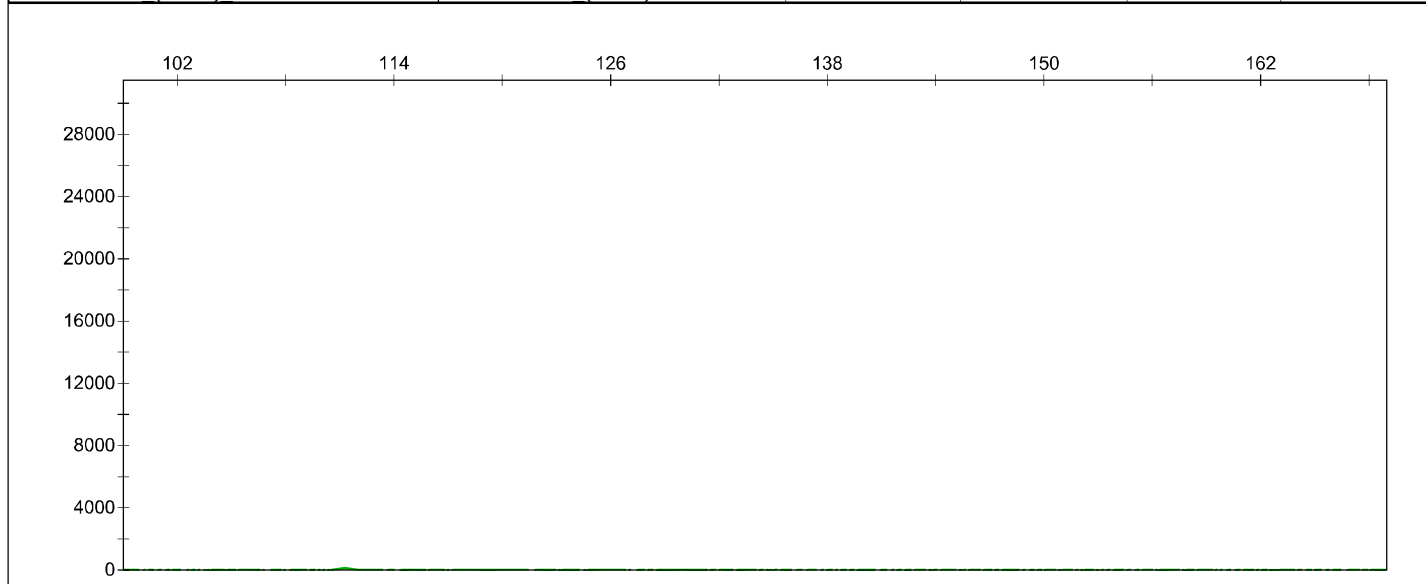
71102731483_(15-59)_C02.fsa	71102731483_(15-59)	None		■	■
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Sample File	Sample Name	Panel	SQO	OS	SQ
71102731484_(15-60)_D02.fsa	71102731484_(15-60)	None			



71102731485_(15-61)_E02.fsa	71102731485_(15-61)	None			
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71102731486_(15-100)_F02.fsa	71102731486_(15-100)	None			
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