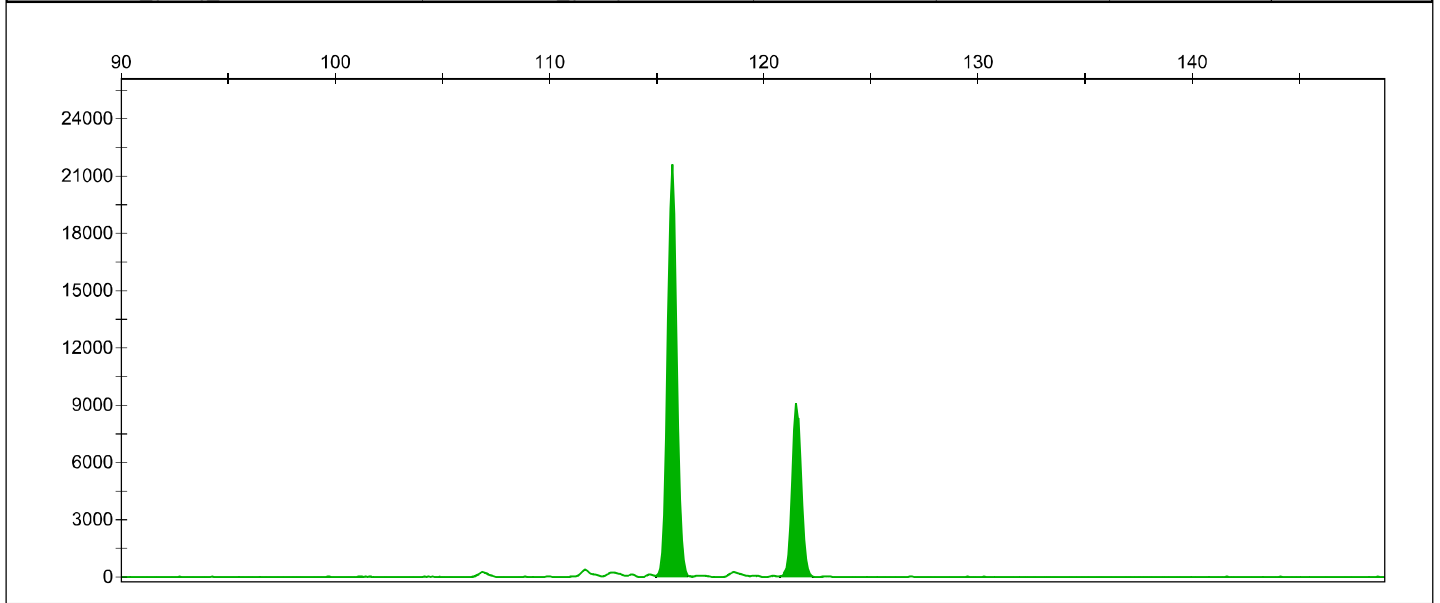
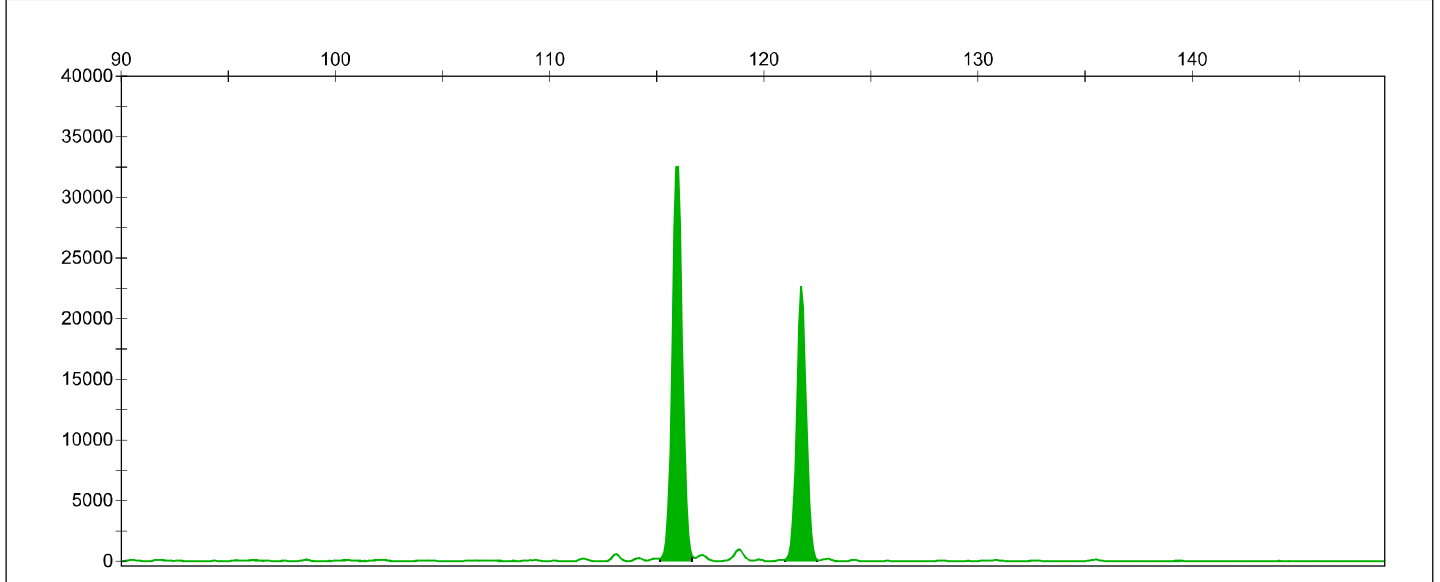


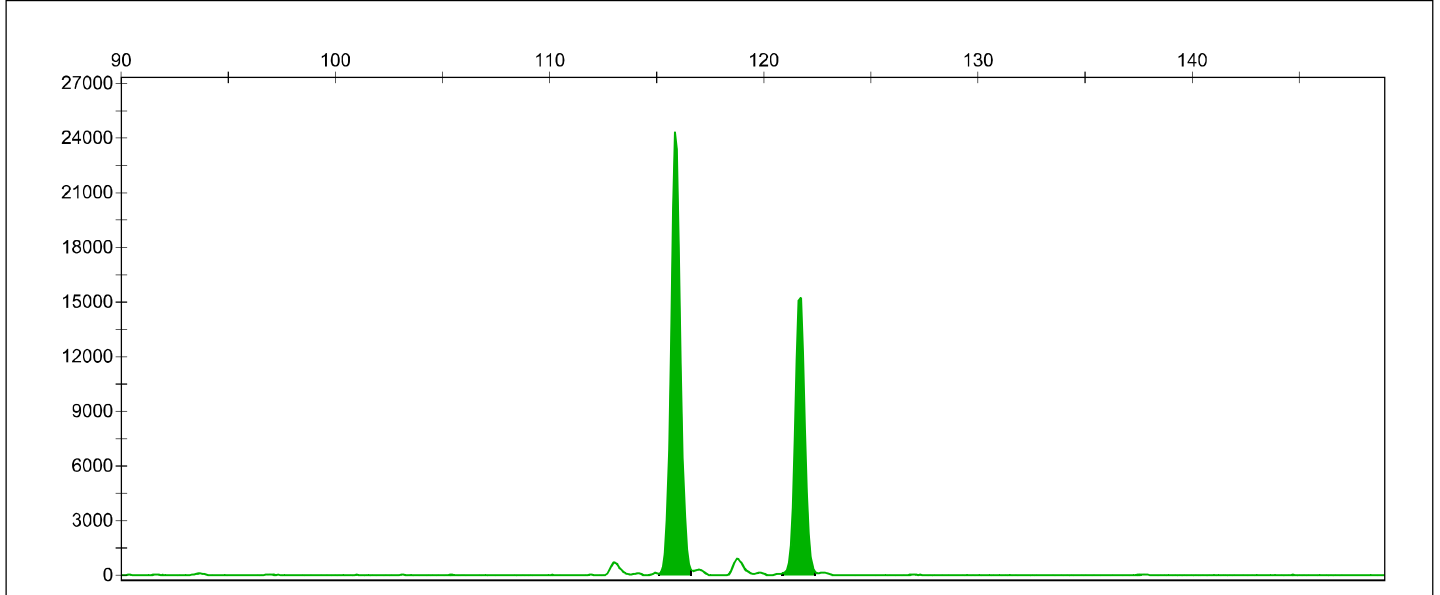
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731487_(35-1)_A03.fsa	71102731487_(35-1)	None		■	■



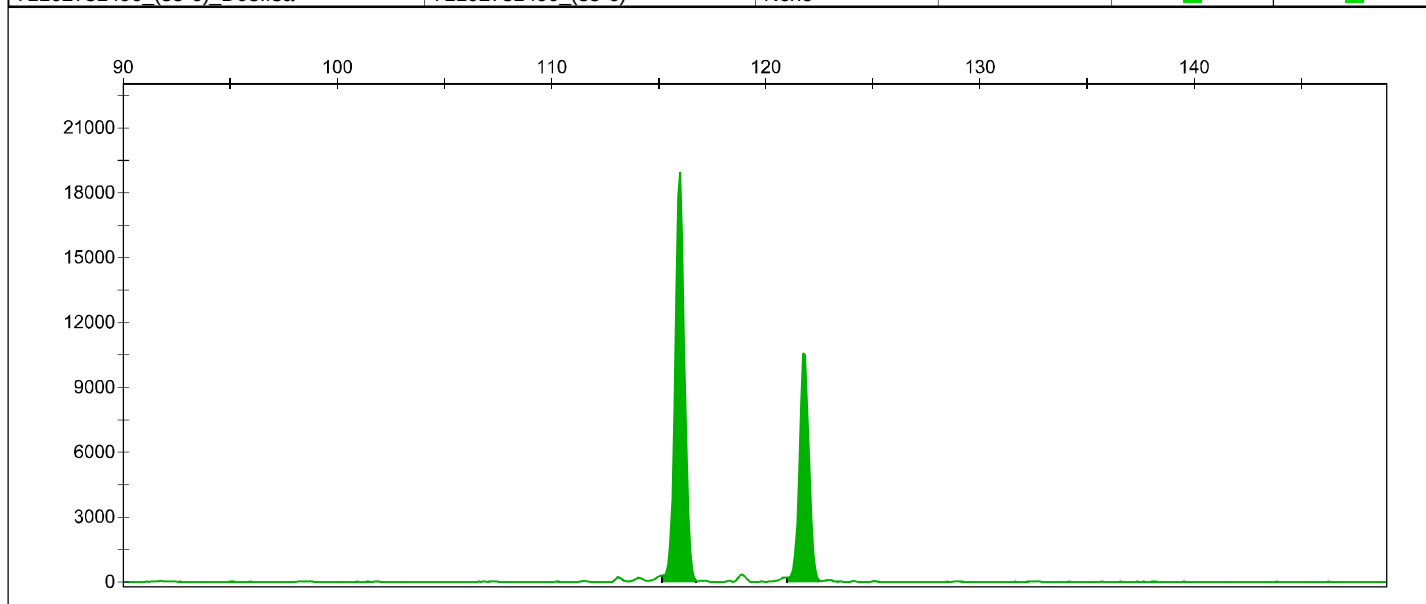
71102731488_(35-3)_B03.fsa	71102731488_(35-3)	None		▲	■
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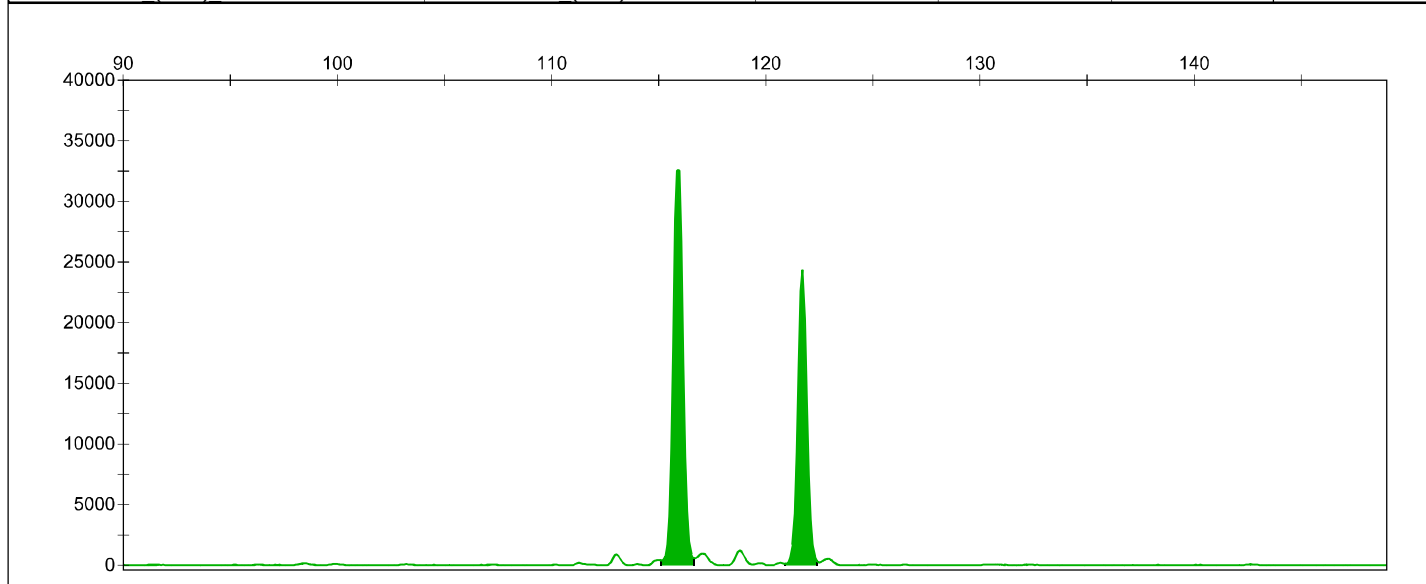
71102731489_(35-5)_C03.fsa	71102731489_(35-5)	None		■	■
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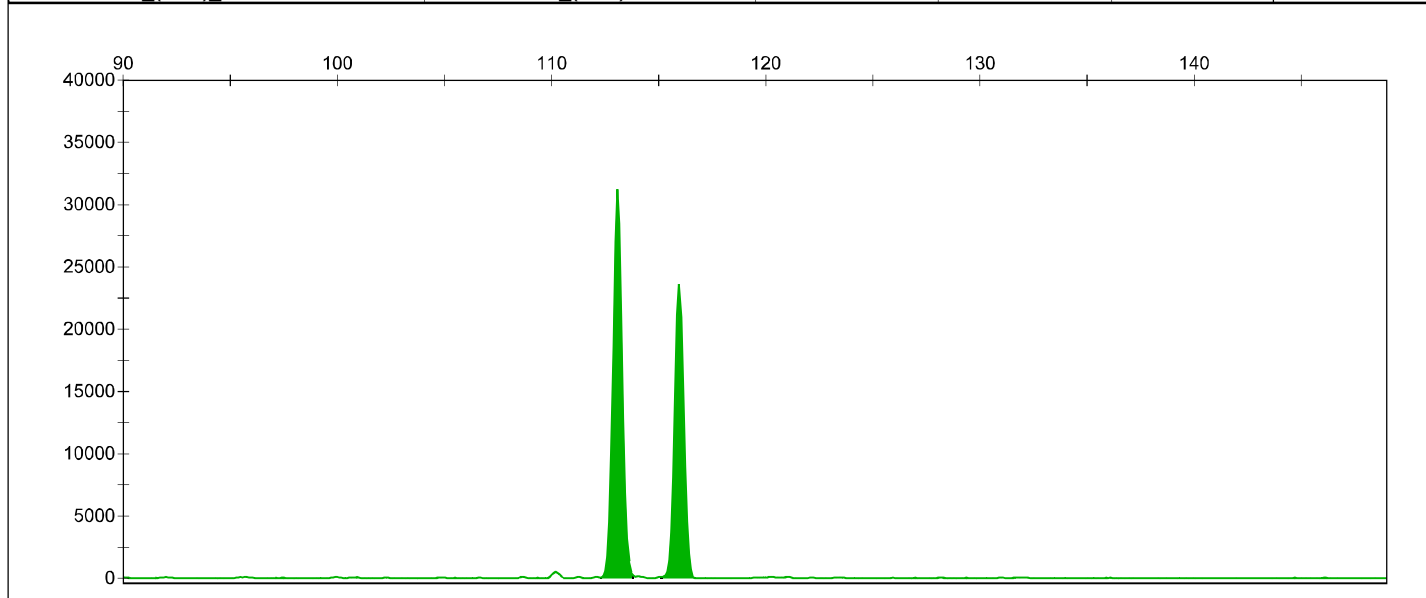
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731490_(35-6)_D03.fsa	71102731490_(35-6)	None		■	■



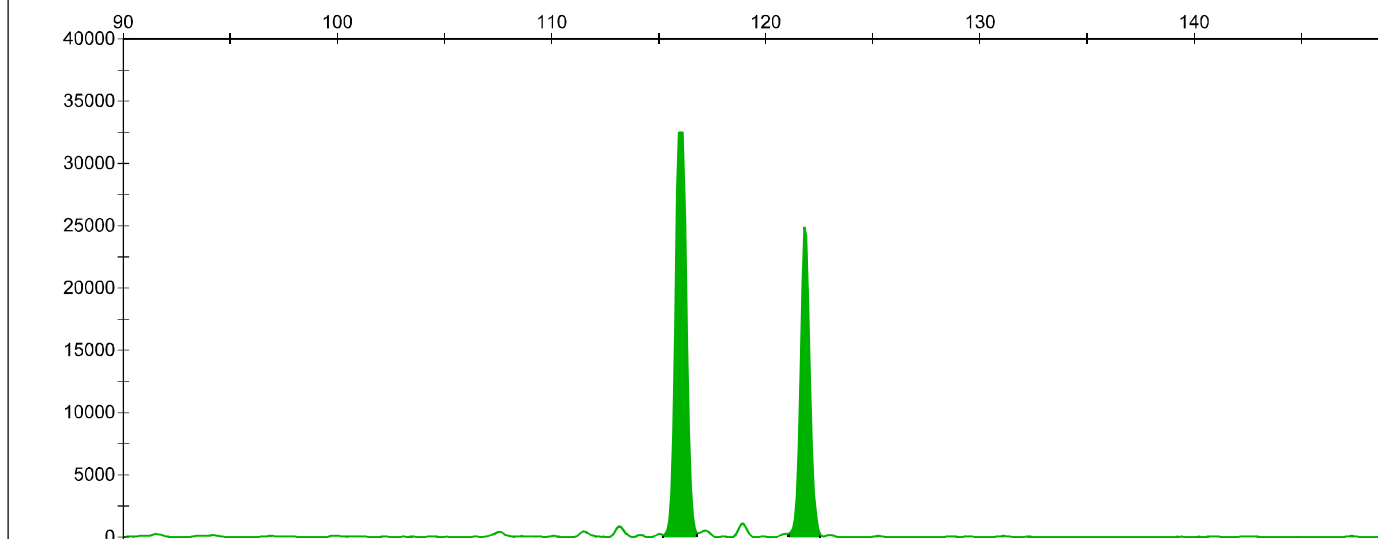
71102731491_(35-7)_E03.fsa	71102731491_(35-7)	None		▲	■
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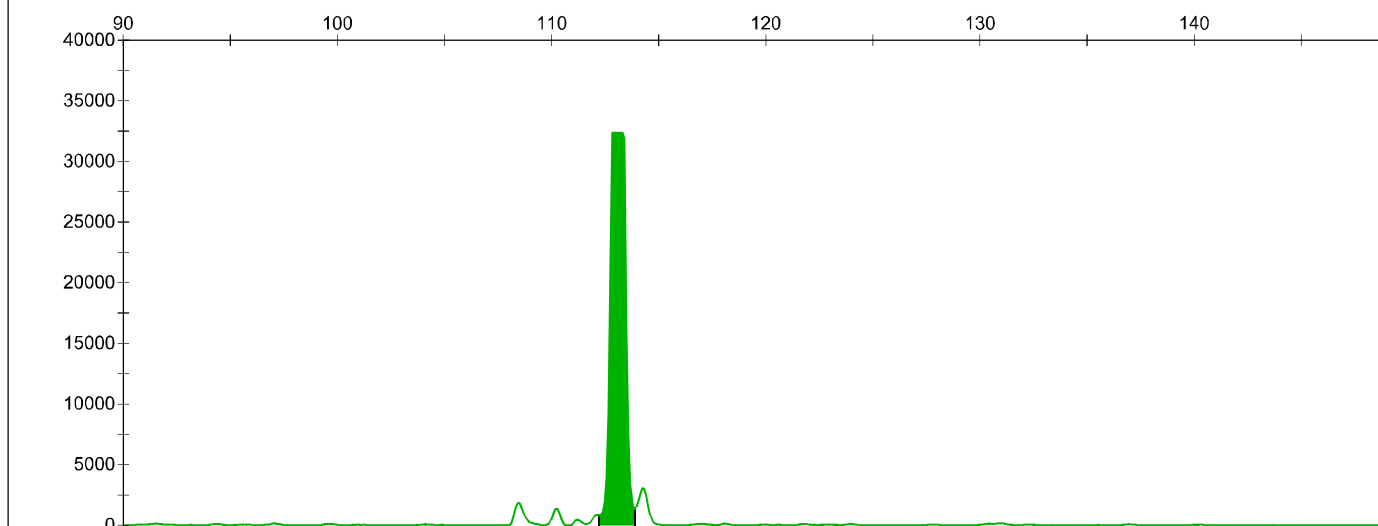
71102731492_(35-9)_F03.fsa	71102731492_(35-9)	None		■	■
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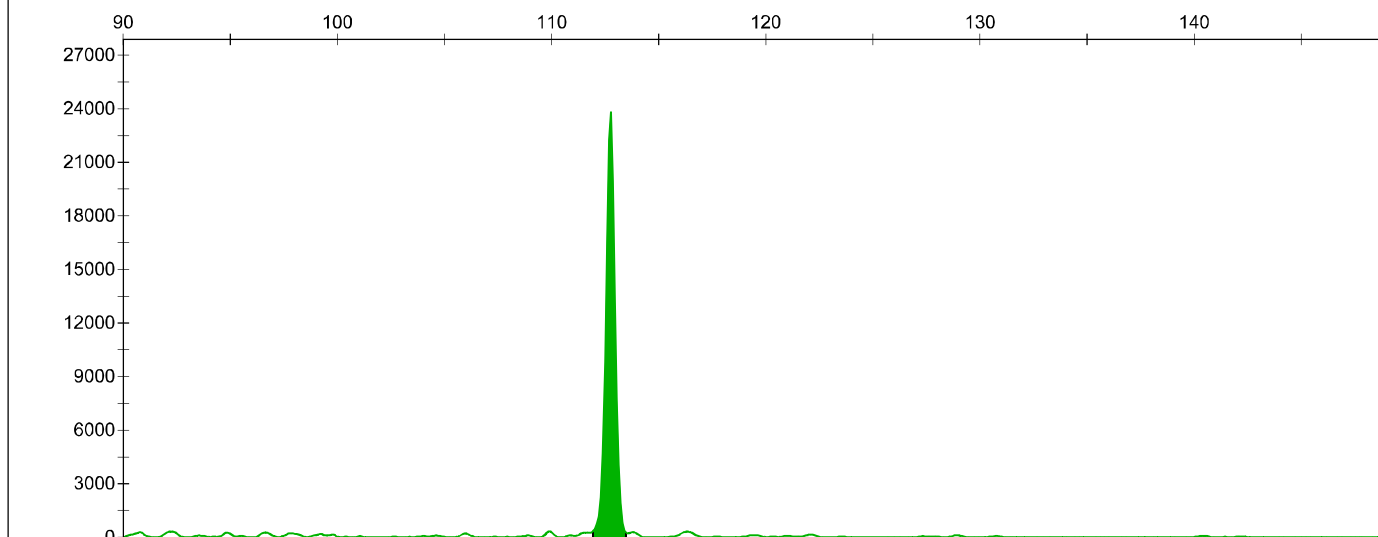
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731493 (35-10) G03.fsa	71102731493 (35-10)	None		▲	■



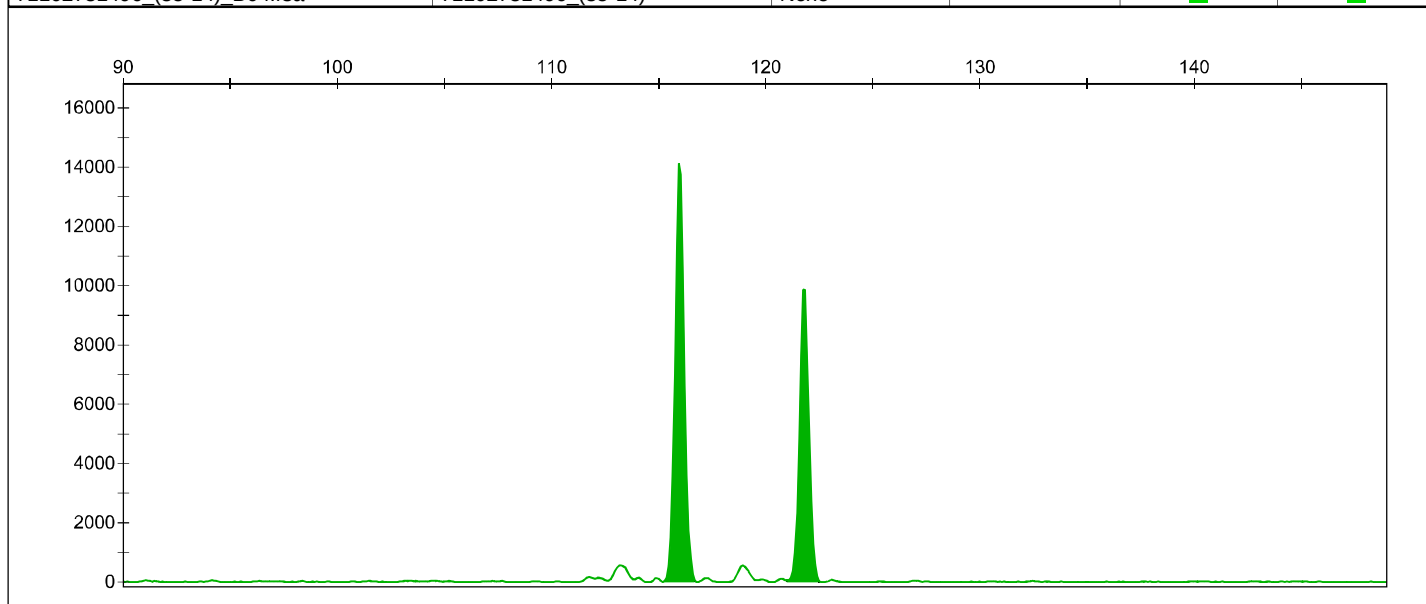
71102731494 (35-12) H03.fsa	71102731494 (35-12)	None		▲	■
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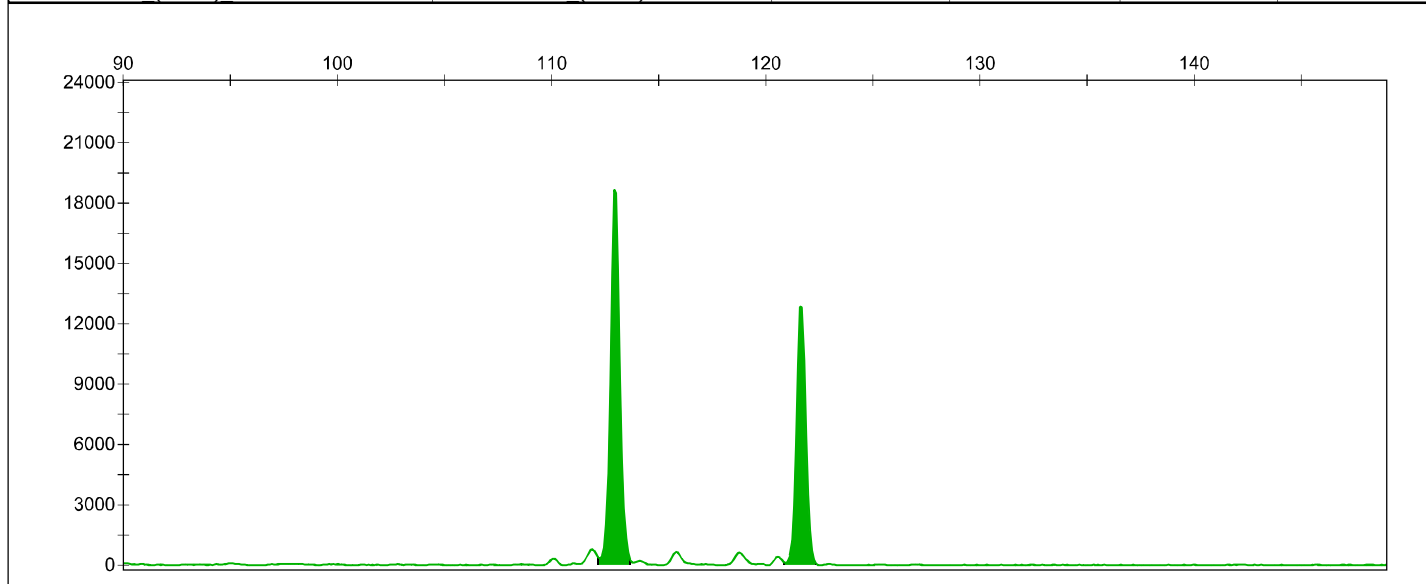
71102731495 (35-13) A04.fsa	71102731495 (35-13)	None		■	■
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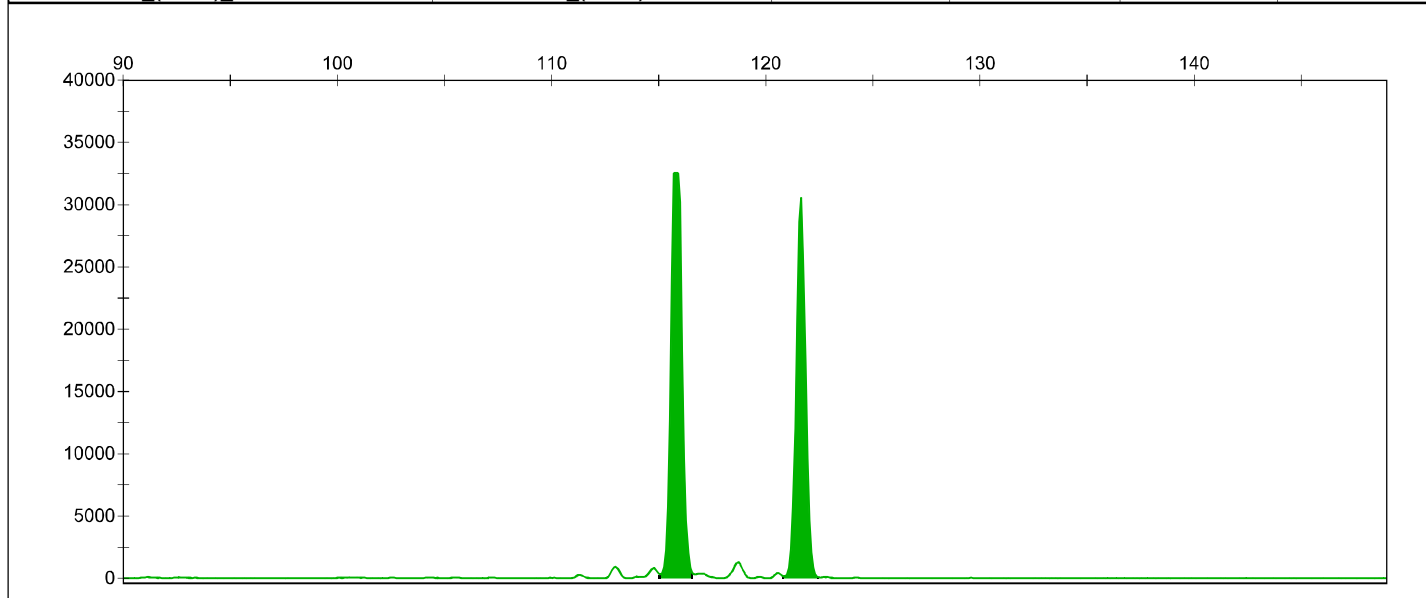
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731496_(35-14)_B04.fsa	71102731496_(35-14)	None		■	■



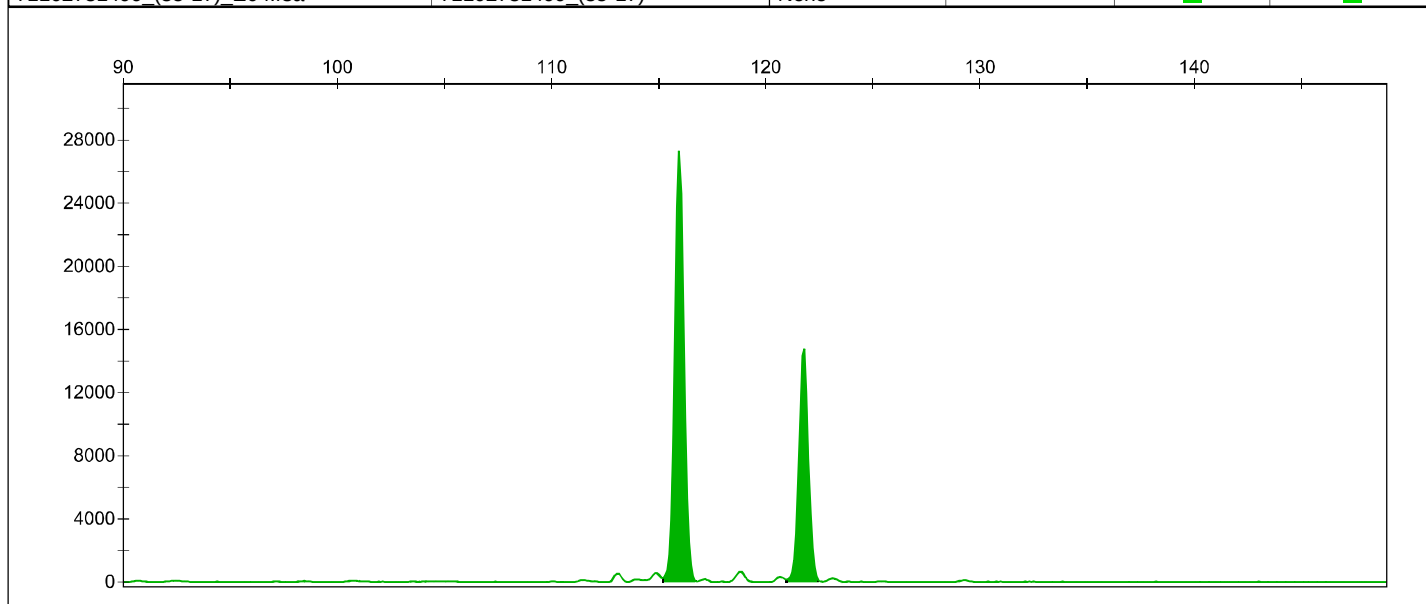
71102731497_(35-15)_C04.fsa	71102731497_(35-15)	None		■	■
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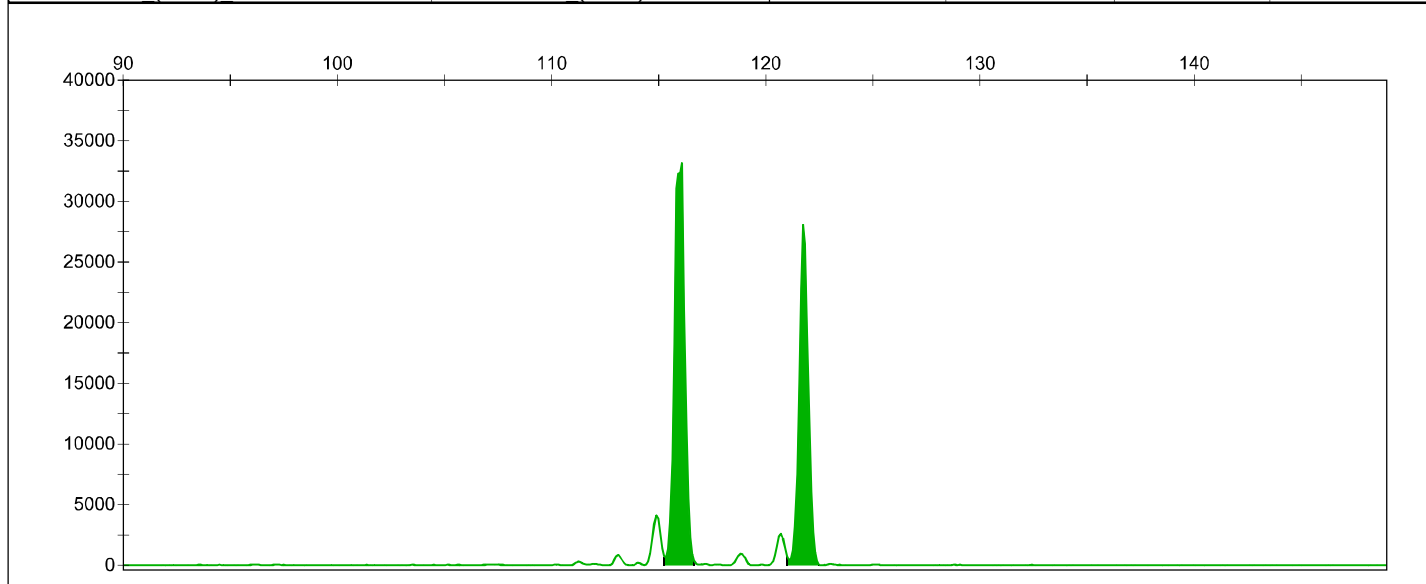
71102731498_(35-16)_D04.fsa	71102731498_(35-16)	None		▲	■
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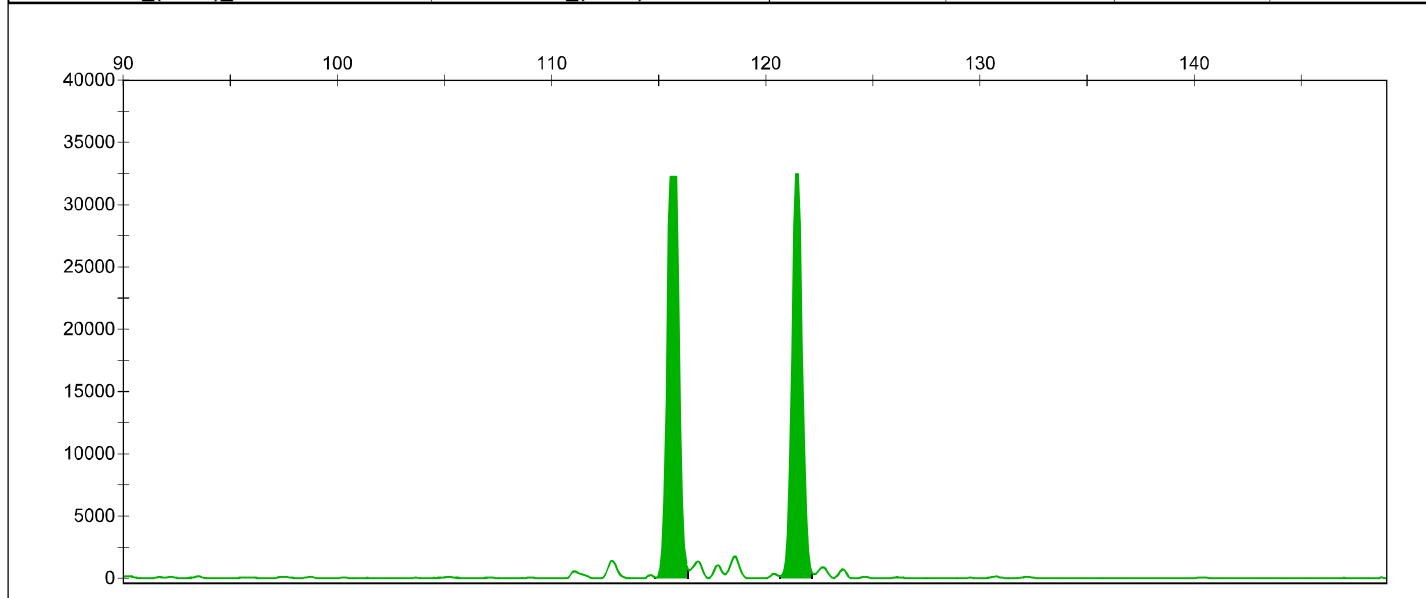
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731499 (35-17) E04.fsa	71102731499 (35-17)	None		■	■



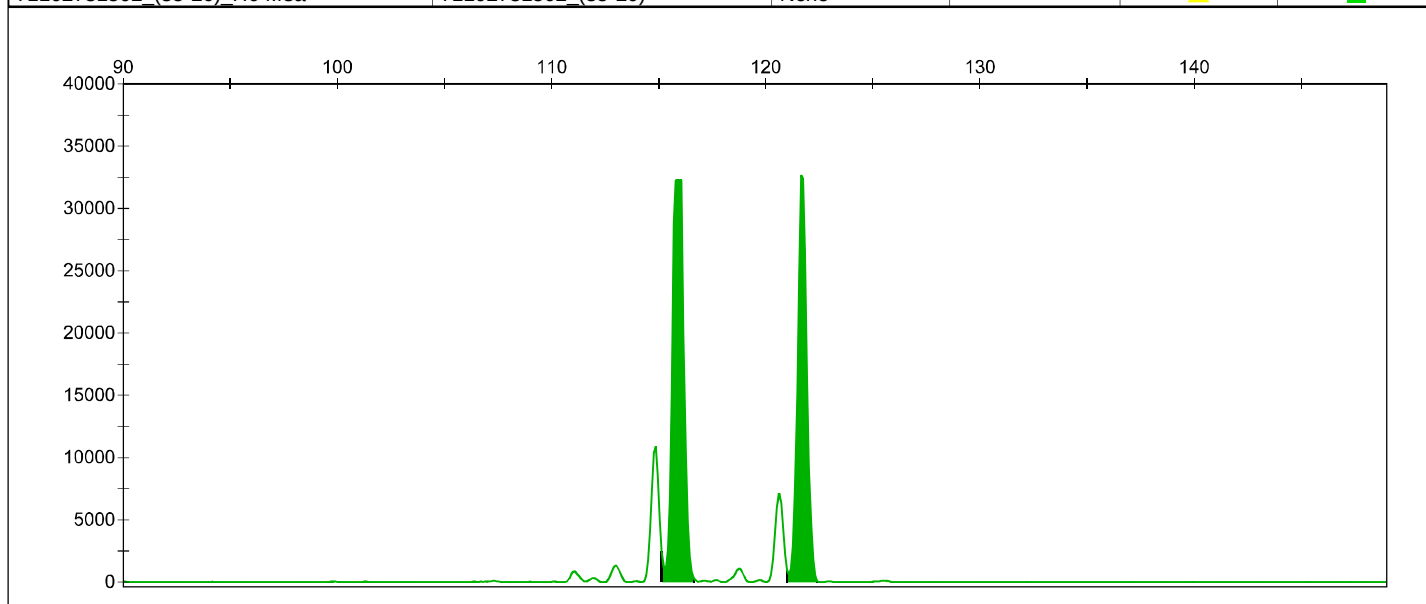
71102731500 (35-18) F04.fsa	71102731500 (35-18)	None		▲	■
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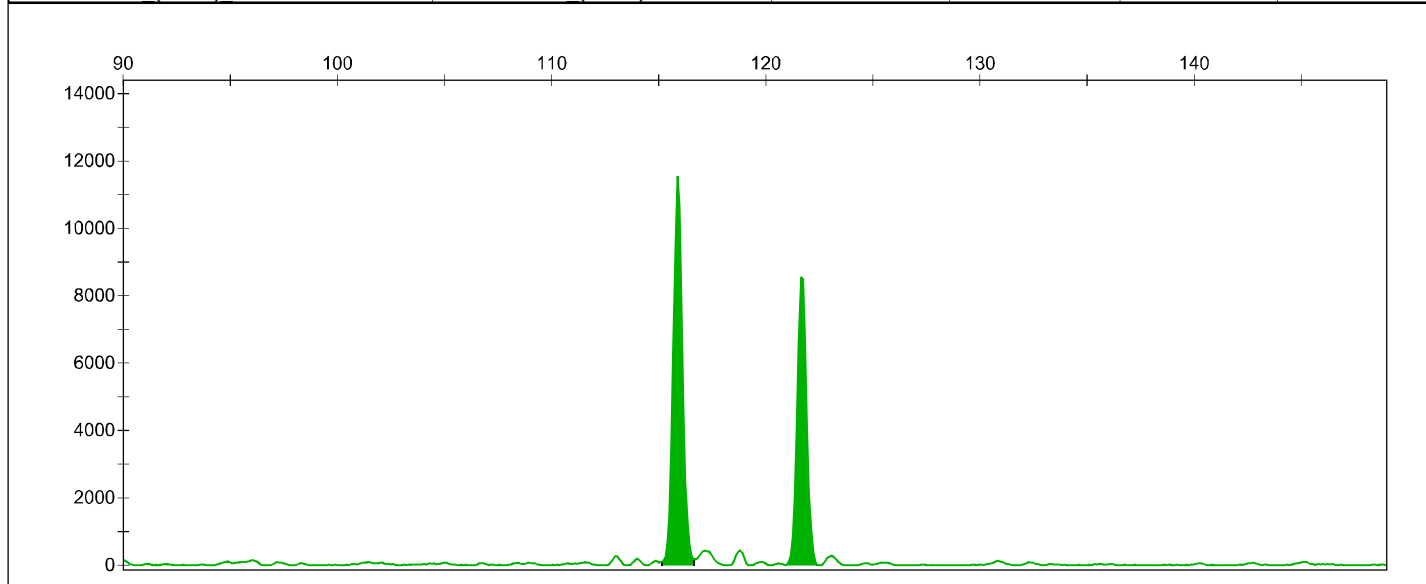
71102731501 (35-19) G04.fsa	71102731501 (35-19)	None		▲	■
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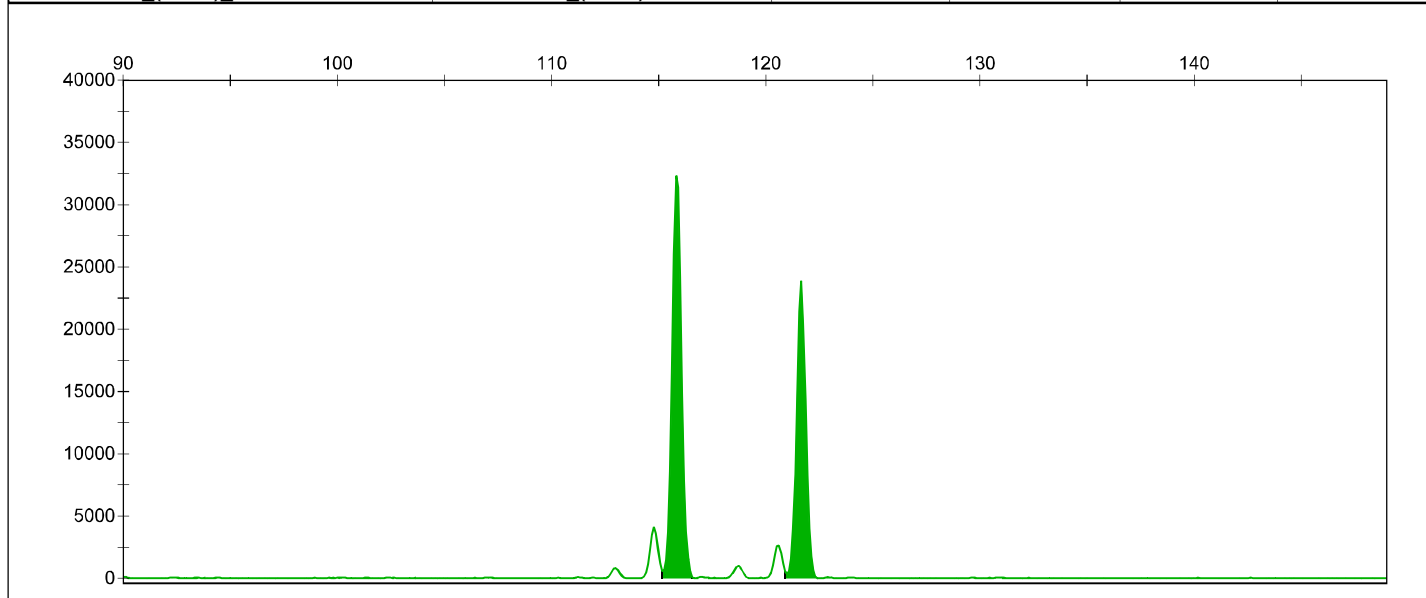
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731502_(35-20)_H04.fsa	71102731502_(35-20)	None		▲	■



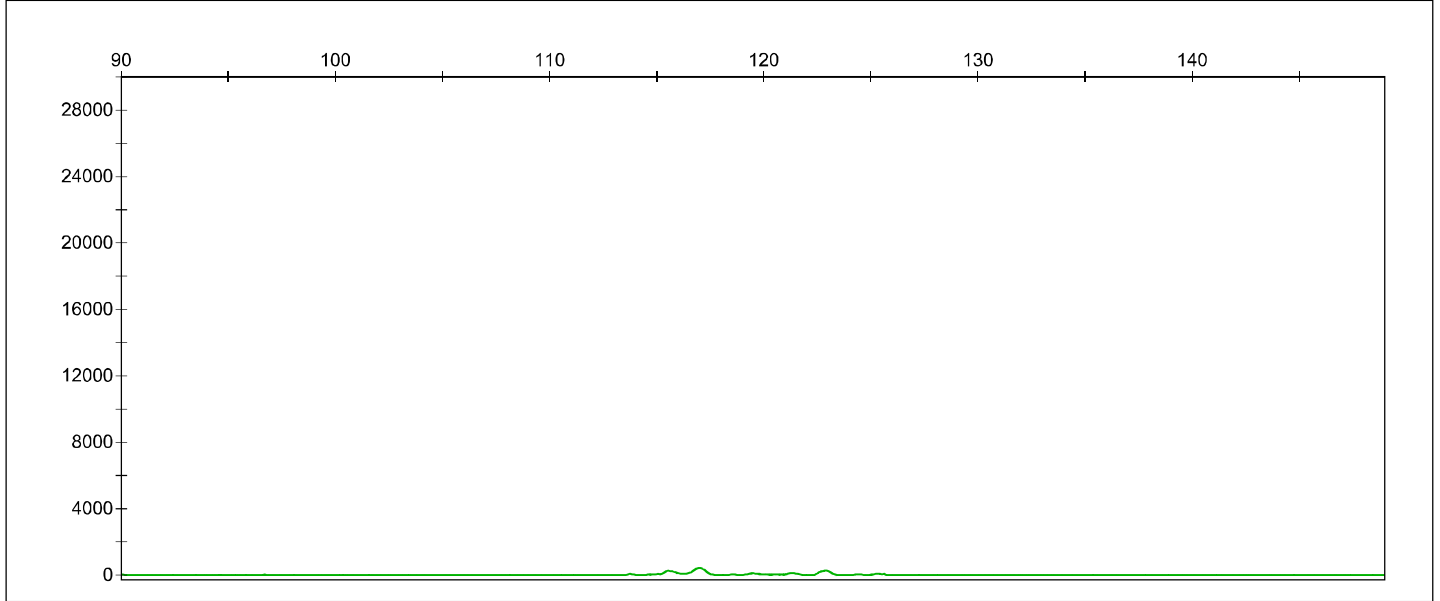
71102731503_(35-21)_A05.fsa	71102731503_(35-21)	None		■	■
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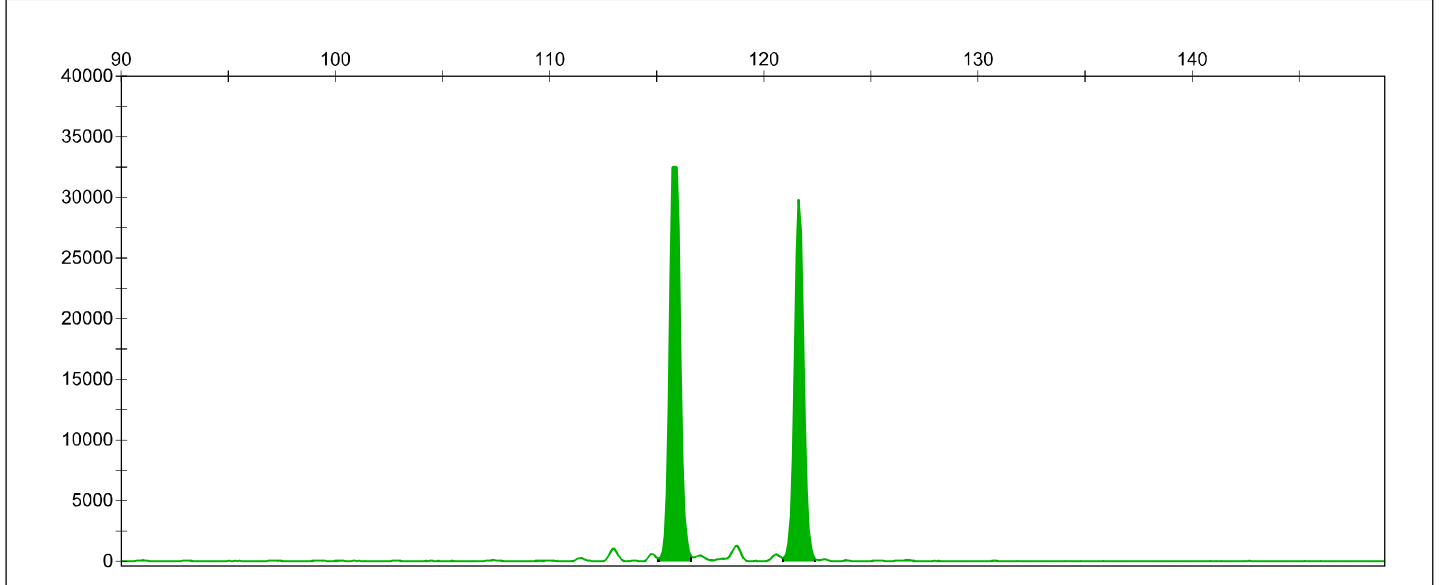
71102731504_(35-22)_B05.fsa	71102731504_(35-22)	None		■	■
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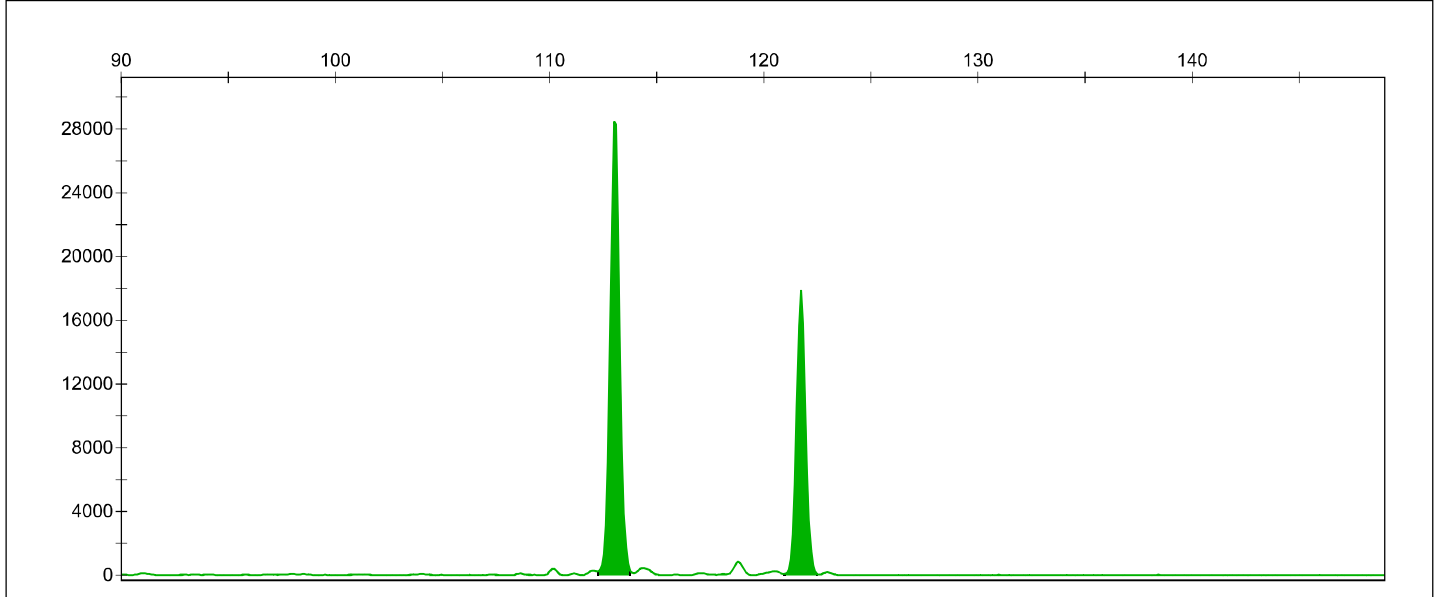
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731505_(35-23)_C05.fsa	71102731505_(35-23)	None		■	■



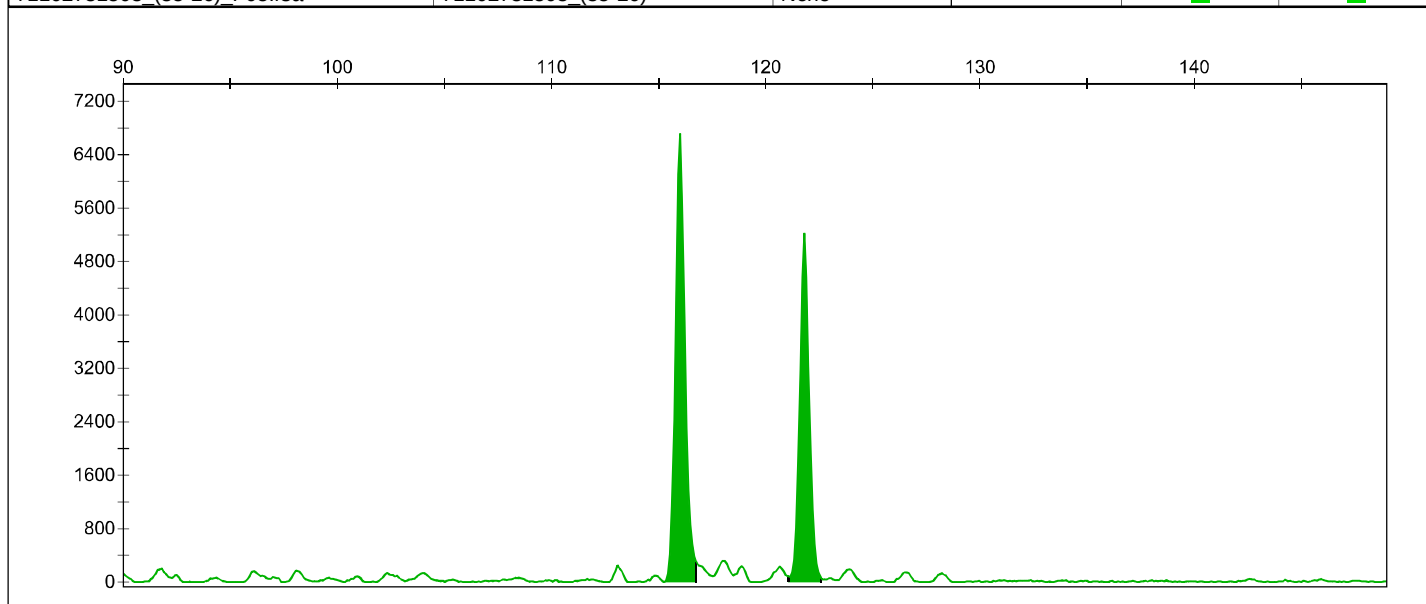
71102731506_(35-24)_D05.fsa	71102731506_(35-24)	None		▲	■
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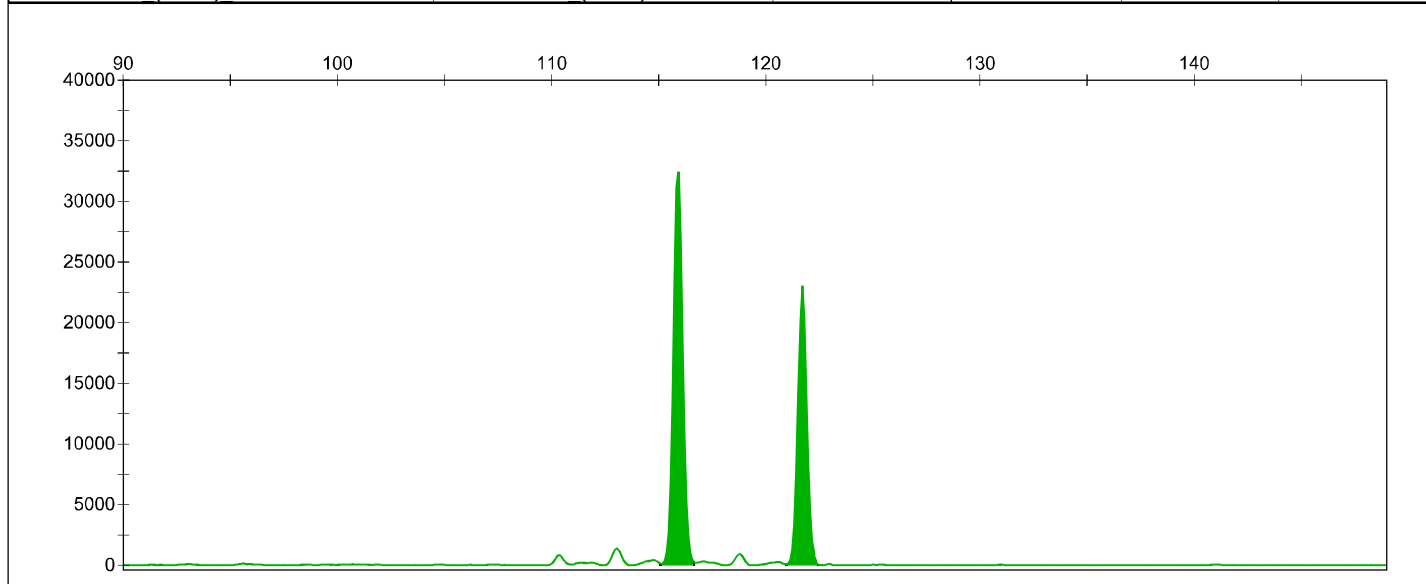
71102731507_(35-25)_E05.fsa	71102731507_(35-25)	None		■	■
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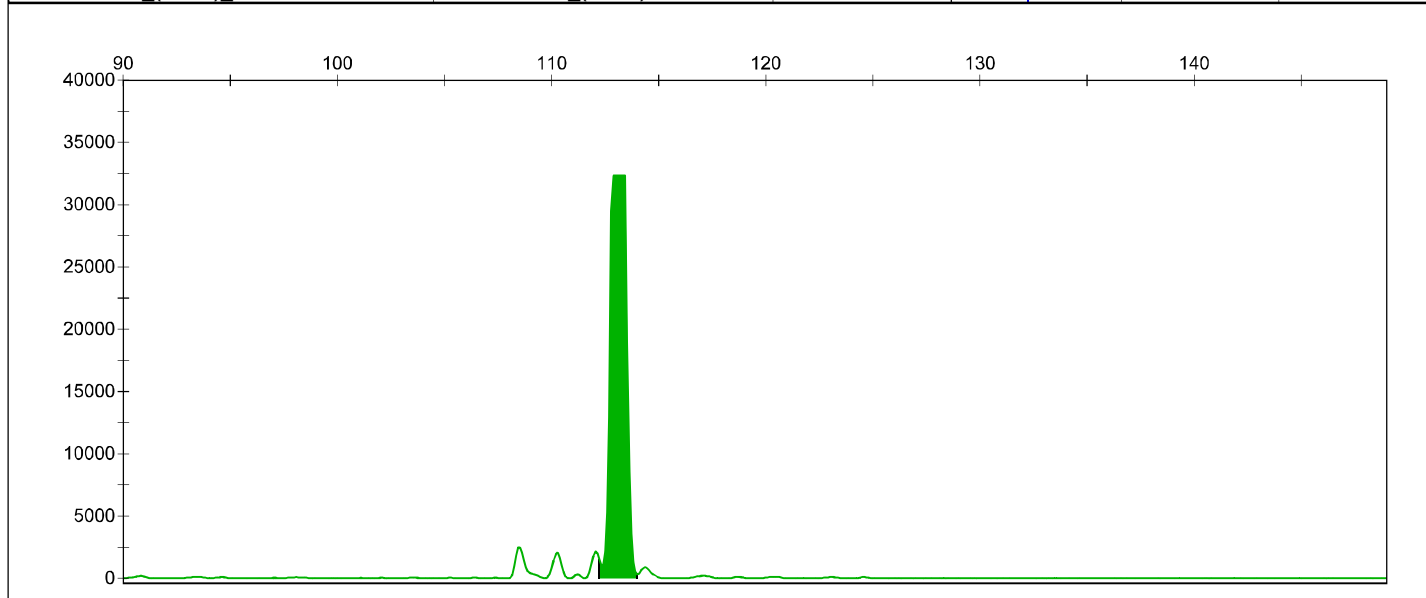
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731508 (35-26) F05.fsa	71102731508 (35-26)	None		■	■



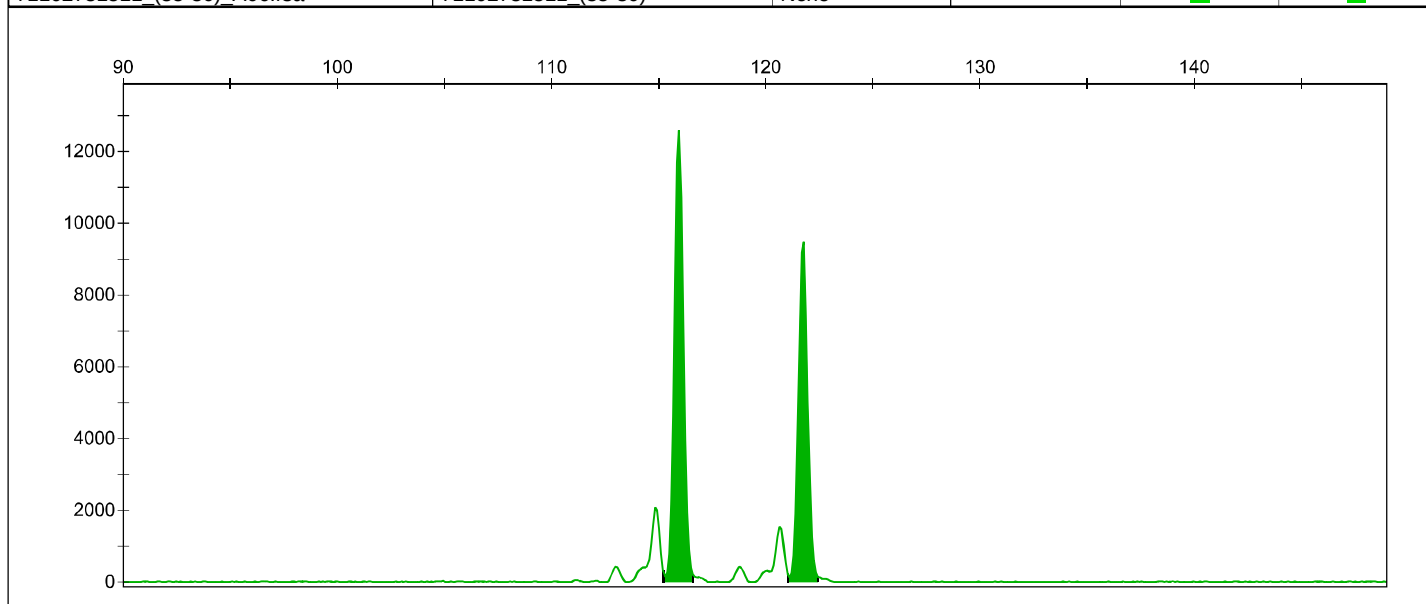
71102731509 (35-27) G05.fsa	71102731509 (35-27)	None		■	■
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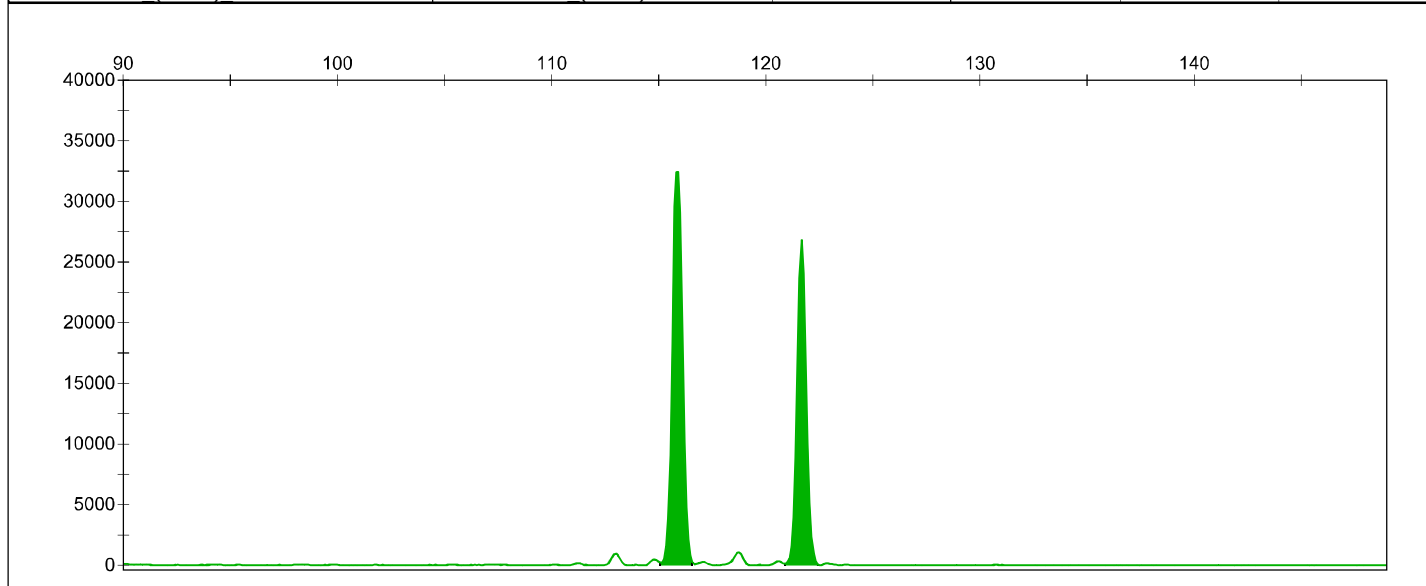
71102731510 (35-29) H05.fsa	71102731510 (35-29)	None	✕	▲	■
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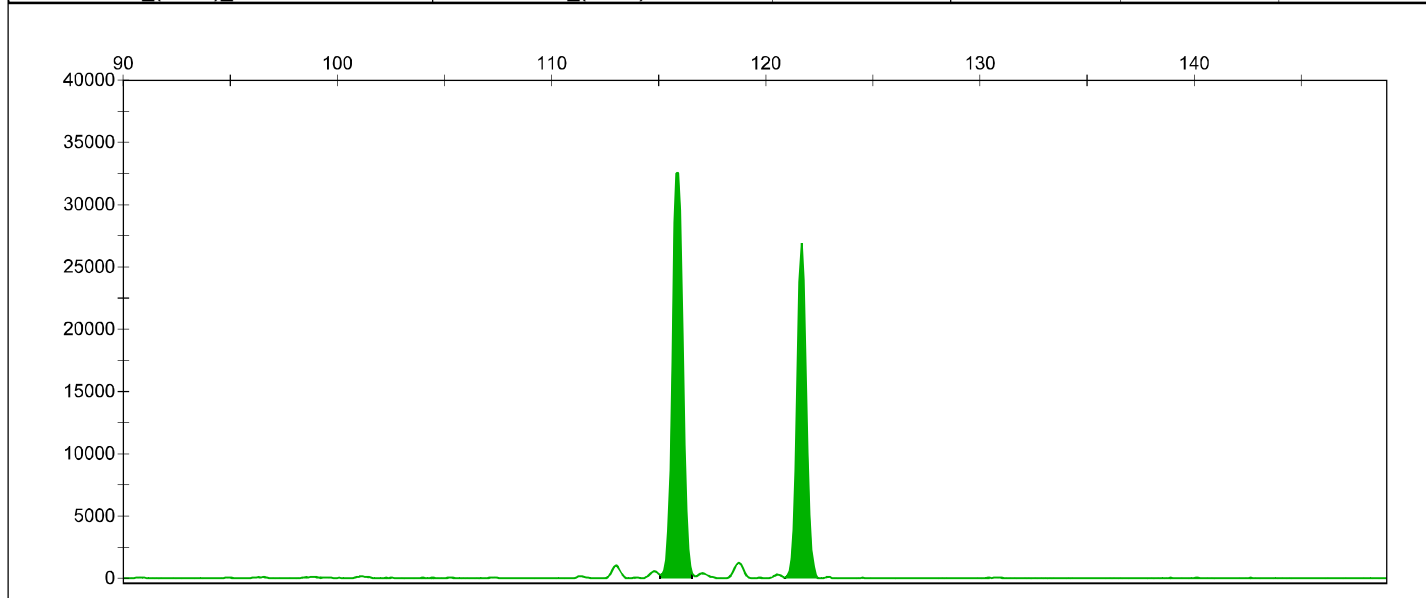
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731511_(35-30)_A06.fsa	71102731511_(35-30)	None		■	■



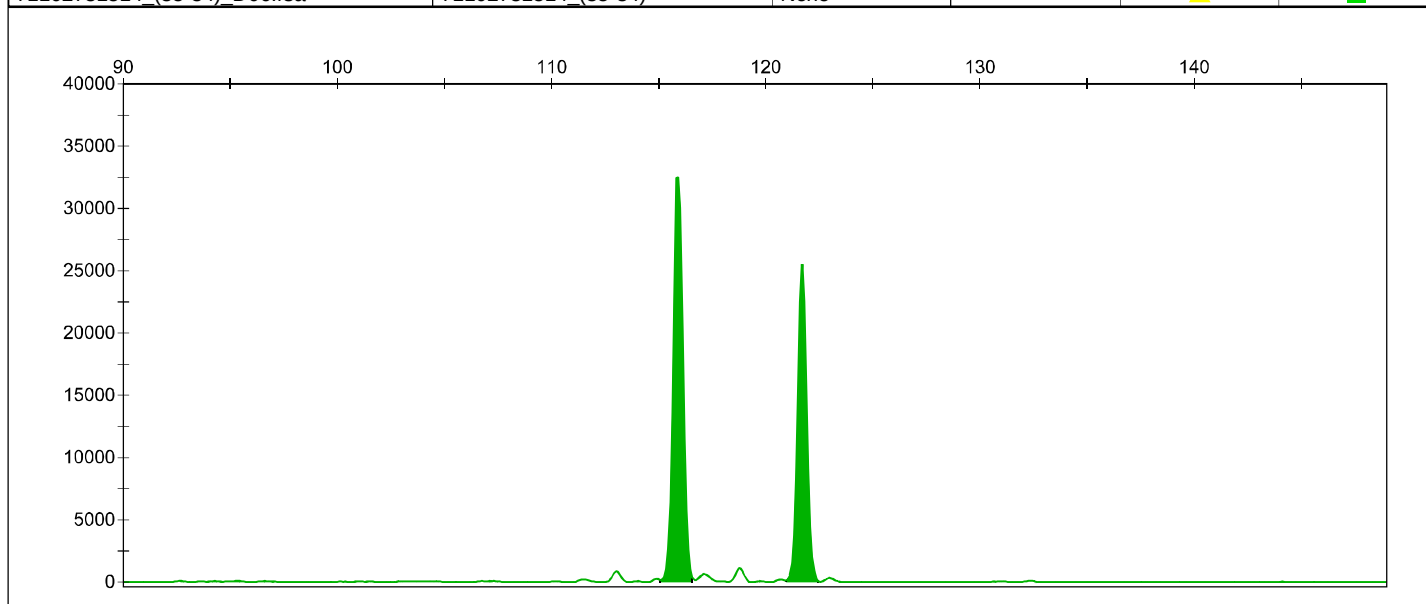
71102731512_(35-31)_B06.fsa	71102731512_(35-31)	None		▲	■
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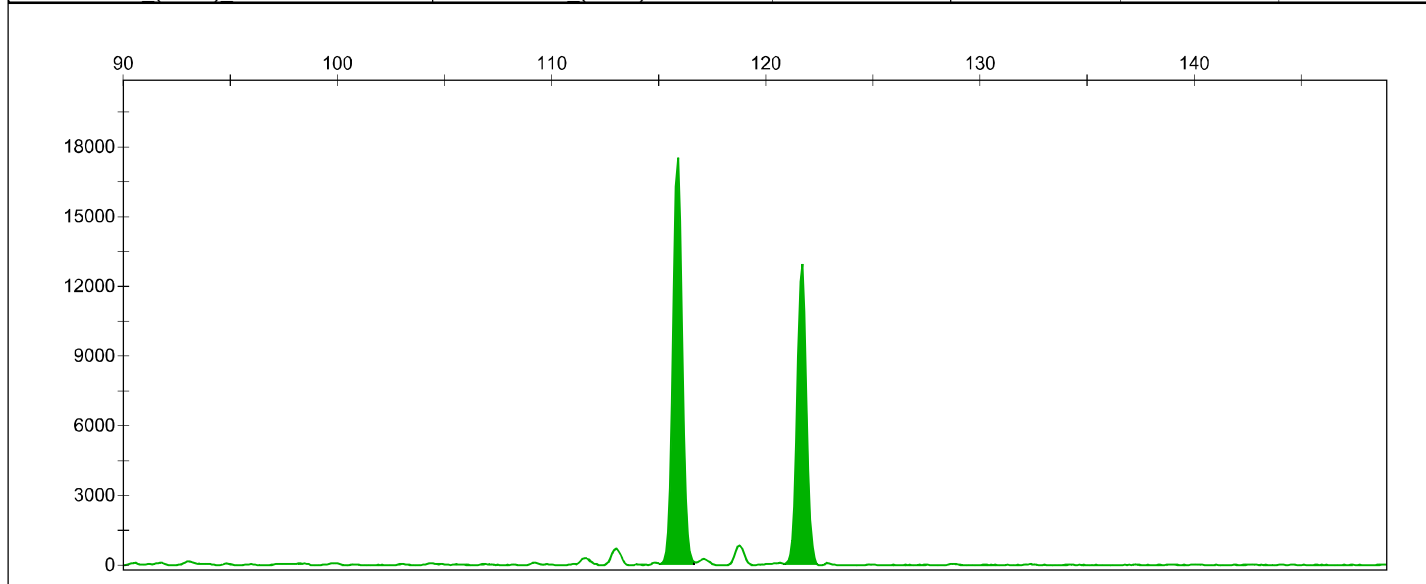
71102731513_(35-33)_C06.fsa	71102731513_(35-33)	None		▲	■
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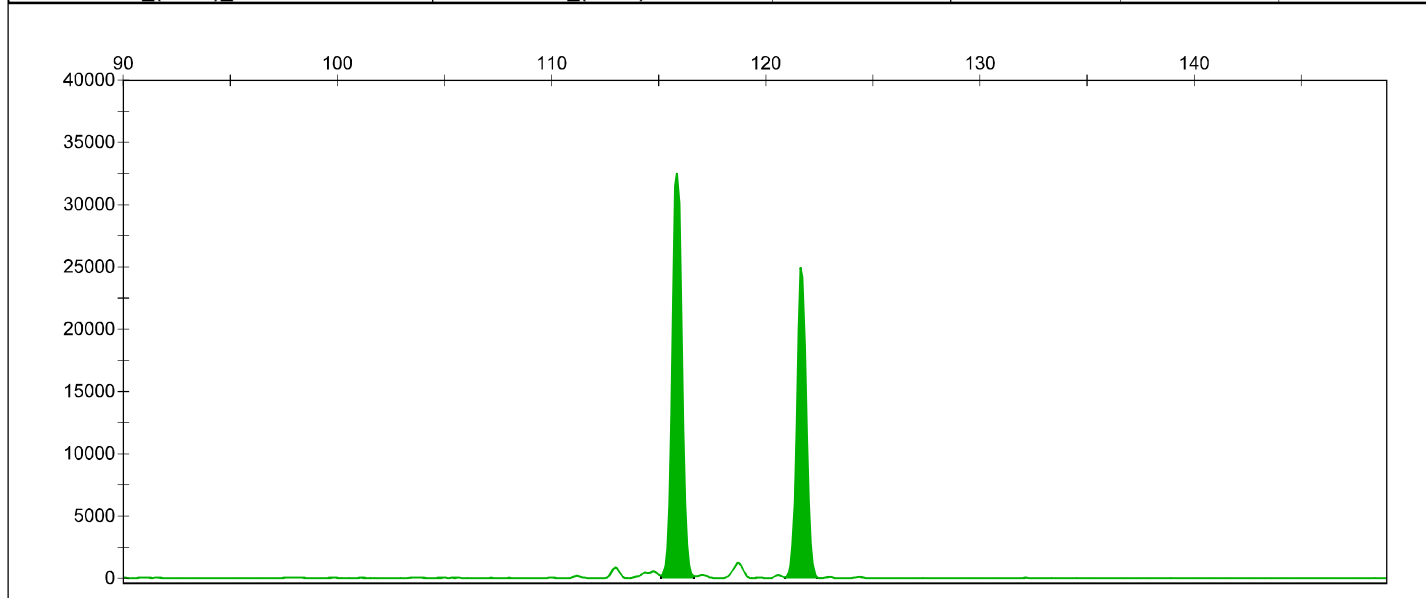
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731514_(35-34)_D06.fsa	71102731514_(35-34)	None		▲	■



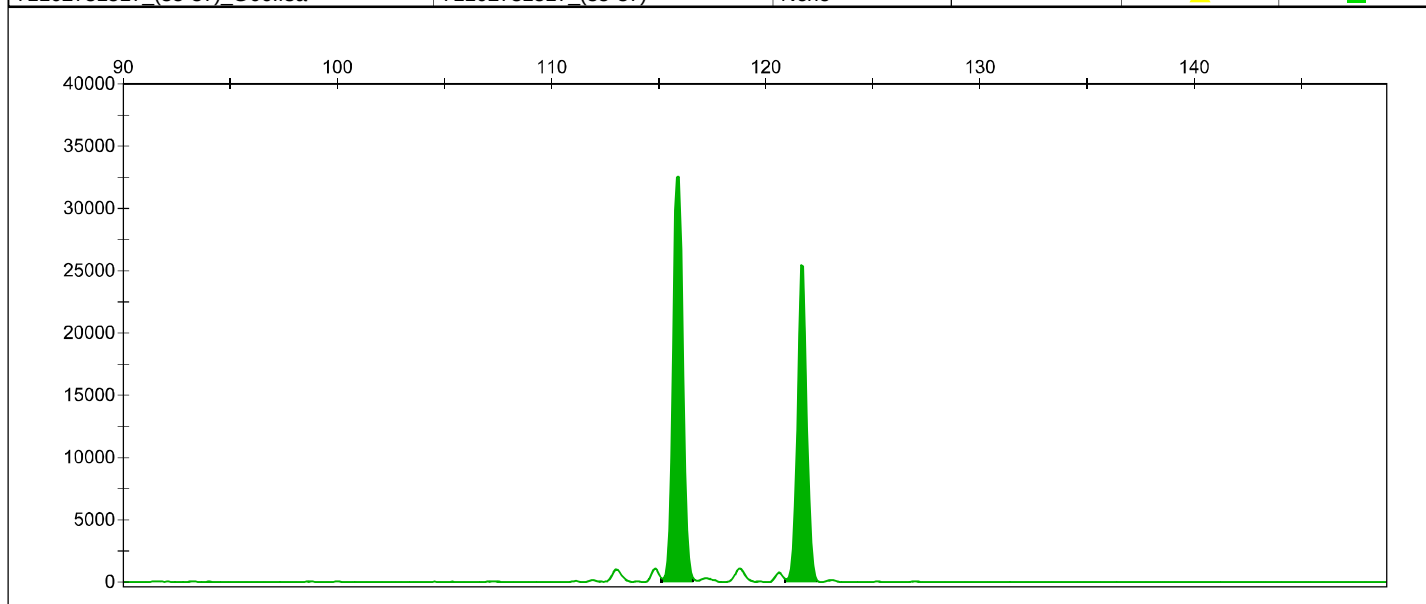
71102731515_(35-35)_E06.fsa	71102731515_(35-35)	None		■	■
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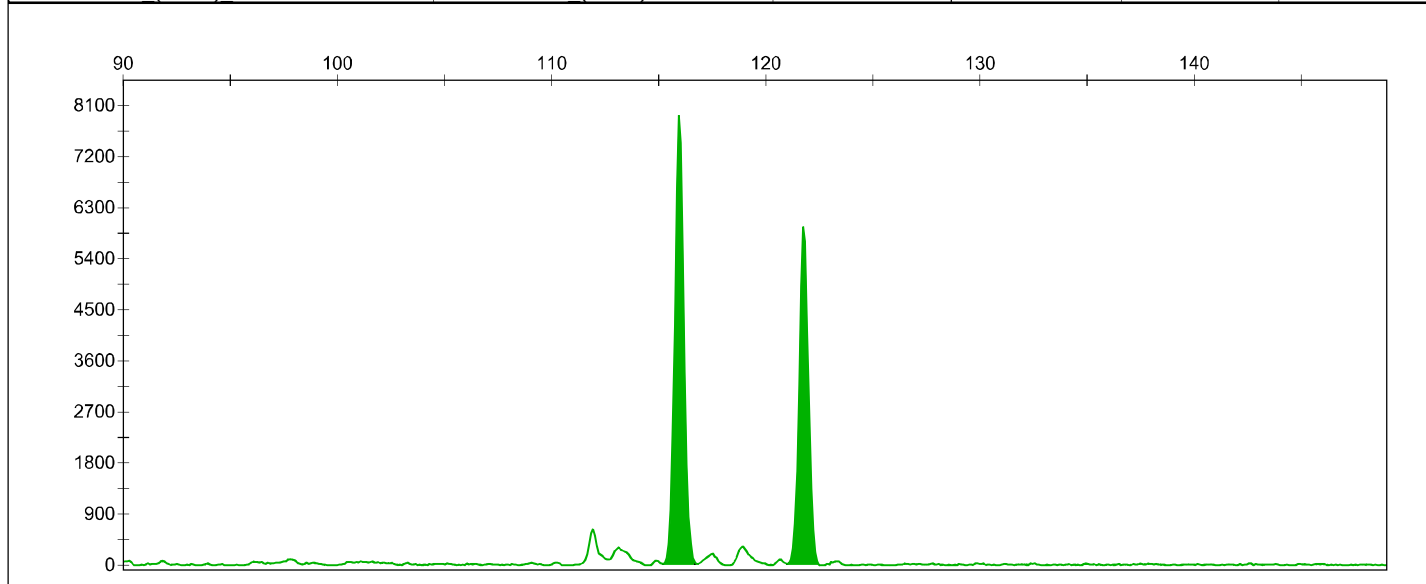
71102731516_(35-36)_F06.fsa	71102731516_(35-36)	None		▲	■
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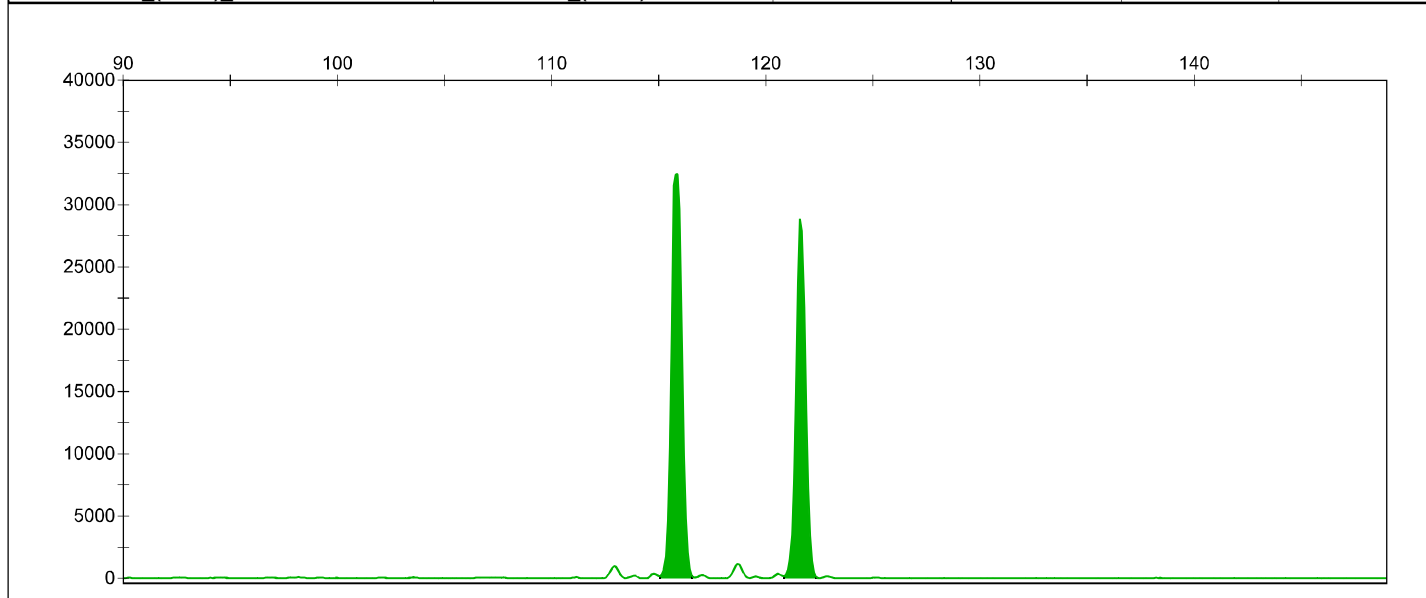
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731517_(35-37)_G06.fsa	71102731517_(35-37)	None		▲	■



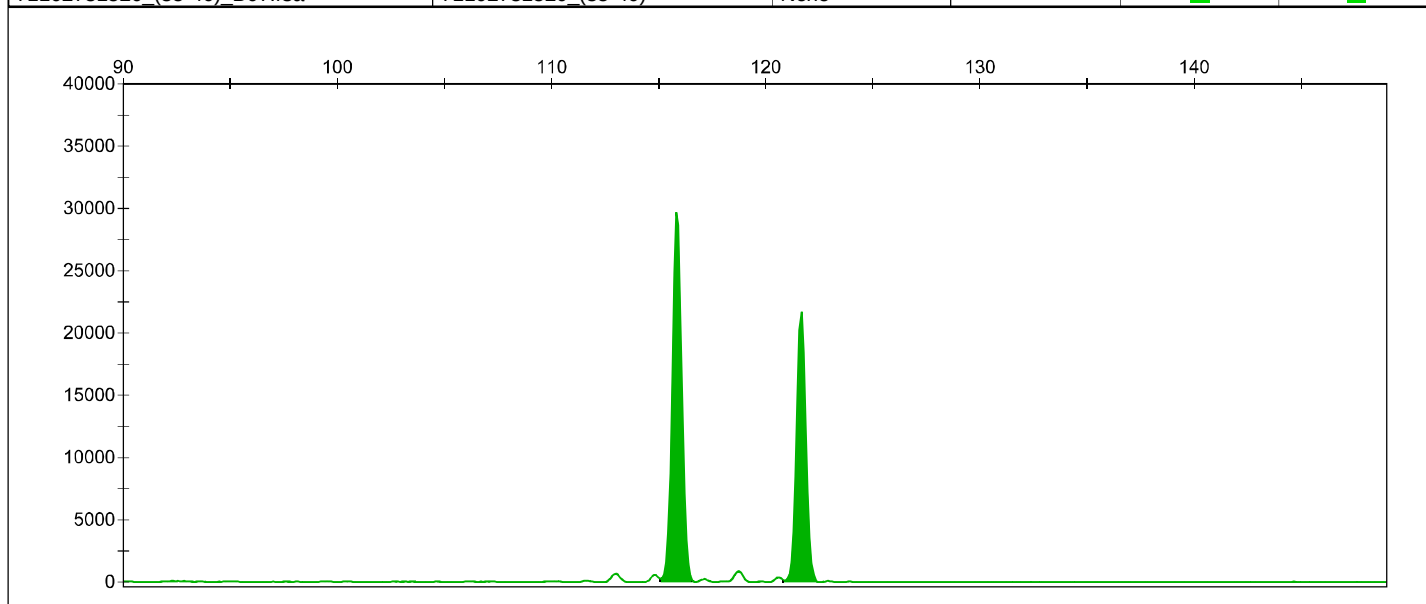
71102731518_(35-38)_H06.fsa	71102731518_(35-38)	None		■	■
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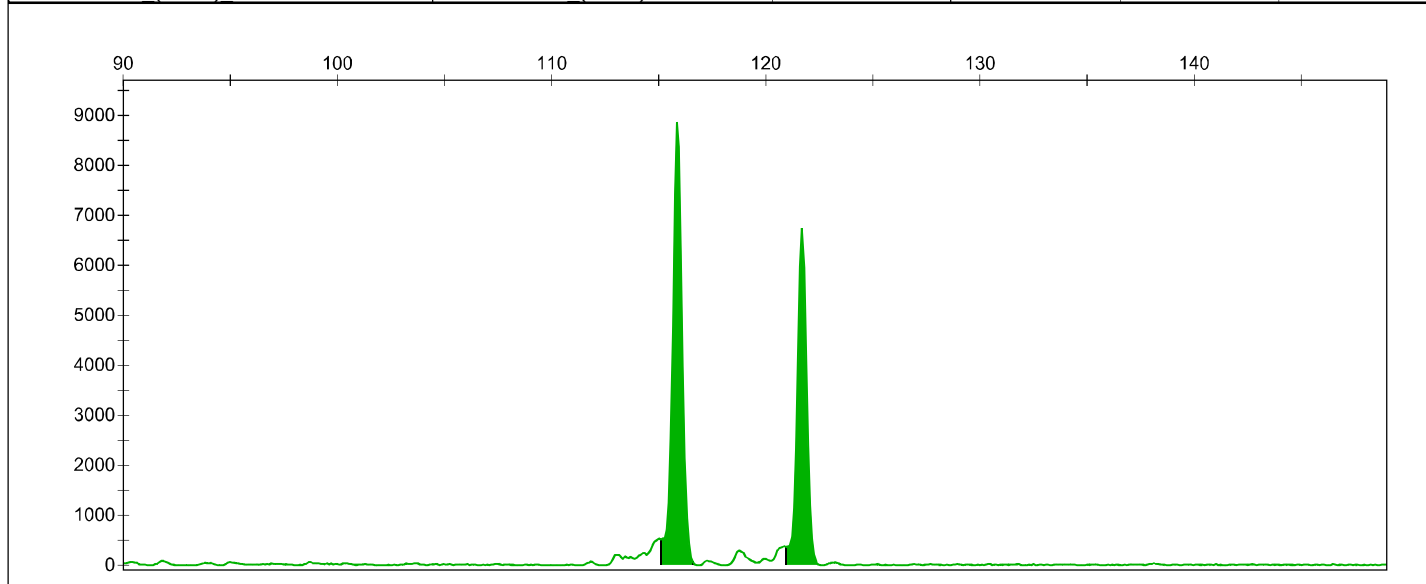
71102731519_(35-39)_A07.fsa	71102731519_(35-39)	None		▲	■
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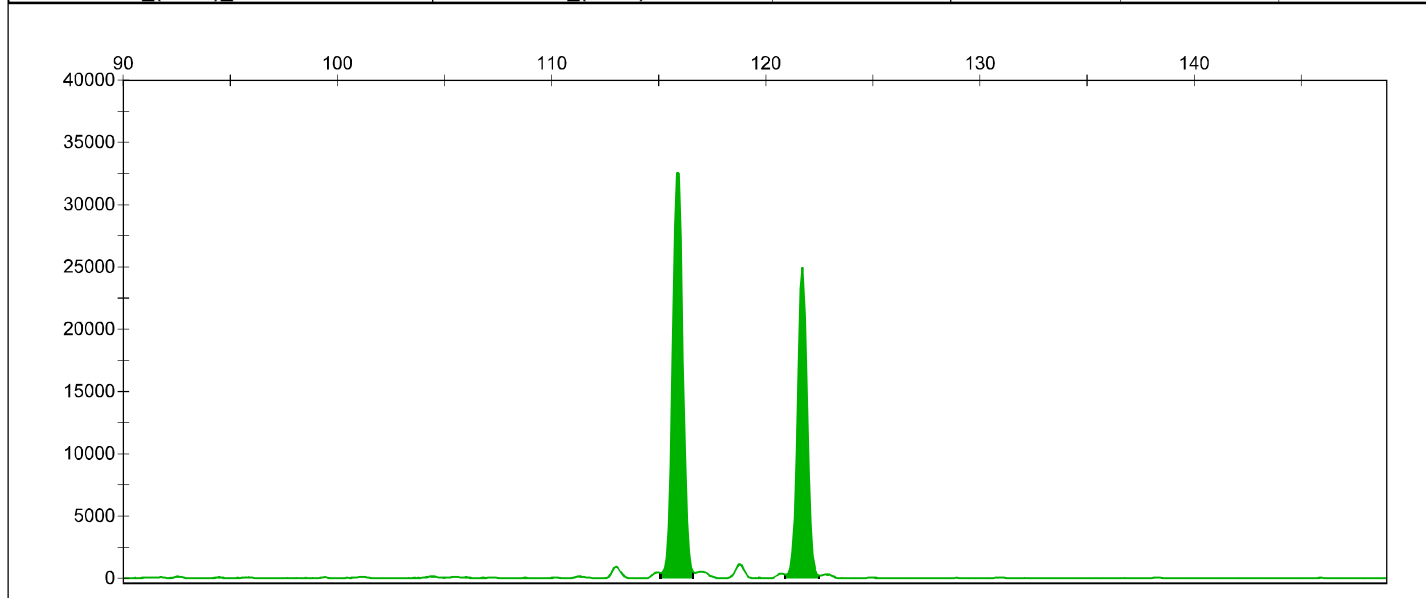
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731520_(35-40)_B07.fsa	71102731520_(35-40)	None		■	■



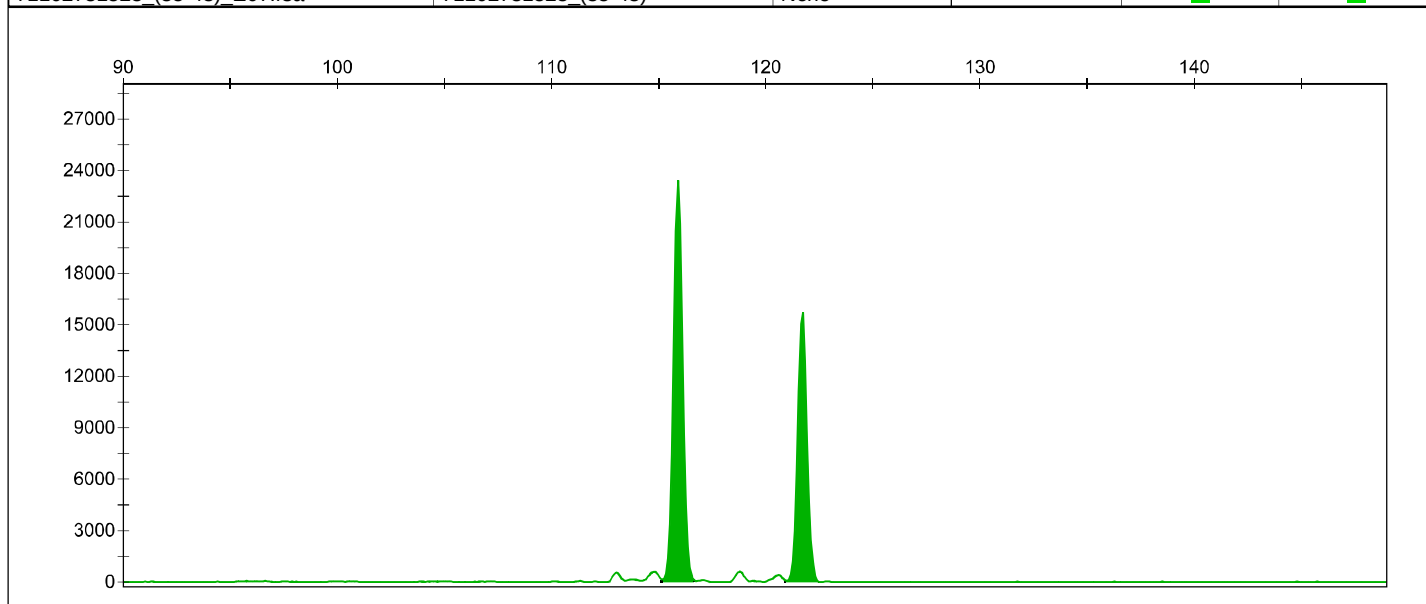
71102731521_(35-41)_C07.fsa	71102731521_(35-41)	None		■	■
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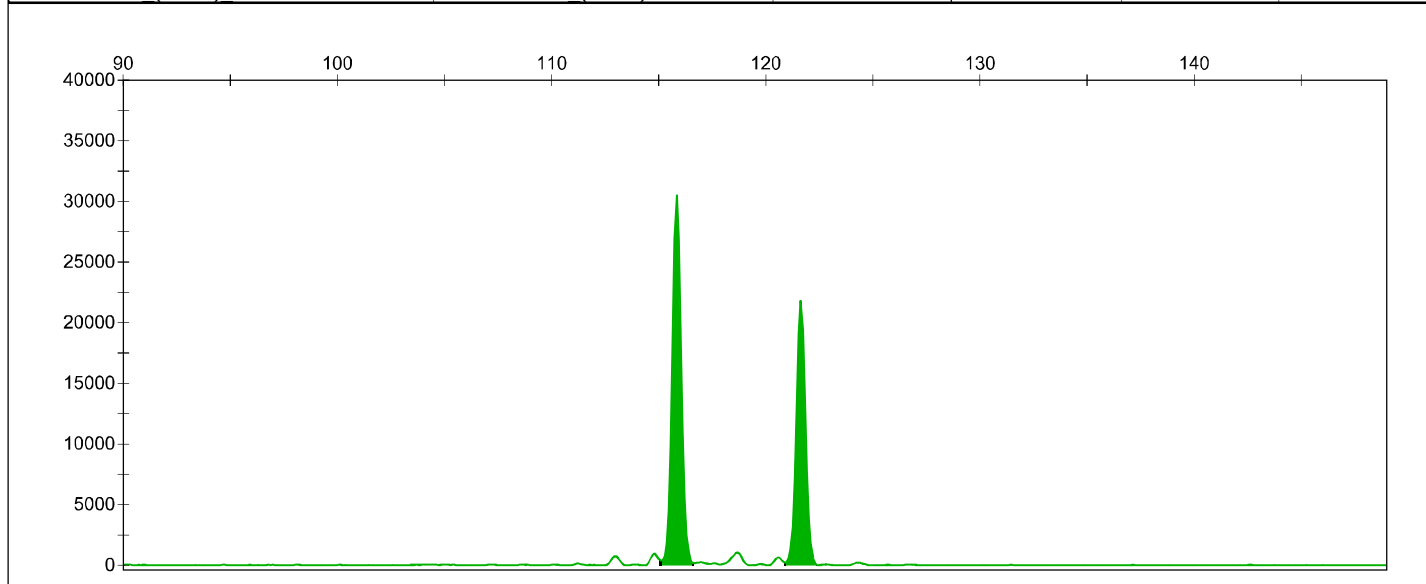
71102731522_(35-42)_D07.fsa	71102731522_(35-42)	None		▲	■
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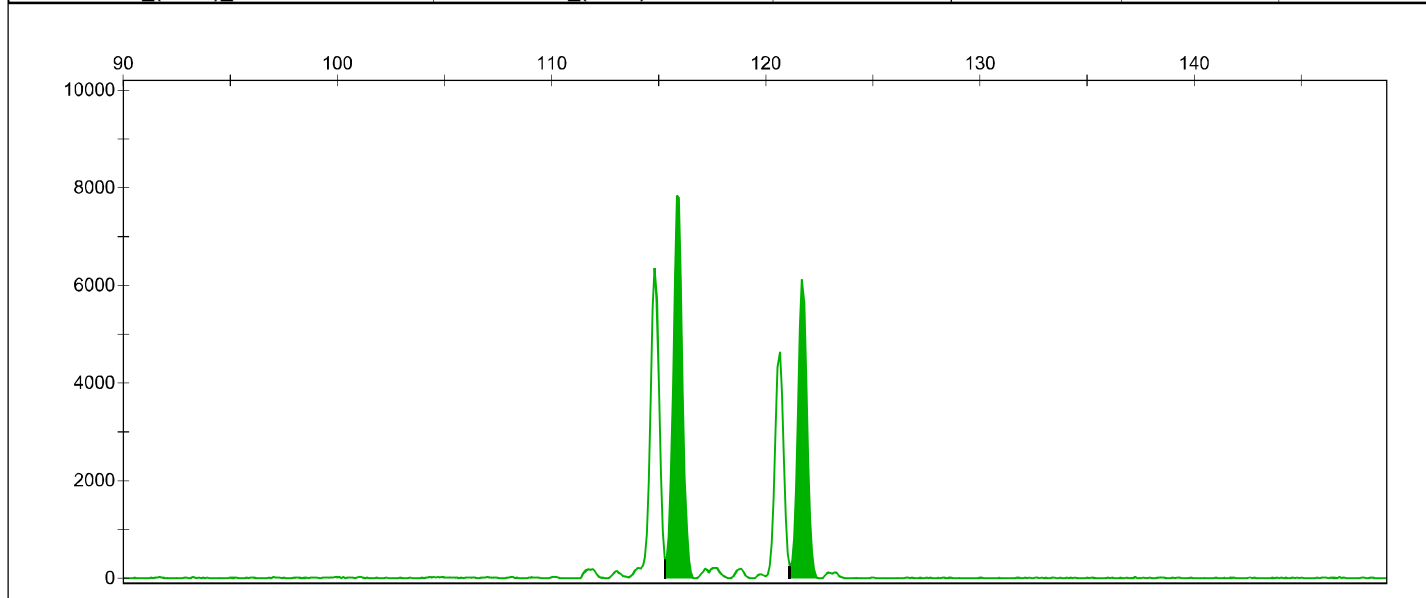
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731523 (35-43) E07.fsa	71102731523 (35-43)	None		■	■



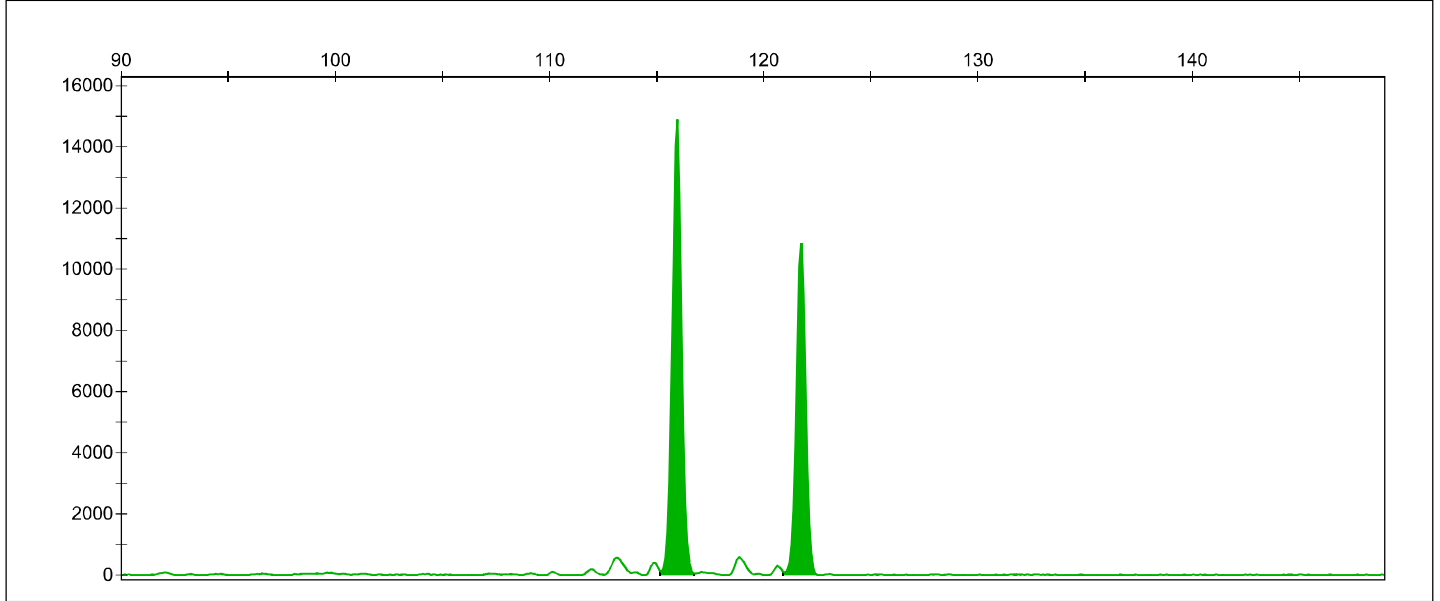
71102731524 (35-45) F07.fsa	71102731524 (35-45)	None		■	■
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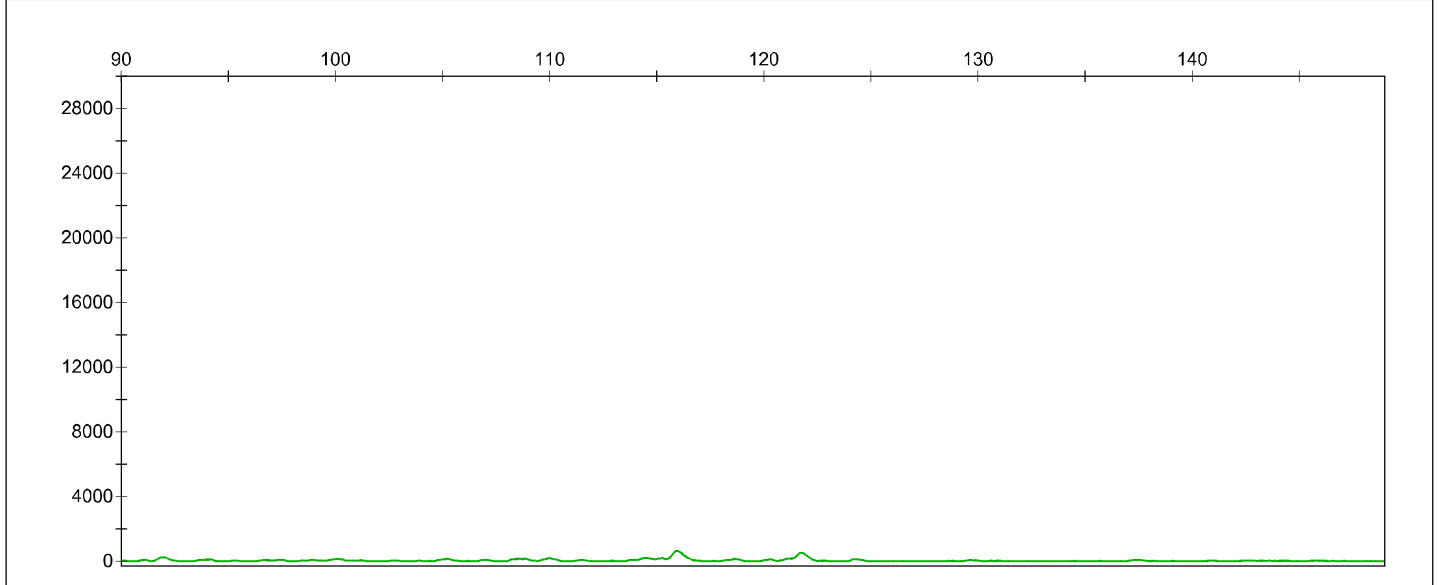
71102731525 (35-46) G07.fsa	71102731525 (35-46)	None		■	■
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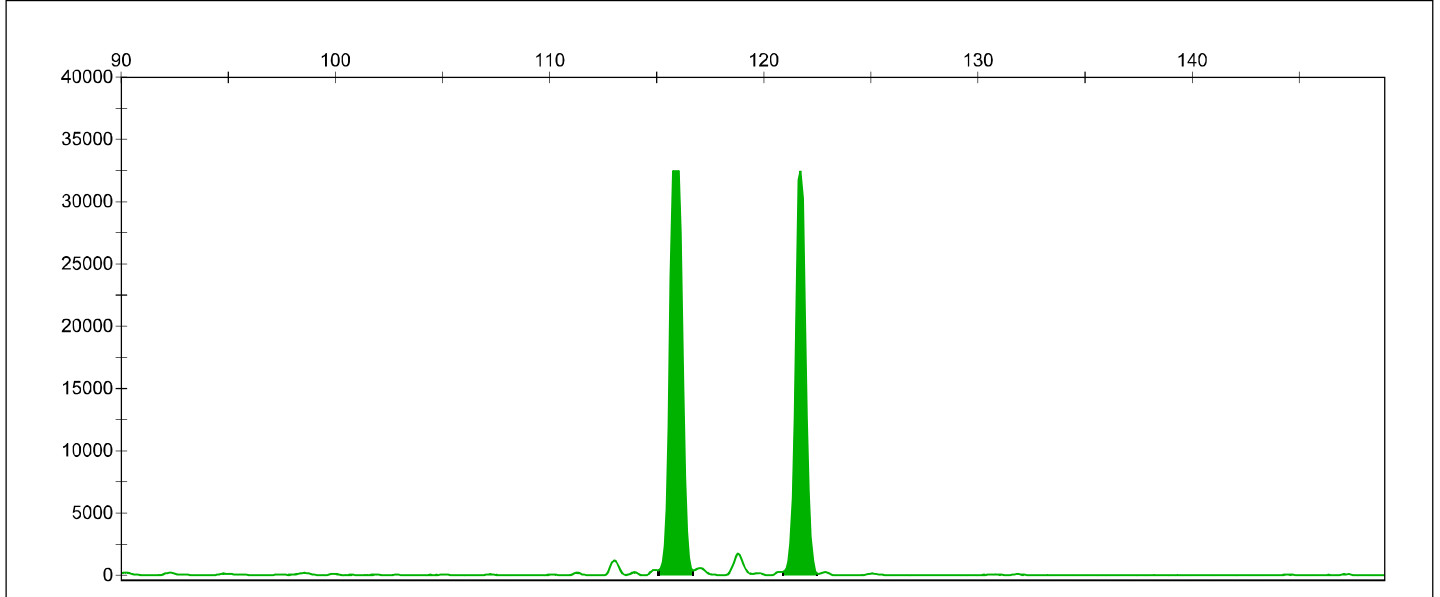
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731526_(35-47)_H07.fsa	71102731526_(35-47)	None		■	■



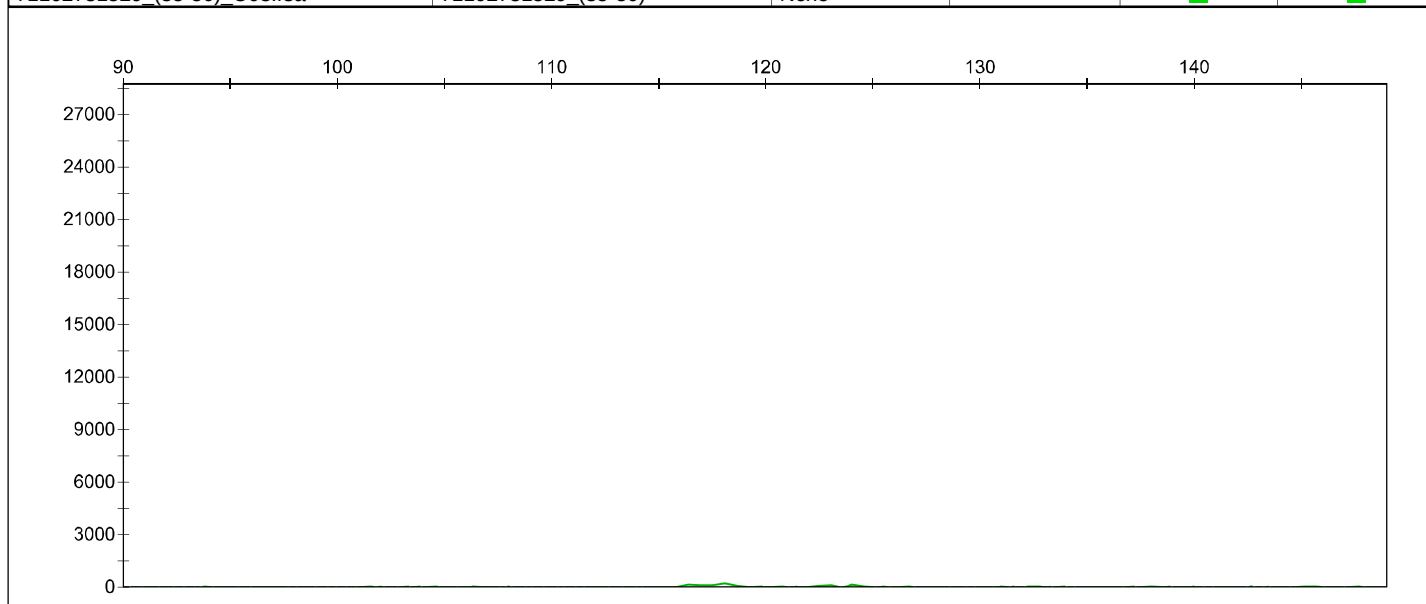
71102731527_(35-48)_A08.fsa	71102731527_(35-48)	None		■	■
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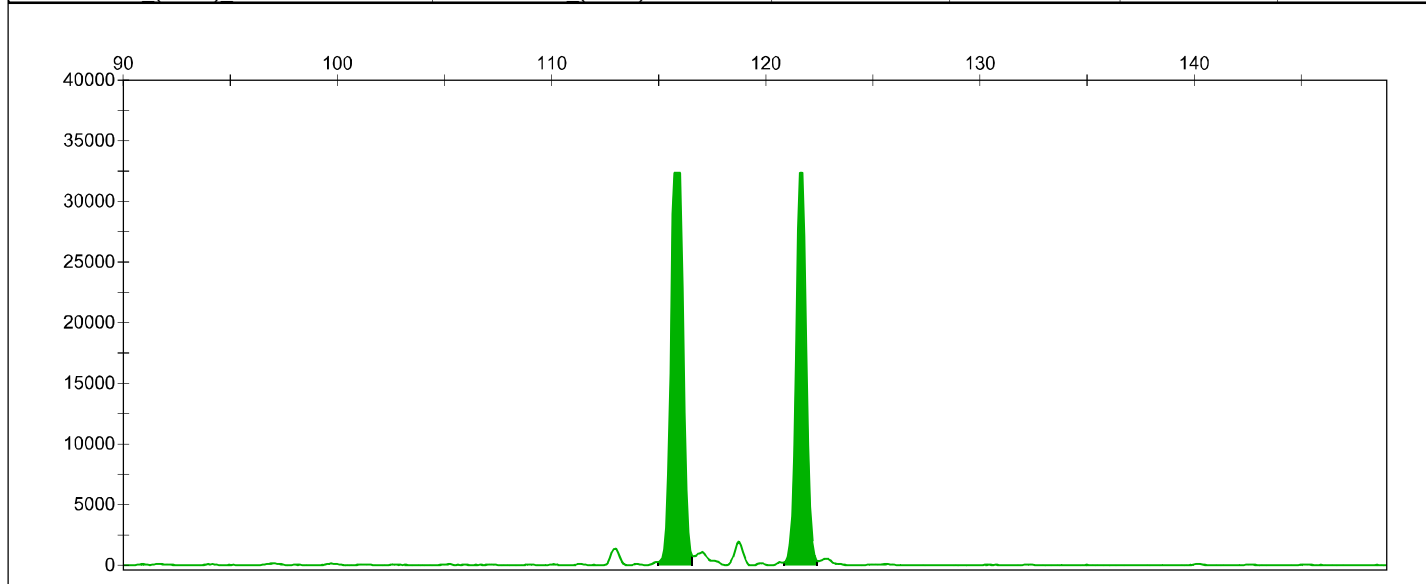
71102731528_(35-49)_B08.fsa	71102731528_(35-49)	None		▲	■
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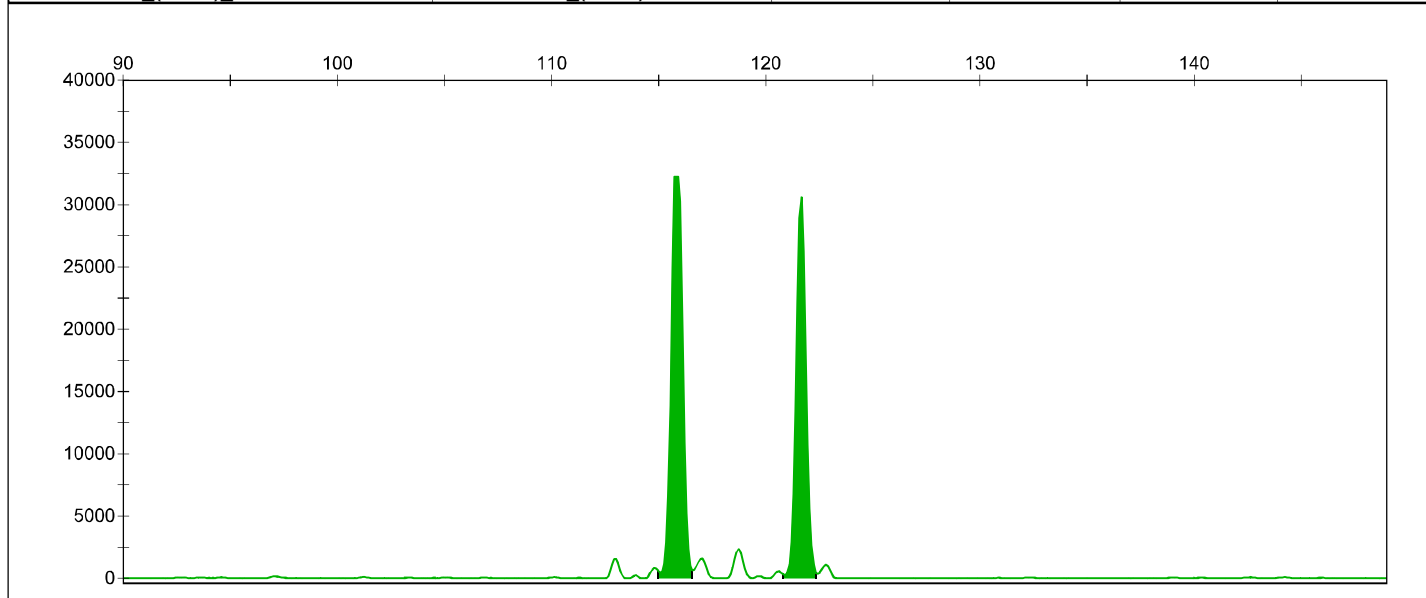
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731529_(35-50)_C08.fsa	71102731529_(35-50)	None		■	■



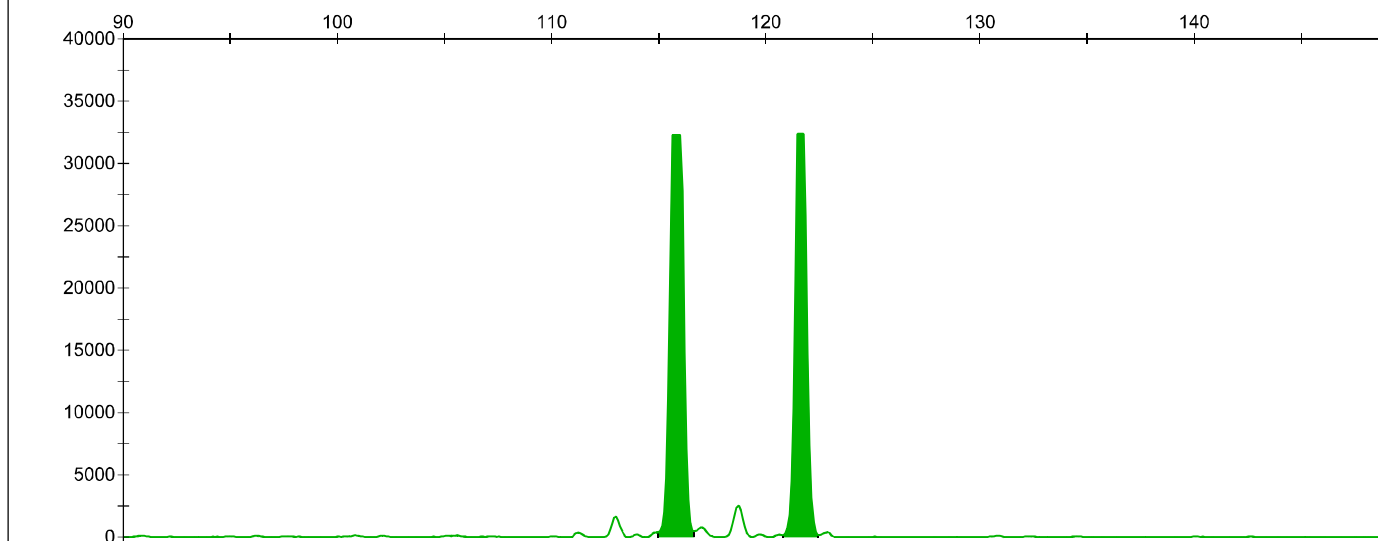
71102731530_(35-51)_D08.fsa	71102731530_(35-51)	None		▲	■
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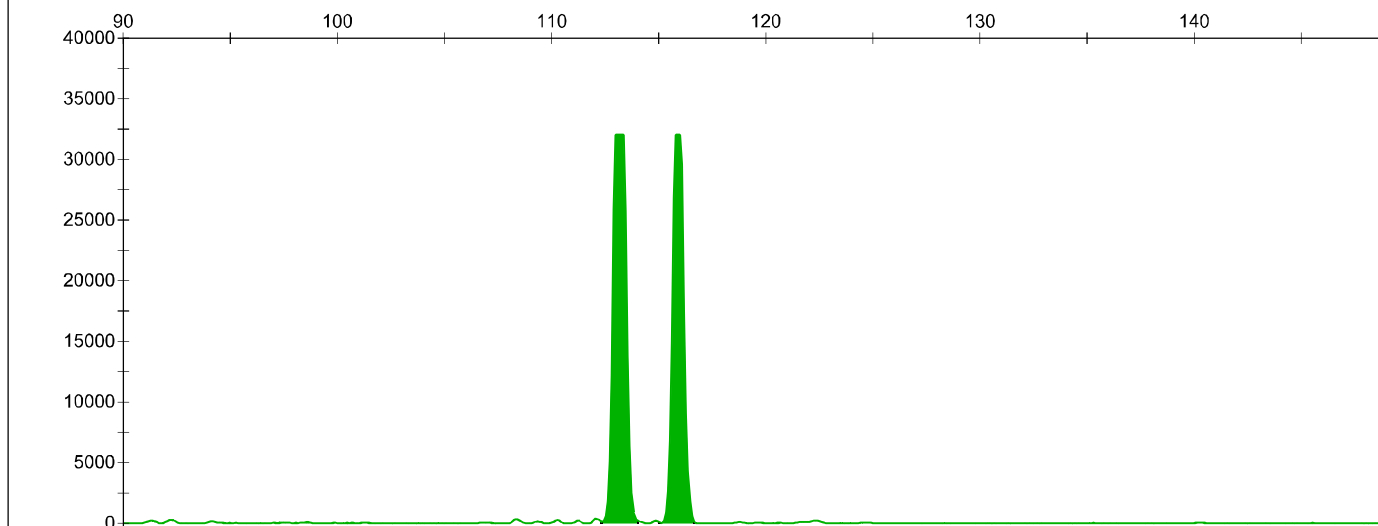
71102731531_(35-53)_E08.fsa	71102731531_(35-53)	None		▲	■
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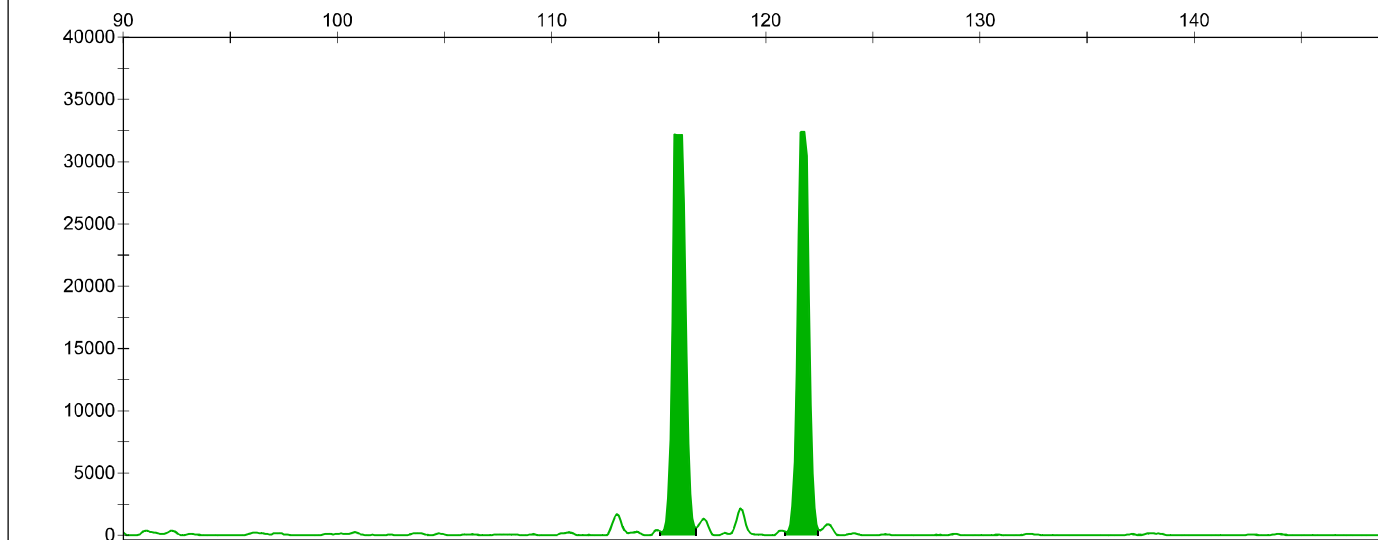
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731532_(35-54)_F08.fsa	71102731532_(35-54)	None		▲	■



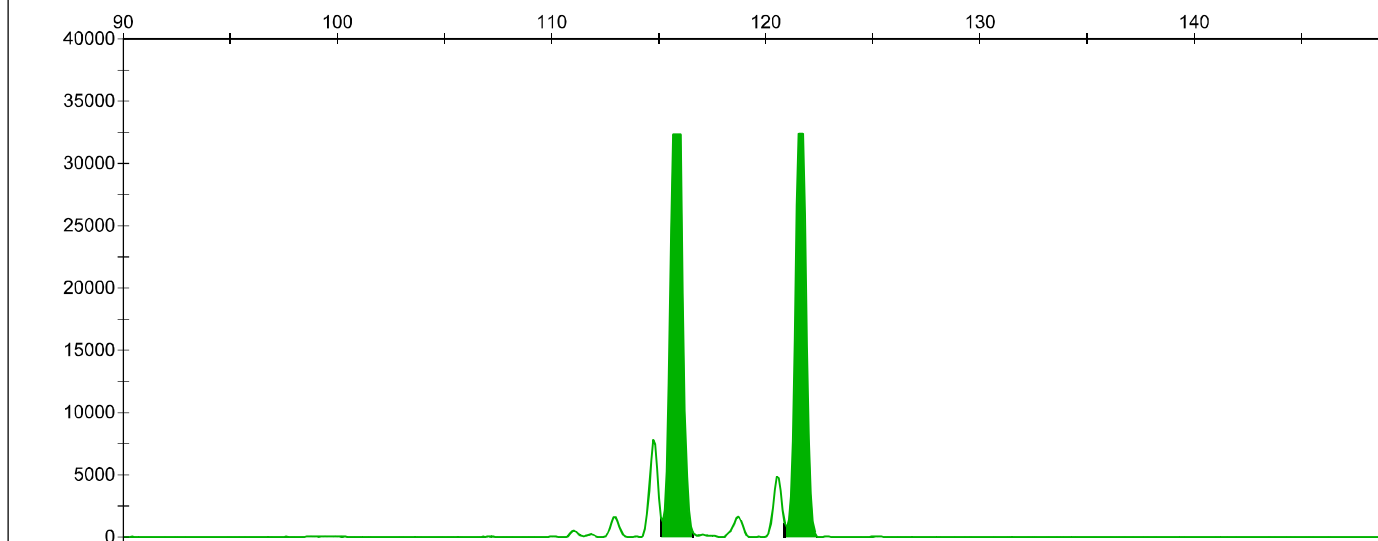
71102731533_(35-55)_G08.fsa	71102731533_(35-55)	None		▲	■
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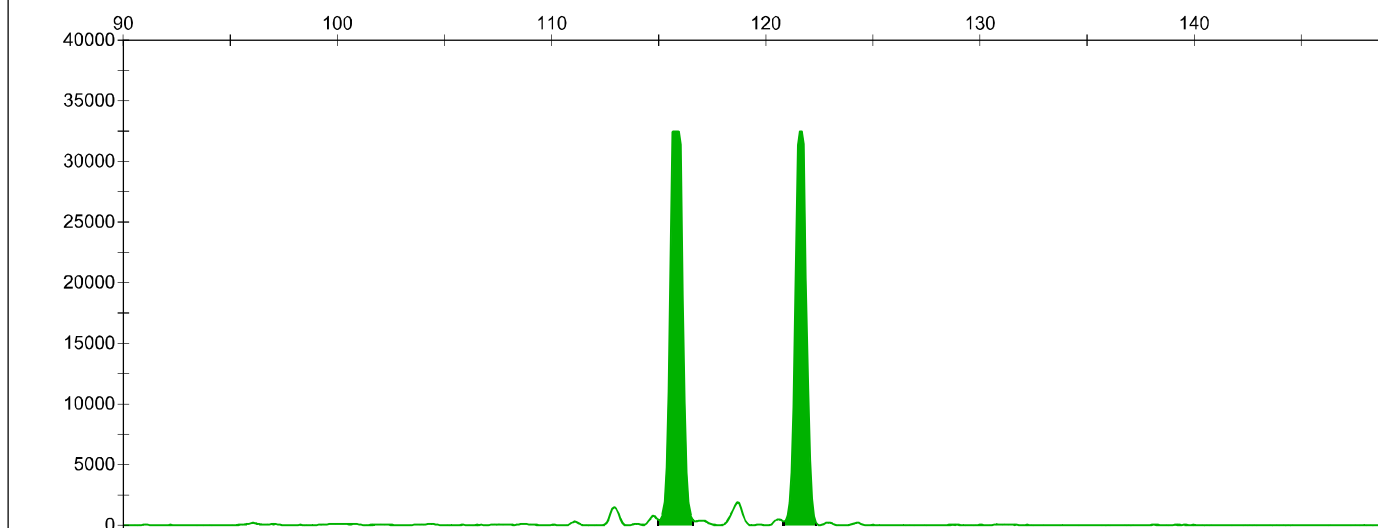
71102731534_(35-56)_H08.fsa	71102731534_(35-56)	None		▲	■
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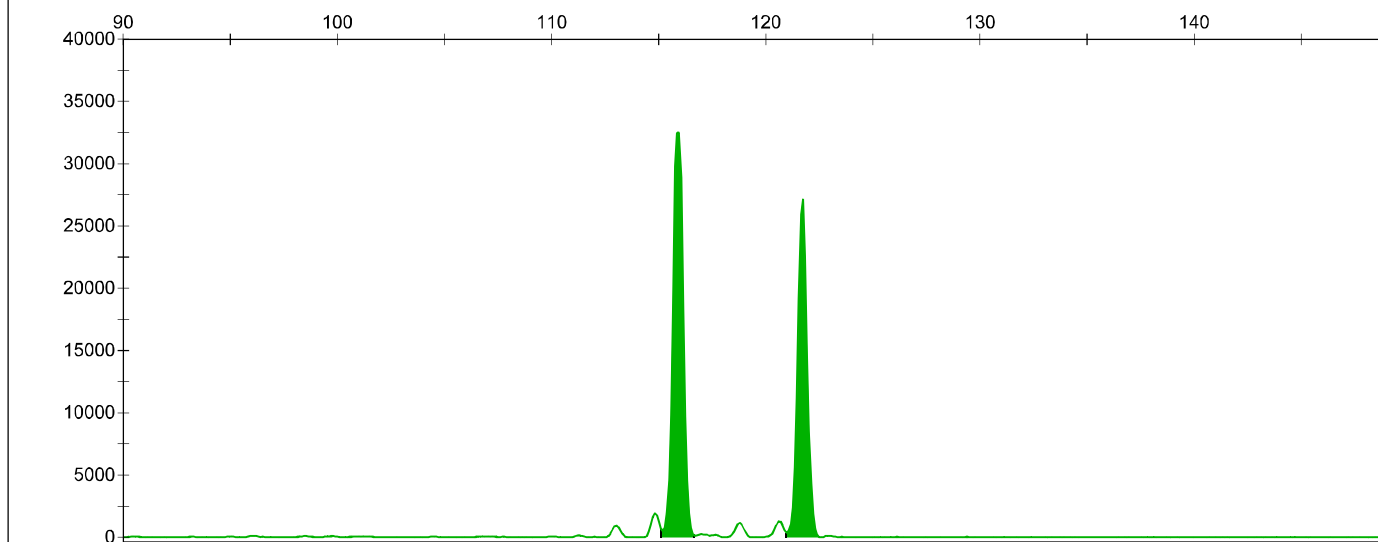
Sample File	Sample Name	Panel	SQ0	OS	SQ
71102731535_(35-57)_A09.fsa	71102731535_(35-57)	None		▲	■



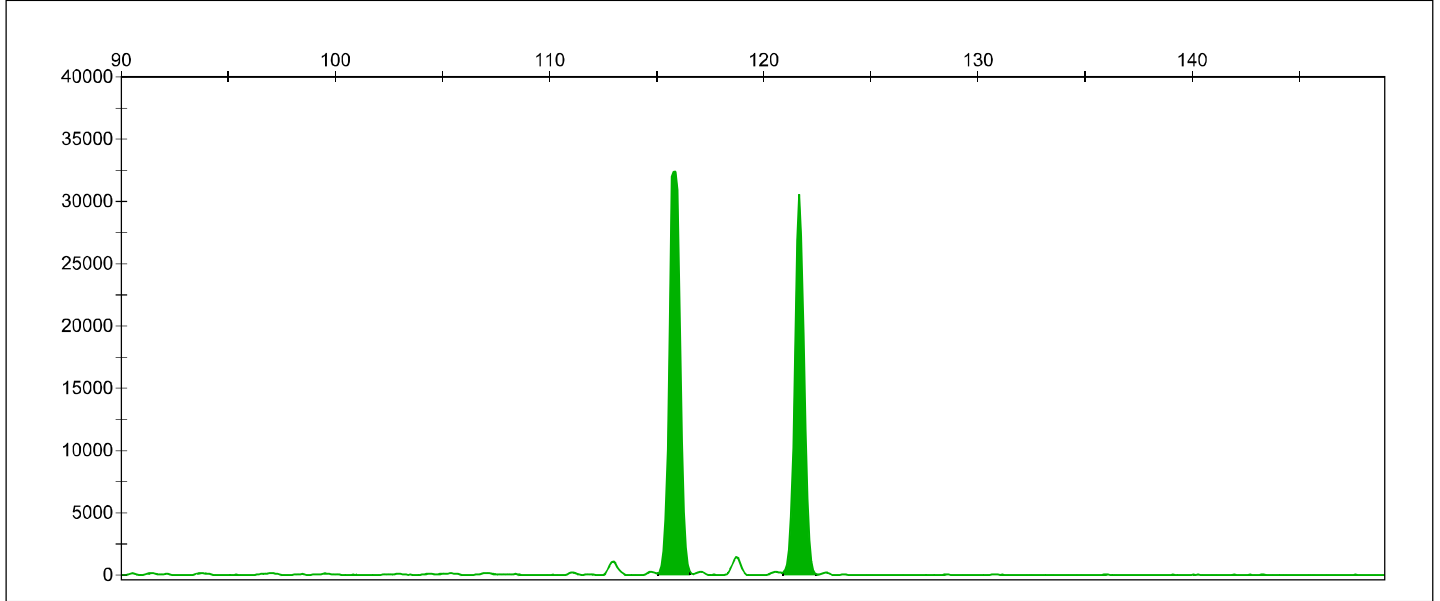
71102731536_(35-58)_B09.fsa	71102731536_(35-58)	None		▲	■
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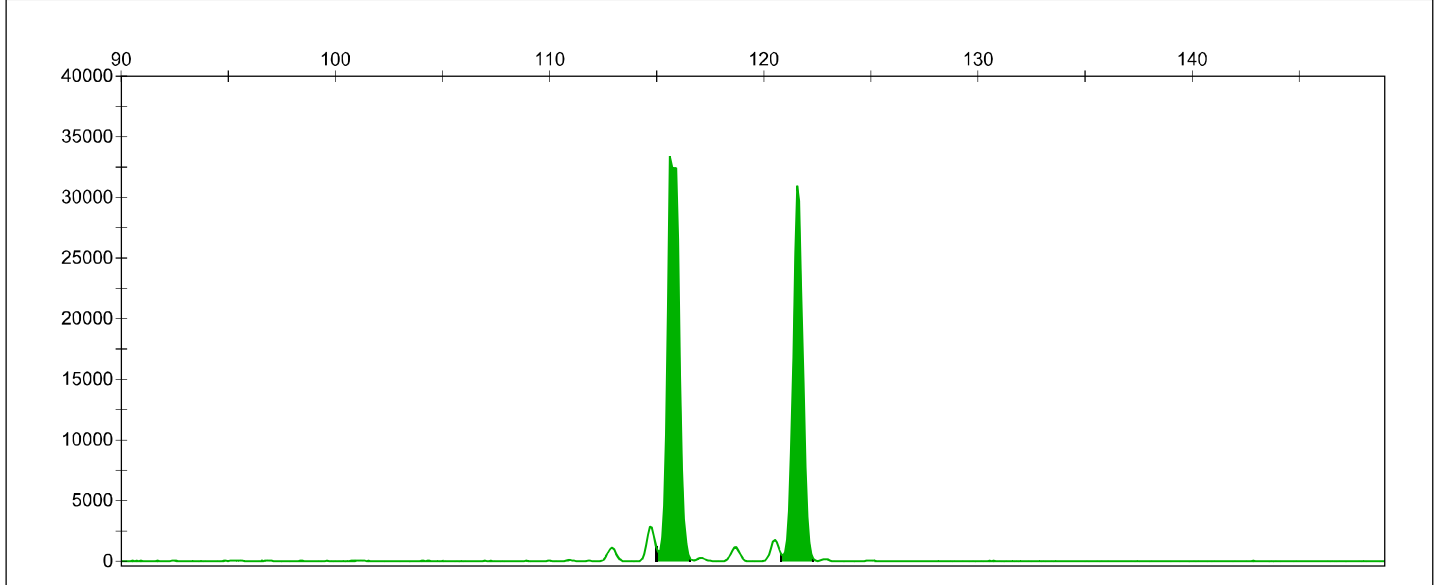
71102731537_(35-59)_C09.fsa	71102731537_(35-59)	None		▲	■
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Sample File	Sample Name	Panel	SQO	OS	SQ
71102731538_(35-60)_D09.fsa	71102731538_(35-60)	None		▲	■



71102731539_(35-61)_E09.fsa	71102731539_(35-61)	None		▲	■
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71102731540_(35-100)_F09.fsa	71102731540_(35-100)	None		■	■
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