



Improvement of Power Quality in a Three-Phase System Using an Adaline-Based Multilevel Inverter

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The existence of harmonics in the power distribution system (PDS) is treated as the most serious issue that affects its stability and reliability. The active power filter (APF) therefore plays a vital role in PDS to compensate for the harmonics for the improvement of the power quality (PQ) and to keep the total harmonic distortion (THD) below 5% as per IEEE-519. In this work, a three-phase four-wire (3P-4W) multi-level inverter (MLI)-based APF is proposed to overcome the shoot-through effect (STE) and reduce the distortions in the supply current. The control of the voltage source inverter (VSI) is achieved using an Adaline-based LMS (A-LMS) algorithm with a hysteresis current controller. The proposed filter that uses the A-LMS technique is compared with the conventional recursive least square (RLS) algorithm. The A-LMS approach is mainly operated for maintaining the dc link voltage of MLI, which follows the principle of capacitor energy and reduces the total harmonic distortion (THD) under different load variations. The performance of MLI under different load conditions is designed, developed, and validated by using a MATLAB/Simulink environment, and the preeminent features are established.

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INTRODUCTION

In the era of modern power systems, it is quite a promising task to reduce the harmonics in the PDS. The harmonics are generally caused due to the usage of a non-linear load. Therefore, amongst the probable solutions in recent years, it is observed that multi-level inverter (MLI)- (Siddique et al., 2019a,b) based APF has the capability to control and regulate the electrical disturbances and improvise the PQ by injecting the compensating current (Babu et al., 2015) to the PDS. MLIs are widely used in various areas like HVDC systems (Xu et al., 2014), battery energy storage systems (Soong and Lehn, 2014), as well as wind (Gangui et al., 2012) and solar energy systems (Yousri et al., 2019). Significant developments linked with the circuit topology (Siddique et al., 2019c) and control techniques of the MLI have been noticed in recent days. Recent studies have demonstrated the importance of the MLI, which delivers an ideal functionality for high and medium voltage systems (Ali and Krishnaswamy, 2018). With the use of an MLI, the compensations in current harmonics are very effective in improving the PQ. An MLI can provide improved PQ by operating the power semiconductor devices at a frequency near to the fundamental (Rodriguez et al., 2002). Of course, for low-power applications, the switching frequencies of the switching devices are not restricted, but, at its lower value, the efficiency of the converter increases (Ozdemir et al., 2008). An MLI

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 $\label{eq:table_table_table} \begin{array}{c} \textbf{TABLE 1} \mid \textbf{Comparative analysis between Conventional Inverter and} \\ \textbf{proposed MLI}. \end{array}$

Issues	Conventional Inverter	Proposed MLI
Shoot-through phenomenon	Available	Removed
Reliability	Less	More
Switch stress (voltage and current)	High	Low
Switching circuit cost	More	Less

also offers advantages, such as the possibility of direct interfacing to the medium voltage system without using a coupling transformer, the production of output voltages with lower distortions, having high power conversion characteristics, and providing a high equivalent switching frequency effect at rather lower values.

Different configurations of MLIs have been discussed so far in this area. In Rodriguez et al. (2009) and Pratheesh et al. (2017), the neutral clamped MLI is discussed where the authors presented the modulation and control techniques with special attention to the distribution losses. In Zhang et al. (2013) and Lei et al. (2017), the authors have discussed the function of the flying capacitors for harmonics and unbalanced current compensation. In Karasani et al. (2016), and Gupta et al. (2015), the performance of the cascaded MLI (CMLI) has been analyzed. Among the available MLI topologies, the CMLI is extensively used; it provides a modular design and can be directly linked to the grid at PCC instead of using a transformer (Ertl et al., 2002). Moreover, it provides power semiconductors with a lower rating compared to the standard two-level configurations, thereby improving the reliability under faulty situations. The MLI is given higher preference over other switching topologies because the MLI achieves the output of equal voltage by using fewer switching devices.

In this paper, a 3P-4W H-bridge interleaved buck-type active power filter (HILBAPF) (Panda and Patel, 2014) is implemented. HILBAPF has been developed to overcome the problems incurred by the conventional MLI in balancing the capacitor voltages and to exclude the interfacing transformer. Also, the conventional MLIs are not capable of managing faults and fault ride-throughs. Another major factor of the MLI is reliability, which decreases by means of STE. The supply voltages, with an unbalanced and non-sinusoidal supply, combined with the existence of negative and zero sequence components in the supply source, results in huge losses and excessive temperature increases in the PDS. Therefore, compensating for the zero sequence components in the neutral wire has become a primary task in the improvement of the PQ. A comparative analysis between conventional inverter and proposed MLI is provided in Table 1.

Various control techniques have been used so far in this area to accomplish the compensation task, which comprises the instantaneous power theory (p-q) (Hachani et al., 2017), the synchronous reference frame (SRF) theory (Hoon et al., 2016), the discrete Fourier transform approach (Wang et al., 2016), and the deadbeat controller (Qi et al., 2017). However, adaptive filters

(Chilipi et al., 2016) play an important role in compensating for the harmonics by injecting the required current to the power grid. Traditionally, a recursive least square (RLS) (Das et al., 2017) is employed, which offers improved performance, simplicity, robustness, and a lower computational burden. However, they are still altered due to factors like a slow convergence rate, longer iteration rate, and large storage capacity. These issues thus sort out use the Adaline approach (Subudhi et al., 2012). The Adaline approach is the most commonly used artificial neural network (ANN) technique to extract the fundamental and/or harmonic components. ANN has been projected as an attractive estimation and regression technique due to its parallel computing nature and high learning capability.

Moreover, the A-LMS is very simple and produces fewer computational problems. It offers natural linearity and the methodology is fast. In this study, therefore, an A-LMS (Merabet et al., 2017) approach in the MLI for calculating the reference current extractions is presented. The proposed approach provides improved robustness, speed, and efficiency along with a lower switching frequency or lower current ripple as compared to standard two-level topologies. Moreover, it produces a sinusoidal component from a non-linear supply voltage. The performance is realized using a Matlab/Simulink and compared with the conventional RLS to check the effectiveness of the proposed system.

The main idea in this work is to design a power system model with an MLI for compensating for the current harmonics and reactive power compensation by supplying a compensating current and reducing the THD percentage below 5% as per IEEE standard. The proposed system is tested under balanced and unbalanced load cases. The reference source current extraction is realized using an A-LMS. The A-LMS extracts the current in a simpler way with a shorter period of execution. The A-LMS algorithm is very dynamic for severe load variations. The proposed method improves the effective dynamic performance of the MLI for compensating for load currents with a reduced computational burden. The proposed technique is easily realized on digital processors and performs the fewest number of calculations.

The paper is arranged into several sections. Section System Configuration provides detail explanations of 3-phase, 4-wire HILBAPF, section Control Methodology provides proposed Adaline techniques, section Results and Discussion provides the result analysis for PQ improvement, and section Conclusions provides the conclusion.

SYSTEM CONFIGURATION

To obtain the fundamental supply current, the MLI must inject or absorb a compensating filter current. The compensated current must be the same and be in phase opposition to the harmonic components. The 3P-4W with HILBAPF is shown in **Figure 1**. The proposed model is designed especially for high voltage, medium-to-high power applications with an objective to end shoot-through problems. It consists of three single-phase Hbridge inverters. The advantage of this topology is that the voltage



across in each H-bridge interleaved buck inverter appears at the single phase voltage only, and hence the dc link reference voltage required is decreased by $\sqrt{3}$ times as compared to the other conventional MLI. The proposed topology requires only one dc link capacitor voltage out of the three individual phases for reference current generation, and this consequently reduces the voltage regulation complexness.

For the 3P-4W HILBAPF, the following mathematical equations are presented:

$$i_{s(a+b+c)} = i_{sn}$$

$$i_{L(a+b+c)} = i_{Ln}$$

$$i_{comp(a+b+c)} = i_{compn}$$
(1)

where, $i_{s(a+b+c)}$, $i_{L(a+b+c)}$, and $i_{comp(a+b+c)}$ represent the corresponding source, load, and compensating current of phase a, b, and c, respectively, while, i_{sn} , i_{Ln} and i_{compn} represent the corresponding neutral supply, load, and compensating currents. Now, in HILBAPF, the compensating current can be presented as the sum of the two coupling inductors current for

individual phases:

$$i_{compa} = i_{La1} + i_{La2} = -i_{La3} - i_{La4}$$

$$i_{compb} = i_{Lb1} + i_{Lb2} = -i_{Lb3} - i_{Lb4}$$

$$i_{compc} = i_{Lc1} + i_{Lc2} = -i_{Lc3} - i_{Lc4}$$
(2)

The dc-side capacitance (C_{dc}) of HILBAPF is given as:

$$0.5 * C_{dc} * \left[\left(V_{dc}^2 - V_{dc\min}^2 \right) \right] = V_s(t) * I * \Delta t$$
(3)

where, C_{dc} , V_{dc} , $V_{dc\min}$, I, $V_s(t)$, and Δt are, respectively, the capacitance of the DC-bus, voltage across the DC-side, minimum dc-bus voltage level, phase current, phase voltage, and change of time where the DC-bus voltage is to be improved. The control strategies of VSI using A-LMS are discussed in subsequent sub-sections.

CONTROL METHODOLOGY

To obtain the fundamental supply current, the APF must inject or absorb a compensating filter current. The compensated

current must be the same and be in phase opposition to the harmonic components. The 3P-4W MLI is shown in **Figure 1**. The performance of the 3P-4W MLI depends on the control strategy designed. In this paper for improving the reliability of the proposed system, two different control strategies, the RLS and A-LMS techniques, are used for controlling the VSI of 3P-4W MLI. The details of the controlling techniques are discussed in subsequent sub-sections.

RLS Algorithm

The RLS approach, with its simple structure and robust performance, is extensively implemented in many applications. The RLS technique is very simple; it reduces noise and automatically adjusts parameters. During the steady state, the active power offered by the power system is equal to the active power required by the load, and there is therefore no active current that flows through the MLI. Furthermore, the DC-link voltage is fixed, and its waveform has six ripples. Therefore, if the transient state occurs due to the load variation, the DC-link capacitor will provide the active power variance between the supply system and load, which may in turn produce a fluctuation in the DC link. In order to sustain a fixed DC-bus voltage and weaken the six ripples in the waveform, the MLI employs an RLS filter to get a DC-bus active parameter (A_{dcx}) for each phase.

In this approach for obtaining the compensation current generation, the RLS filter is used in the MLI such that the dc link active current parameter (A_{dcx}) is obtained with a constant dc link voltage and so that it can be ripple free in the waveform. **Figure 2** provides the functioning of the RLS algorithm, where V_{ref} references the dc link voltage, and V_{dc} (*i*) references the dc link voltage, having an instantaneous sampling value. To sustain the dc link, the instantaneous active power required to maintain the DC-link voltage (P_{dc}) is specified as

$$P_{dc}(i) = \left[\left(V_{ref} + y(i) \right)^2 - V^2_{ref} \right] * C/2$$
(4)

where, *C*, termed as the DC-bus capacitor, and A_{dcx} can be obtained as

$$A_{dcx} = P_{dc}(i) / V_x^2 \tag{5}$$

The final A_x , i.e., the active parameter, is obtained as

$$A_x = A_{ax} + A_{dcx} \tag{6}$$

For each phase, the instantaneous current is given as

$$i_x(t) = i_{ax}(t) + i_{nx}(t) \tag{7}$$

where,

$$i_{nx}(t) = i_{rx}(t) + i_{hx}(t)$$
 (8)

In a grid voltage, harmonics do not exist, and therefore instantaneous voltage is expressed as

$$V_x(t) = V_{ax}(t) \tag{9}$$



For each phase, the reference active current is expressed as

$$i_{ax}(t) = A_x V_x(t) \tag{10}$$

The instantaneous reference compensation current is identified as

$$i(t) = i_x(t) - i_{ax}(t)$$
 (11)

Therefore, for the distortion limit, the reference compensating current is injected into the power system.

A-LMS Algorithm

This algorithm is a combined technique between the Adaline and LMS algorithm. The weights in Adaline are basically updated using the LMS algorithm. Due to its simple structure and robust nature, the A-LMS algorithm is widely implemented in signal processing and control system application. The proposed technique is used to calculate the reference current components for the MLI. The convergence rate of the LMS algorithm depends on the fixed step-size parameter. The A-LMS offers several advantages, such as a simple structure, and it can be realized easily in practical applications. The results of frequency tracking, especially harmonics detection, demonstrate that the A-LMS algorithm can be utilized effectively in analyzing the PQ issues. The learning capacity of the ANN supports online adaptation to any variation in electrical parameters. Each neuron in ANN is treated as an elementary neuron. Each neuron collects a number of input variables from upstream neurons. Each input is assigned with a weight "w" representative of the strength of the connection. The output of the neuron acts on the activation function of the weighted sum inputs. Figure 3 illustrates the configuration of the Adaline neural network.

The Adaline output can be computed as per Equation (12):

$$Y = \sum x(i)w(i) = x^T w \tag{12}$$

where x and w (w_0 , w_1 ... w_n) are the input and weight vector, respectively, with n being the dimension, and Y being the estimated output.

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Weight Value Estimation Using the A-LMS Algorithm

The A-LMS technique is designed for the extraction of the real fundamental frequency component of the load current. For maintaining a fixed DC-bus voltage, a PI controller is employed in which the DC-bus voltage is compared with its reference value.

Considering a three-phase system where the source voltage is sinusoidal and it is expressed as

$$v_s = v_m \sin wt \tag{13}$$

and the current in the non-linear load is given as

$$i_L = I_1 \sin(wt + \phi_1) + \sum_{n=2}^{\infty} I_n \sin(nwt + \phi_n)$$
 (14)

Equation (14) can be represented as

$$i_L = i_{lp}^+ + i_{lq}^+ + i_{lh}^- \tag{15}$$

where, i_{lp}^+ , i_{lq}^+ , and i_{lh}^- are the positive sequence active load current, positive sequence reactive load current, and negative sequence load current (harmonic components), respectively.

In a single phase the current active component (i_p) , is expressed as

$$i_p = w_m u_s \tag{16}$$

The unit vector is denoted as u_s , and w_m is denoted as the calculated weight of the A-LMS.

Therefore, the iterated weight is given as

$$w_{m(k+1)} = w_{m(k)} + \mu \left[i_{lk} - w_{m(k)} u_{s(k)} \right] u_{s(k)}$$
(17)

where μ is termed as the convergence coefficient.

$$w_m^+ = \frac{\left(w_{ma}^+ + w_{mb}^+ + w_{mc}^+\right)}{3} \tag{18}$$

DC Link Voltage Control

The error across the DC-bus is given as

$$v_{dc(n)}^* - v_{dc(n)} = \Delta v_{dc(n)} \tag{19}$$

where the output from PI is expressed as

$$I_{sm(n)} = I_{sm(n-1)} + I_{P1}$$
(20)

and where, $I_{P1} = k_{pdc} \left[\Delta v_{dc(n)} - \Delta v_{dc(n-1)} \right] + k_{idc} \Delta v_{dc(n)}$ as well as k_{pdc} and k_{idc} are the proportional and integral gain of the controller, respectively.

Generation of Reference Source Currents

The three-phase source reference currents are expressed as

$$i_{sa}^* = (w_m^+ + I_{sm}) u_{sa}$$
 (21)

$$i_{ch}^* = \left(w_m^+ + I_{sm}\right) u_{sh} \tag{22}$$

$$i_{sc}^{*} = \left(w_{m}^{+} + I_{sm}\right)u_{sc}$$
 (23)

where, i_{sa}^* , i_{sb}^* , and i_{sc}^* are considered as reference source currents. These currents are compared with detected source currents, and their resultant output is supplied to the HCC (hysteresis current controller) for gating signal generation. The detailed algorithm is shown in **Figure 4**. The proposed technique needs less computational workout. Moreover, this technique instantaneously compensates for the source currents, which is not easy in the case of conventional techniques. The weights are computed online using the LMS algorithm. The weights are averaged not for the purpose of averaging at a fundamental frequency but for the canceling of the sinusoidal oscillating components in weights, which are available due to load current harmonics. The weight average in each different phase is depicted in **Figure 4**.

The ADALINE receives the PCC voltage and the individual load current, and it generates the individual weight with the reference source current. From the figure, it is clear that, for switching signal generation, the sensed source currents are subtracted from the reference source currents, and then the individual source current errors are supplied to the HCC.

RESULTS AND DISCUSSION

A power system model in the MATLAB/ SIMUINK and Simpower-System Block set is designed to analyze the function of the proposed controller in the three-phase system using the MLI. The complete system is composed mainly of a threephase source, a non-linear load (balanced and unbalanced load), the MLI system, and the PI controller with different proposed control strategies.

The performance of the proposed power system model is measured with the conventional RLS technique and the proposed Adaline-LMS technique under balanced and unbalanced loads. The concerned parameters of the system are provided in subsequent subsections.

To investigate the performance of the system, the test is carried out using a three-phase supply source and a non-linear load. The non-linear loads for balanced and unbalanced loads are provided in the **Appendix** section. Due to the presence of harmonics, it is noted that the load current is distorted from the normal sinusoidal current when a non-linear load is connected. The current wave forms of the distorted load currents during an unbalanced non-linear load are illustrated in **Figure 5A**. From the figure, it can be seen that the distortion with a current dip is found from 0 to 0.05 s. After 0.05 s, the distorted load current is maintained at a rated value up to 0.2 s. The THD value found during the unbalanced load is around 38.97%, which is illustrated in **Figure 5B**. Now, the proposed model is operated during the balanced load. The distortion in the load current is found around 28.53%, which is found to be less compared to the case of unbalance load. The waveforms of the load current distortions are illustrated in **Figures 5C,D**. It is observed that in both of the cases the distortion of harmonics is not maintained as per the IEEE-519 standard. Therefore, to reduce the harmonics and to improve the PQ, the power system is run under the MLI using the RLS and A-LMS harmonics estimation technique. The individual test for both the RLS and A-LMS are analyzed under different load conditions. The performances of both techniques are observed individually to show the feasibility of the control technique.

Balanced Loading Condition

The performance study of the proposed power system model using the MLI is operated under a balanced load using the RLS and A-LMS techniques. Initially, it was observed that, with the use of the RLS technique, the distortion of voltage harmonics was improved as compared to the case that did not use the compensating device. The case study is designed for the case of balanced load conditions. **Figure 6** provides the simulation



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outcomes of three-phase system using the RLS algorithm. **Figure 6A** provides the simulation outcomes of single-phase source current, load current, filter voltage, and dc-link voltage. The THD analysis is shown in **Figure 6B**, and was found to be 1.75%. From the simulation results, it was proven that the load current, which is composed of harmonics, is compensated for by the MLI, and the compensated RLS technique was found to maintain the IEEE-519 standard.

Furthermore, the proposed model was operated using the A-LMS technique and the corresponding results are depicted in



Figure 7. The source and load current, filter voltage, and dc-link voltage are provided in **Figure 7A**. The THD results are shown in **Figure 7B** and found to be 1.21%. It is observed that, when using the A-LMS technique under balanced load conditions, the distortions in the harmonics was improved compare to the RLS technique.

Unbalanced Loading Condition

In this sub-section, the MLI under the unbalanced non-linear loading condition is observed. In the first case, the system is run using the RLS technique. The corresponding results are shown in **Figure 8**. The source and load current, filter voltage, and DC-link voltage are produced in **Figure 8A**. The THD value was found to be 4.20% and is produced in **Figure 8B**.

Furthermore, the test was carried out using the A-LMS technique. The simulation outcomes of the source and load current, the filter voltage, and the DC-link voltage for the single phase is shown in **Figure 9A**. The corresponding THD results were found to be 3.70%. The THD analysis is shown in **Figure 9B**.

The simulation outcomes revealed that the distortion in the harmonics with the non-linear loads were compensated for using the MLI with the proposed A-LMS technique. It was noticed that the proposed model using the A-LMS provided satisfactory results. The THD of the load voltage was implied to display the better operation of the MLI under such conditions. The comparative analysis of THD is shown in **Table 2**.



algorithm under balanced load conditions: (A) simulation results showing supply current, load current, filter voltage, and DC-bus voltage; and (B) THD value of 1.21%.







FIGURE 9 Power quality analysis in three phase system using the A-LMS algorithm under unbalanced load conditions: (A) simulation results showing supply current, load current, filter voltage, and DC-bus voltage; and (B) THD value of 3.72%.

 TABLE 2 | Comparative THD analysis of proposed technique under different load conditions.

Parameters	Unbalanced load (% THD)	Balanced load (% THD)
Without using MLI	38.97	28.53
Using MLI with RLS	4.20	1.75
Using MLI with A-LMS	3.70	1.21

CONCLUSIONS

The proposed power system model employed a 3P-4W MLI. The proposed MLI used the A-LMS technique for reference current generation and was compared with the conventional RLS technique. The performance of the proposed MLI was tested the using Matlab/ Simulink tool. The MLI was operated under balanced and unbalanced loads using both the RLS and A-LMS techniques. Even though both of the techniques were capable of compensate for the harmonics, it was concluded from simulation outcomes that the A-LMS techniques gave better results compared to the conventional RLS technique. The fall in %THD was found to be more satisfactory in the A-LMS technique, which proves the effectiveness of the proposed technique. It was concluded that the compensation in the load current using the A-LMS technique was found to satisfactory compared to the RLS technique. Moreover, the proposed MLI needed only one DC-link voltage to operate, and it was thus simple to control.

DATA AVAILABILITY STATEMENT

The raw data supporting the conclusions of this article will be made available by the authors, without undue reservation, to any qualified researcher.

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AUTHOR CONTRIBUTIONS

All authors listed have made a substantial, direct and intellectual contribution to the work, and approved it for publication.

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Conflict of Interest: KB was employed by the company Offshore Technology Development Private Ltd.

The remaining authors declare that the research was conducted in the absence of any commercial or financial relationships that could be construed as a potential conflict of interest.

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APPENDIX

TABLE 1A | Parameters of the system.

Parameters	Values
Grid voltage	415 v
Supply frequency	50 Hz
Boost inductor	300 μΗ
Capacitor across dc side	1,000 μF
Voltage across dc link	320 v
Resistance across load	$R_L = 60 \ \Omega$, $(R_1 = 2 \ \Omega, R_2 = 4 \ \Omega, R_3 = 6 \ \Omega)$
Inductance across load	10 μF
Learning rate	0.5
Controller gain	$K_P = 0.05, K_I = 0.025$
Sampling time	2e ⁻⁵ s