



Power Decoupling Control of MMC and Small-Signal Stability Analysis of AC/DC Distribution Network With Renewable Energy

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Xianyong Z, Zijuan G, Yaohong H, Li L, Weikuan P and Jian C (2021) Power Decoupling Control of MMC and Small-Signal Stability Analysis of AC/DC Distribution Network With Renewable Energy. Front. Energy Res. 9:734797. doi: 10.3389/fenrg.2021.734797 AC/DC hybrid distribution network can realize the high penetration utilization of renewable energy and modular multilevel converter (MMC) is the key equipment to connect AC grid and DC grid. In this paper, the direct modulation based on the virtual resistor and the reference value of dc-bus voltage is adopted, and instantaneous-value model of threephase half-bridge MMC is derived by introducing differential-mode and common-mode component representation. Then, the decoupling control of AC active power and reactive power is designed based on the linear active disturbance rejection control theory (LADRC). The total disturbances including the coupling term and capacitor voltage fluctuation can be estimated by extended state observer (ESO), and then cancelled exactly. Based on the simplified average-value model of MMC, the small signal stability analysis of three-terminal AC/DC distribution network with 26-level MMCs corresponding to the Tangjiawan-Jishan1-Jishan2 demonstration project is carried out by root locus and bode diagram method, which gives the guidance for the choice of the main circuit component and controller parameters, and shows that the virtual resistor greatly reduces the resonant peak of AC/DC distribution network. Simulation and experiment results verify the effectiveness of the power decoupling control strategy of MMC and the AC/DC distribution network can realize complex multi-directional power flow.

Keywords: MMC, power decoupling control, small signal, stability analysis, virtual resistor

1 INTRODUCTION

In order to realize the plan for peaking carbon dioxide emissions before 2030, it is necessary to build a new power system with renewable energy as the main body. Renewable energy such as wind and solar power is intermittent and varies greatly affected by climate conditions and geographical location. Along with the increasing proportion of renewable energy, high degree of concentration grid-connected renewable energy brings serious challenge to grid stability and power quality (Mithulananthan et al., 2013). Renewable energy access to the distribution network, to achieve local use of power generation, reduce transmission loss, become a new way to improve the utilization rate of renewable energy (Chen et al., 2016a). With the rapid development of DC renewable generation such as PV, and the popularity of DC load such as electric vehicles, DC distribution network gains great attentions because it reduces energy conversion loss and there is no inherent

frequency stability and skin effect of ac system. Since the traditional distribution network is still ac grid, Professor Wang Peng proposed the concept of AC/DC hybrid distribution network (Wang et al., 2011). It can be compatible with the AC grid and DC gird and realize high proportion utilization of renewable energy, improve the reliability of power supply system. Therefore, AC/DC distribution network becomes the latest development trend of the current distribution network.

The converter is the crucial equipment to realize the power exchange among different distribution networks. The voltagesourced converter has become the most popular because it has the advantages of power supply to island, fast independent control of active and reactive power, and fast response of power flow reversal. With the development of the voltage level and capacity of the transmission system, the converter topology of VSC-HVDC has gradually transited from the two-level or threelevel structure to the MMC (Alassi et al., 2019). MMC has cascaded sub-modules (SMs), instead of the power electronic devices to connect in series directly, which has the advantages of low switching frequency, low switching loss, good waveform quality, and easy capacity expansion, etc. (Lesnicar and Marquardt, 2004). There is the half-bridge SM, the full-bridge SM, the clamp double SM, and three-level SM and so on (Marquardt, 2010; Marquardt, 2011). Although the full-bridge SM and the clamp double SM has the ability of DC fault selfcleaning, the half-bridge SM becomes the dominant topology because of its simple control and low cost.

The AC and DC voltages of MMC are obtained simultaneously by switching the SMs. The modulation strategy of MMC mainly includes the nearest level modulation (NLM) and the phaseshifted-carrier PWM (PSC-PWM) (Debnath et al., 2015). The NLM needs to measure the current direction and sort the capacitor voltage of SM, which needs additional control loops. PSC-PWM generates N group of PWM signals when N groups of triangular carriers with phase offsets of $2\pi/N$ angle are compared with the same modulation wave. The PSC-PWM is much simpler and widely used in MMC. The steady state characteristic and open-loop control strategy of MMC are studied in (Antonopoulos et al., 2009; Angquist et al., 2011). The open-loop scheme analysis usually assumes that the modulation indices and the AC current are both sinusoidal, and doesn't consider capacitor voltage ripple, which doesn't reflect the real case. The closed-loop controller design of MMC is similar to that of two-level converters (Guan and Xu, 2012). In order to avoid the instability, the reference value of the dc-bus voltage (Harnefors et al., 2013) or the estimated capacitor voltage (Vasiladiotis et al., 2014) is used to calculate the modulation index, which leads to the strong coupling between active and reactive power (Yang et al., 2018).

A particular average-value model of MMC is presented and the coupled transient response is revealed in (Yang et al., 2017). Reference (Wang and Wang, 2019) identifies four power coupling paths and two power influence factors. The above decoupling strategies usually adopt PI controller and execute the exact cancellation according to the analytical expressions. But the analytical expression can't describe the complex coupling among ac current, circulating current and dc current under different operation conditions precisely. Disturbance-observerbased control instead of precise modeling provides a new idea. A number of widely used linear and nonlinear disturbance estimation techniques such as DUE and DUEA are reviewed in (Chen et al., 2016b). The active disturbance rejection control (ADRC) has obtained excellent control results in motion control (Alonge et al., 2017; Liu et al., 2017; Wang et al., 2019; Liu et al., 2020). The fundamental part of ADRC is the extended state observer (ESO), which estimates the total disturbances including unknown uncertainties and external disturbances. The early MMC based HVDC project is mainly single terminal or twoterminal for wind farm integration (Friedrich, 2010; Bergna Diaz et al., 2015). With the accretion of the grid and renewable energy capacity and the improved requirement of power supply reliability, multi-terminal MMC based HVDC projects becomes the mainstreams (Rao, 2015; Trinh et al., 2016; Zhang et al., 2017). The MMC based multi-terminal HVDC is a complex power electronic system. It is a challenge to investigate the dynamic performance and stability based on appropriate modeling and simulation method to satisfy planning and operational criteria (Adam and Williams, 2014; Wang et al., 2016a; Wang et al., 2016b). Averaged model (Saad et al., 2013), detailed equivalent circuit model (Ahmed et al., 2016), and equivalent circuit model (Xiang et al., 2017) are established to simulate the electromagnetic transient response of HVDC system. When MMC can't provide sufficient damping, the multi-terminal DC grid is easy to resonance and instability. Therefore, virtual impedance damping control is proposed to improve the DC grid's performance (Li et al., 2019; Wan et al., 2018). Small signal stability analyses are used to design the system dynamics and select the controller parameters and DC inductance (Kotb et al., 2016; Li et al., 2018; Lu et al., 2018). On the one hand, above small signal model usually comprises ac current dynamics, phase-locked loop, and MMC dynamics, and DC networks, which is too complex and not easy to identify the key impact factor. On the other hand, above small signal model doesn't consider the effect of the modulation strategy and the virtual impedance simultaneously (Li et al., 2018).

MMC is the key equipment to connect the AC grid and DC grid. MMC based HVDC project usually has unidirectional power flow. But the MMC based DC/AC hybrid distribution network has complex multidirectional power flow. There is seldom theoretical and experimental research about the MMC based medium-voltage DC/AC distribution network. In this paper, the MMC based Tangjiawan-Jishan1-Jishan2 DC/AC distribution network demonstration project is studied. The contribution of this paper is summarized as followings: 1) a novel instantaneous-value model of MMC which introducing the differential-mode and common-mode component representation is derived. The direct modulation strategy is adopted based on the virtual resistor and the reference value of dc-bus voltage, which avoids the direct measurement of SM capacitor voltage and is easy to calculate. 2) AC active and reactive power decoupling control based on LADRC is realized, which doesn't require the precise modeling of coupling factor and uncertainty. 3) a simplified average-value model of MMC is deduced and small signal stability of AC/DC distribution network is analyzed, which identifies the damping effect of the virtual resistor. 4)



theoretical and empirical research on Tangjiawan-Jishan1-Jishan2 DC/AC distribution network demonstration project are carried out.

The rest of this paper is organized as follows. Section 2 introduces the common-mode and differential-mode component instantaneous-value model of MMC. Section 3 presents ac power decoupling control of MMC based on LADRC. Section 4 is the small signal stability analysis of MMC based three-terminal AC/DC distribution network. Section 5 is the simulations based on PSCAD. Section 6 is experiments on the Tangjiawan-Jishan1-Jishan2 AC/DC distribution network. Section 7 is the conclusions.

2 INSTANTANEOUS-VALUE MODEL OF MODULAR MULTILEVEL CONVERTER

Figure 1 shows the structure of three-phase half-brideg MMC. Each phase consists of the upper and lower arm. Each uppper (lower) arm has N half-bridge submodules and a inductor L_0 in series. L_0 is used to restrain the circulating current and buffer the short circuit. p(n) presents the vairable of upper (lower) arms. $u_{jp}(u_{jn})$ and

 i_{jp} (i_{jn}) are the upper (lower) arm voltage and current of phase j (j = a, b, c). L_{ac} is the inductor connecting the ac-grid and MMC. C is the submodule capacitor. u_{gj} and i_{gj} are the ac voltage and current respectively. u_{dc} and i_{dc} are the dc voltage and current respectively.

MMC is a complex AC-DC coupling system. The commonmode and differential-mode component representation is introduced to analyze the coupling system.

Defining the common-mode component of variable x is

$$\overline{\mathbf{x}_j} = 0.5 \big(x_{jp} + x_{jn} \big) \tag{1}$$

The differential-mode component of variable x is

$$\widehat{x}_j = 0.5 \Big(x_{jp} - x_{jn} \Big) \tag{2}$$

where $x_{jp}(x_{jn})$ is the upper (lower) arm variable of phase *j*. The arm current dynamics of phase *j* are

$$L_0 \frac{di_{jp}}{dt} - L_{ac} \frac{di_{gj}}{dt} = \frac{u_{dc}}{2} - u_{jp} - u_{gj}$$
(3)

$$L_0 \frac{di_{jn}}{dt} + L_{ac} \frac{di_{gj}}{dt} = \frac{u_{dc}}{2} - u_{jn} + u_{gj}$$
(4)

Let $\overline{i_j} = 0.5(i_{jp} + i_{jn}), \ \hat{i_j} = 0.5(i_{jp} - i_{jn}), \quad \overline{u_j} = 0.5(u_{jp} + u_{jn}),$ $\hat{u_j} = 0.5(u_{jp} - u_{jn}), \text{ Then Eqs 3, 4 can be expressed as}$

$$(2L_{ac} + L_0)\frac{d\hat{i}_j}{dt} = -\hat{u}_j - u_{gj}$$
⁽⁵⁾

$$2L_0 \frac{d\overline{l_j}}{dt} = u_{dc} - 2\overline{u_j} \tag{6}$$

Eq. 5 shows that the differential-mode current \hat{i}_j can be controlled by the differential-mode voltage \hat{u}_j . **Eq. 6** shows that the common-mode current \overline{i}_j can be controlled by the common-mode voltage \overline{u}_j . Therefore the independent control of \hat{i}_j and \overline{i}_j can be realized, which greatly simplifies the arm current controller design.

When the switch S1 is on and S2 is off, U_{sm} is equal to the capacitor voltage. When the switch S2 is on and S1 is off, U_{sm} is zero. Arm voltage is obtained from capacitor voltage by PSC-PWM. The relationship between the arm voltage and the capacitor voltage is

$$\mathbf{u}_{jp} = m_{jp} v_{jpc} \tag{7}$$

$$\mathbf{u}_{jn} = m_{jn} v_{jnc} \tag{8}$$

where v_{jpc} (v_{jnc}) is the sum of upper (lower) arm capacitor voltages, m_{jp} and m_{jn} are the modulation indices.

Let $\overline{m_j} = 0.5(m_{jp} + m_{jn})$, $\hat{m_j} = 0.5(m_{jp} - m_{jn})$, $\overline{v_{jc}} = 0.5(v_{jpc} + v_{jnc})$, $\hat{v_{jc}} = 0.5(v_{jpc} - v_{jnc})$, Eq. 7 can be expressed as

$$\overline{u_j} = \overline{m_j v_{jc}} + \widehat{m}_j \widehat{v_{jc}} \tag{9}$$

$$\widehat{u}_j = \overline{m_j} \widehat{v_{jc}} + \widehat{m_j} \overline{v_{jc}} \tag{10}$$

Supposed that the volatge balance control of submodule capacitor is realized for each upper or lower arm, and then the dynamics of capacitor voltages are

$$\frac{C}{N}\frac{dv_{jpc}}{dt} = m_{jp}i_{jp}$$
(11)

$$\frac{C}{N}\frac{dv_{jnc}}{dt} = m_{jn}i_{jn}$$
(12)

Adopting the common-mode and differential-mode component representation, Eqs 11 and 12 can be expressed as

$$\frac{C}{N}\frac{d\overline{v_{jc}}}{dt} = \overline{m_j}\overline{i_j} + \widehat{m_j}\widehat{i_j}$$
(13)

$$\frac{C}{N}\frac{d\hat{v}_{jc}}{dt} = \overline{m_j}\hat{i}_j + \hat{m}_j\overline{i}_j \tag{14}$$

It shows that the common-mode component and the differential-mode component of the capacitor voltage have complex nonlinear characteristics.

According to (Eqs 5, 6, 9, 10, 13, 14), the differential-mode current \hat{i}_j is mainly determined by \hat{m}_j . The common-mode current i_j is mainly determined by \overline{m}_j . Because the capacitor voltage is hard to measure and DC grid is also easy to resonate because of lack of damping, the modulation indices are determined as followings.

$$\overline{m_i} = 0.5 + R_v \overline{i_i} / v_{eac}^* \tag{15}$$

$$\widehat{m}_{j} = \widehat{u}_{j}^{*} / v_{eqc}^{*} \tag{16}$$



where v_{eqc}^{*} is the reference value of dc-bus voltage, and it is also the reference value of the sum of the upper (lower) arm capacitor

reference value of the sum of the upper (lower) arm capacitor voltage. v_{eqc}^* is used to avoid the measurement of the real capacitor voltage and the virtual resistor R_v is added to increase the damping of DC grid. \hat{u}_j^* is the reference value of the differential-mode component of arm voltage, and it is also the control variable.

3 AC POWER DECOUPLING CONTROL OF MODULAR MULTILEVEL CONVERTER BASED ON LINEAR ACTIVE DISTURBANCE REJECTION CONTROL THEORY

3.1 AC Power Modeling of Modular Multilevel Converter

MMC can exchange active power and reactive power with acgrid. From **Figure 1**, ac current in the three-phase stationary coordinates is

$$i_{gj} = i_{jn} - i_{jp} = -2\hat{i}_j \tag{17}$$

Power is usually analyze in the dq rotating coordinates. The relationship between the three-phase stationary coordinates and dq rotating coordinates is shown in **Figure 2**(Lu et al., 2018). We can get

$$\begin{bmatrix} x_d \\ x_q \\ x_0 \end{bmatrix} = \frac{2}{3} \begin{bmatrix} \cos\theta & \cos\left(\theta - \frac{2\pi}{3}\right) & \cos\left(\theta + \frac{2\pi}{3}\right) \\ \sin\theta & \sin\left(\theta - \frac{2\pi}{3}\right) & \sin\left(\theta + \frac{2\pi}{3}\right) \\ \frac{1}{2} & \frac{1}{2} & \frac{1}{2} \end{bmatrix} \begin{bmatrix} x_a \\ x_b \\ x_c \end{bmatrix}$$
(18)

where $\theta = \theta_0 + \int_0^t \omega dt$ is the angle between the coordinates. x_d , x_q , and x_0 are the variables in the dq coordinates. x_a , x_b , and x_c are the variables in the three-phase stationary coordinates.

When the d-axis of the rotating coordinates is aligned with the grid voltage space vector, that is $u_{gq} = 0$, the ac active power and reactive power can be calculated as

$$\begin{cases} P_s = 1.5 u_{gd} i_{gd} = -3 u_{gd} \hat{i}_d \\ Q_s = 1.5 u_{gd} i_{gq} = -3 u_{gd} \hat{i}_q \end{cases}$$
(19)

where active power is determined by \hat{i}_d and reactive power is determined by \hat{i}_q . The power decoupling control is simplified to the differential-mode arm current decoupling control.

The differential-mode arm current in eq. 5 can be expressed in dq rotating coordinates as

$$2L_{s}\frac{di_{d}}{dt} = -2\omega L\hat{i}_{q} - \hat{u}_{d} - u_{gd}$$

$$2L_{s}\frac{d\hat{i}_{q}}{dt} = 2\omega L\hat{i}_{d} - \hat{u}_{q} - u_{gq}$$
(20)

Where $L_s = L_0/2 + L_{ac}$.

According to Eq. 16, we can get

$$\begin{cases} 2L_s \frac{d\hat{i}_d}{dt} = -2\omega L\hat{i}_q - \frac{v_{eqc}}{v_{eqc}^*}\hat{u}_d^* - u_{gd} \\ 2L_s \frac{d\hat{i}_q}{dt} = 2\omega L\hat{i}_d - \frac{v_{eqc}}{v_{eqc}^*}\hat{u}_q^* - u_{gq} \end{cases}$$
(21)

where v_{eqc} is the actual value of the sum of the upper (lower) arm capacitor voltages. Eqs 13 and 14 show that v_{eac} is nonlinear and hard to measure, so it is difficult to determine the control variables $\hat{u_d^*}$ and $\hat{v_a^*}$.

3.2 AC Power Decoupling Controller Design **Based on Linear Active Disturbance Rejection Control Theory**

The structure of LADRC is shown in Figure 3. LADRC is composed of tracking differentiator (TD), linear extended state observer (LESO), and linear state error feedback control law (LSEF). TD can not only overcome the interference noise by obtaining differential signals, but also make reasonable arrangements for the transition process. In other words, TD realizes the fast tracking of reference signal without overshoot and gives the tracking signals of all-order derivatives of reference signal. LESO is based on the development of the traditional observer, which takes the input and output of the control object as one of its inputs. The all-order derivatives of the plant state and the total disturbance variables can be observed through LESO. An obvious advantage of ESO is that it does not rely on the detailed mathematical model for generating disturbances, nor does it need to measure its role directly. LSEF generates the control signal using the output error between the TD and LESO. The mathematical description of one-order LADRC is shown as followings.

One-order TD is

$$\frac{dz_{11}}{dt} = k_0 \left(x_1^{ref} - z_{11} \right) \tag{22}$$

where z_{11} is the tracking signal of reference input x_1^{ref} . Two-order LESO is

$$\begin{cases} \frac{dz_{21}}{dt} = z_{22} + k_{21}\varepsilon_1 + u \\ \frac{dz_{22}}{dt} = k_{22}\varepsilon_1 \\ \varepsilon_1 = x_1 - z_{21} \end{cases}$$
(23)

where z_{21} is the estimated value of the plant state x_1 . z_{22} is the estimated value of the whole disturbance, which including w(t)and other parameter uncertainties. k_{21} and k_{22} are gain coefficients. LSEF control law is

$$u = -z_{22} + k(z_{11} - z_{21}) \tag{24}$$

where k are the gain coefficient.

Eq. 21 can be expressed as

$$\begin{cases} 2L_s \frac{d\hat{i}_d}{dt} = f_d - \hat{u_d^*} \\ 2L_s \frac{d\hat{i}_q}{dt} = f_q - \hat{u_q^*} \end{cases}$$
(25)

 $f_d = -2\omega L \hat{i}_q - u_{gd} - \frac{v_{eqc} - v_{eqc}^*}{v_{eqc}^*} \hat{u}_d^*, \qquad f_q = 2\omega L \hat{i}_d - u_{gq} - \frac{\omega_{eqc} - v_{eqc}^*}{\omega_{eqc}^*} \hat{u}_d^*,$ Where

 $\frac{v_{eqc} - v_{eqc}}{v_{eqc}} \widehat{u_q^*}$. f_d and f_q are nonlinear and difficult to measure accurately. According to the LADRC theory, f_d and f_q can be regarded as the total disturbances and estimated by the LESO. So the dynamics of \hat{i}_d and \hat{i}_q can be decoupled to two independent subsystems based on LADRC. Then we can design the ac power decoupling control structure as Figure 4, which is divided into MMC valve level controller, MMC inner controller and MMC outer controller. The MMC valve level controller contains the modulation strategy and capacitor voltage balancing control,etc. The phase-shifted carrier PWM strategy is adopted, which is calculated based on the virtual resistor and the reference value of DC-bus voltage. The inner controller is core point of decoupling controller, which is designed based on LADRC. The outer controller can receive active power and reactive power commands from power dispatch center.

4 SMALL-SIGNAL STABILITY ANALYSIS OF MODULAR MULTILEVEL CONVERTER **BASED THREE-TERMINAL AC/DC DISTRIBUTION NETWORK**

Tangjiawan town is located in Zhuhai city, China. With the continuous progress of industrial park, the load of Tangjiawan town has increased too fast. If only Tangjiawan station supplies power, the power supply radius will be too long, the reliability is low, and the line loss is high. In order to improve the power supply quality and reliability, two 10kV buses are introduced





from Jishan station and connected with the 10 kV bus of Tangjiawan station, so as to increase the load transfer capacity of Tangjiawan station and reserve capacity of grid accident. The topology of Tangjiawan-Jishan1 -Jishan2 AC/DC distribution network demonstration project is shown in **Figure 5**. The rated capacity of Tangjiawan, Jishan1, and Jishan2 station is 20, 10, and 10 MVA individually. The rated DC-bus voltage is ± 10 kV and the rated AC voltage is 10.5 kV.

Although MMC has the same hardware topology, it can work as power station or voltage station. So the operation mode of MMC based three-terminal AC/DC distribution network is versatile. In this paper, the master-slave operation mode is studied according to the engineering application, that is, Tangjiawan Station is voltage station and Jishan1 and Jishan two stations are power stations. The instantaneous-value model of MMC is too complicated to the small-signal stability analysis. In order to simplify the analysis, averaged-value model is needed.

4.1 Averaged-Value Model of Modular Multilevel Converter Power Station

According to **Eq. 14**, the differential-mode component of capacitor voltage is ac variables and can be neglected based on the switching period. The common-mode component of capacitor voltage is balanced, that is

$$\overline{v_{apc}} = \overline{v_{anc}} = \overline{v_{bpc}} = \overline{v_{bnc}} = \overline{v_{cpc}} = \overline{v_{cnc}} = v_{eqc}$$
(26)

When common-mode component of arm current is balanced, there is

$$\overline{i_j} = \frac{i_{dc}}{3} \tag{27}$$

According to Eqs 7, 9, 12, 26, 27 and the law of energy conservation, the state equation of the equivalent capacitance is

$$C_{eq} v_{eqc} \frac{dv_{eqc}}{dt} = i_{dc} v_{eqc} + \frac{2R_v v_{eqc}}{3V_{eqc}^*} i_{dc}^2 + P_g$$
(28)

where the equivalent capacitance $C_{eq} = 6C/N$ and P_g is AC active power controlled by LADRC.

According to Eqs 6, 9, 12, 26, 27, the state equation of the DC current

$$2L_0 \frac{di_{dc}}{dt} = 3u_{dc} - 3v_{eqc} - \frac{2R_v v_{eqc}}{v_{eqc}^*} i_{dc}$$
(29)

The linearized model of the Eqs 28 and 29 is

$$\begin{cases} 2L_{0}\frac{d\Delta i_{dc}}{dt} = 3\Delta u_{dc} - 3\Delta v_{eqc} - \frac{2R_{v}}{v_{eqc}^{*}}v_{eqc0}\Delta i_{dc} - \frac{2R_{v}}{v_{eqc}^{*}}i_{dc0}\Delta v_{eqc} \\ C_{eq}v_{eqc0}\frac{d\Delta v_{eqc}}{dt} = v_{eqc0}\Delta i_{dc} + i_{dc0}\Delta v_{eqc} + \frac{4R_{v}}{3v_{eqc}^{*}}i_{dc0}v_{eqc0}\Delta i_{dc} + \frac{2R_{v}}{3v_{eqc}^{*}}i_{dc0}^{*}\Delta v_{eqc} + \Delta P_{g} \end{cases}$$
(30)



where v_{eqc0} and i_{eqc0} are the steady-state operating points satisfying the following equations.

$$\begin{cases} 0 = 3u_{dc0}v_{eqc}^* - 3v_{eqc0}v_{eqc}^* - 2R_v v_{eqc0}i_{dc0} \\ 0 = 3v_{eqc}^*i_{dc0}v_{eqc0} + 2R_v v_{eqc0}i_{dc0}^2 + P_{g0} \end{cases}$$
(31)

4.1.1 $R_v = 0$

When the virtual resistance is 0, the equilibrium point is

$$\begin{cases} i_{dc0} = \frac{P_{g0}}{u_{dc0}} \\ v_{eqc0} = u_{dc0} \end{cases}$$
(32)

where v_{eqc0} is determined by the external DC voltage source. i_{eqc0} is determined according to the law of conservation of power. The different choices of v_{eqc}^* don't affect the equilibrium points.

4.1.2 $R_v \neq 0$

When the virtual resistance is nonzero, the equilibrium point is

$$i_{dc0} = \frac{-\left(2R_v P_{g0} + 3u_{dc0}v_{eqc}\right) + \sqrt{4R_v^2 P_{g0}^2 + 9u_{dc0}^2 v_{eqc}^2 - 12u_{dc0}R_v v_{eqc}^2 P_{g0}}{4u_{dc0}R_v}}{v_{eqc0}}$$
(33)
$$v_{eqc0} = \frac{3u_{dc0}v_{eqc}}{3v_{eqc}^2 + 2R_v i_{dc0}}$$

where v_{eqc}^* and R_v affect the equilibrium point. The characteristic equations of the system **Eq. 30** is

$$a_2s^2 + a_1s + a_0 = 0 \tag{34}$$

where $a_2 = 3L_0 C_{eq} v_{eqc}^* v_{eqc0}$, $a_1 = f(x) = 3v_{eqc0}^2 C_{eq} R_v - 2L_0 i_{dc0}^2 R_v - 3L_0 i_{dc0} v_{eqc}^*$, $a_0 = v_{eqc0} (3v_{eqc}^* + 2i_{dc0}R_v)^2$. According to the Routh

criterion, the MMC power station is stable only when the following equation is satisfied

$$\begin{cases} f(x) > 0\\ R_{\nu} \neq -\frac{1.5\nu_{\text{eqc}}^{*}}{i_{dc0}} \end{cases}$$
(35)

So the stable region is that $R_v > 3L_0 i_{dc0} v_{eqc}^* / (3v_{eqc0}^2 C_{eq} - 2L_0 i_{dc0}^2)$ and $R_v \neq -1.5 v_{eqc}^* / i_{dc0}$.

4.2 Small-Signal Stability Analysis of Three-Terminal Modular Multilevel Converter Based DC Grid

The nodal equation of DC network is

$$\begin{cases}
L_{L1} \frac{di_{dc1}}{dt} = E_0 - u_{dc1} \\
L_{L2} \frac{di_{dc2}}{dt} = E_0 - u_{dc2} \\
L_{L3} \frac{di_{dc3}}{dt} = E_0 - u_{dc3} \\
i_{dc1} + i_{dc2} + i_{dc3} = 0
\end{cases}$$
(36)

where L_{L1} , L_{L2} , L_{L3} are the DC line inductors and E_0 is the nodal voltage at the common connection point.

The relationship between voltage and current of MMC3 is

$$E_0 = L_{L3} \frac{di_{dc3}}{dt} + E$$
 (37)





where *E* is the terminal voltage of MM3, the output voltage control can be seen in (Li et al., 2018) and is not discussed in detail in this paper.

According to (**Eq. 30**, **36**, **37**), the linearization model of threeterminal MMC based DC grid is

$$\begin{cases} (2L_{0} + 3L_{L1} + 3L_{L1})\frac{d\Delta i_{dc1}}{dt} + 3L_{L3}\frac{d\Delta i_{dc2}}{dt} = 3\Delta E - \frac{2R_{v1}}{v_{eqc}}v_{eqc10}\Delta i_{dc1} - \left(\frac{2R_{v1}}{v_{eqc}}i_{dc10} + 3\right)\Delta v_{eqc1} \\ C_{eq}v_{eqc10}\frac{d\Delta v_{eqc1}}{dt} = \left(v_{eqc10} + \frac{4R_{v1}}{3v_{eqc}}i_{dc10}v_{eqc10}\right)\Delta i_{dc1} + \left(\frac{2R_{v1}}{3v_{eqc}}i_{dc10}^{2} + i_{dc10}\right)\Delta v_{eqc1} + \Delta P_{g1} \\ (2L_{0} + 3L_{L2} + 3L_{L3})\frac{d\Delta i_{dc2}}{dt} + 3L_{L3}\frac{d\Delta i_{dc1}}{dt} = 3\Delta E - \frac{2R_{v2}}{v_{eqc}}v_{eqc20}\Delta i_{dc2} - \left(\frac{2R_{v2}}{v_{eqc}}i_{dc20} + 3\right)\Delta v_{eqc2} \\ C_{eq}v_{eqc20}\frac{d\Delta v_{eqc2}}{dt} = \left(v_{eqc20} + \frac{4R_{v2}i_{dc20}v_{eqc20}}{3v_{eqc}}\right)\Delta i_{dc2} + \left(i_{dc20} + \frac{2R_{v2}i_{dc20}}{3v_{eqc}}\right)\Delta v_{eqc2} + \Delta P_{g2} \end{cases}$$

$$(38)$$



The operation condition is that active power of MMC1 is -2 MW and that of MMC2 is 1 MW, the arm inductor is 7 mH and DC inductor is 4 mH. **Figure 6** shows the root locus. there are two pairs of conjugate complex roots in the left half plane. With the increase of R_{ν} , the roots gradually moves away from the virtual axis, which indicates that R_{ν} can enhance the stability of DC grid.

Figure 7 shows the amplitude frequency characteristics of the transfer function $\Delta i_{dc3}/\Delta P_{g1}$. When there is no virtual resistor, there are two low frequency resonance points in bode diagram, which are 22.1 and 30.6 Hz respectively. The resonance peak value at 22.1 Hz is obviously higher than that at 30.6 Hz. So the main resonance frequency of DC grid is about 22.1 Hz. When the virtual resistor is added, the resonance peak value attenuates greatly. Virtual resistor can effectively suppress the system oscillation and improve the transient performance of DC grid.

5 SIMULATIONS OF MODULAR MULTILEVEL CONVERTER BASED THREE-TERMINAL AC/DC DISTRIBUTION NETWORK

The number of sub-modules N = 25. The sub-module capacitance is 13 mF and the switching frequency is 300 Hz. The phase leg reactor is 7 mH and the rated voltage of AC and DC are 10kVac and 20 kVdc individually. PSC-PWM is adopted and the electromagnetic transient model of MMC based three-terminal AC/DC distribution network is established on PSCAD.

Figures 8–13 shows the decoupling power control of MMC under LADRC and PI controller. **Figure 8** shows the simulation results of active power step response. Active power immediately responds step disturbance and changes from 1 MW to -1 MW at 0.2 s and changes from -1 to 1 MW at 0.4 s. Reactive power also instantly responds step disturbance and varies from 0.5 Mvar to -0.5 Mvar at 0.6 s and varies from -0.5 Mvar to 0.5 Mvar at 0.8 s. Obviously, it results in a little















fluctuations when step disturbance occurs momentarily. **Figure 9** is the differential-mode component of arm current which has waveform aberration. **Figure 10** are the common-mode component of arm current with a little more waveform burred. As shown in **Figure 11**, The each phase peak AC voltage output of MMC has reached at about 8.17 kV. **Figure 12** shows the derivatives of LADRC. Z_{d22} and Z_{q22} are estimated of total system disturbances. **Figure 13** is the sub-module capacitor voltages, which is balanced and the capacitor voltage difference is less than 1.2%. In briefly, the simulation results reveal that LADCR can successfully realize the decoupling control of AC active power and reactive power. Compared with PI controller, LADRC has the advantages of smoother power waveform, faster response and lower overshoot, etc.

Figure 14 shows variables of MMC based three-terminal DC grid. When there is no virtual resistor, there exists continuous oscillation in DC-bus voltage, DC current, capacitor voltage and active power because of lack of damping. When virtual resistor is added at t = 0.5 s, the oscillation can be suppressed quickly and the DC-bus voltage can be stabilized at 20 kV. When virtual

resistor is added, the stable capacitor voltage is determined by the active power.

6 EXPERIMENTS ON TANGJIAWAN-JISHAN1-JISHAN2 AC/DC DISTRIBUTION NETWORK

6.1 Active Power Step Test of Jishan2 Station

Figure 15 shows system waveforms with active power step change in Jishan2 station. As shown in **Figure 15A**, active power of Jishan1 station is constant at -1 MW. Active power of Jishan2 station changes from -1 to 1 MW at t_1 , and from 1 MW to -1 MW at t_2 . Accordingly, active power of Tangjiawan station changes from 2 to 0 MW at t_1 , and from 0 to 2 MW at t_2 . Reactive power of Jishan1 remains 1 MVar. There are fluctuations in reactive power of Jishan2 station and Tangjiawan station at the moment of active power



change. But reactive power of Jishan2 and Tangjiawan will remain stable at 1Mvar and 0Mvar when active power is stable as shown in **Figure 15B. Figure 15C** shows the DC-bus voltage. The DC-bus voltage of Tangjiawan station is constant at 20 kV. There exist slight fluctuations in DC-bus voltages of Jishan1 station and Jishan2 station at the moment of active power change, but the DC-bus voltages still are within the reasonable range.

6.2 Reactive Power Step Test of Jishan1 Station

Figure 16 shows the system waveforms with reactive power step change in Jishan1 station. As shown in **Figure 16A**, active power of Jishan1 station, Jishan2 station, and Tangjiawan station is constant at -1, -1, and 2 MW respectively. **Figure 16B** shows that reactive power of Jishan1 station changes from -1Mvar to 1Mvar at t_1 , and changes from 1 to -1 Mvar at t_2 . Reactive power of Jishan2 station and Tangjiawan station remains unchanged at -1Mvar and 0Mvar individually. **Figure 16C** shows that the

reactive power step change of Jishan1 has influence on the DC-bus voltages of Jishan1 station and Tangjiawan station, but the DC-bus voltage is still within the reasonable range.

6.3 Active Power Flow Reversal Test of Jishan1 Station

Figure 17 is the experiments results of active power reversal test of Jishan1 station. **Figure 17A** shows that Jishan2 station active power still remains 1 MW when Jishan1 station active power flow reversal starts at t_1 and completes at t_2 with active power varies from 1 to -1 MW. However, Tangjiawan station active power responds flow reversal immediately with active power varies from -2 to 0 MW. **Figure 17B** shows that active power flow reversal can influence reactive power of Jishan1 and Tangjiawan station, but the reactive power of Jishan1 station and Tangjiawan station still remains 1 Mvar and 0 Mvar respectively with waveform burr increased.



Figure 17C shows that DC bus voltage of three converters station is stable at 20 kV, but Jishan1 station DC bus voltage fluctuates a little bit more than other two stations.

7 CONCLUSION

A novel instantaneous-value model of MMC is derived by introducing the differential-mode and common-mode component representation. Then, ac power modeling of MMC is established based on the PSC-PWM strategy and power decoupling control based on LADRC is realized. Simplified average-value model of MMC power station is deduced and the small signal stability analysis of three-terminal DC grid is carried out. Simulation and experiment results of the Tangjiawan-Jishan1-Jishan2 AC/DC distribution network demonstration project verify that the power decoupling strategy based on LADRC is better than PI controller. The virtual resistor can greatly improve the transient performance and system stability. The modeling and analysis method proposed in this paper is concise and effective. The research of this paper is the expansion of MMC application in the field of distribution network. MMC based multi-terminal AC/DC distribution network can realize the multidirectional power flow among AC-grids, which can greatly improve the power supply reliability.

DATA AVAILABILITY STATEMENT

The raw data supporting the conclusion of this article will be made available by the authors, without undue reservation.

AUTHOR CONTRIBUTIONS

ZX and HY: conceptualization, data curation, methodology and writing. GZ and LL: formal analysis and simulation study. PW and CJ: reagents/materials/analysis.

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