



Bypass Inductor Type LCL Filter Parameter Optimization for Three-Level Grid-Connected Converter

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Li N, Zhang S, Xiao Z, An Z, Zhang Y and Jiang L (2022) Bypass Inductor Type LCL Filter Parameter Optimization for Three-Level Grid-Connected Converter. Front. Energy Res. 10:852046. doi: 10.3389/fenrg.2022.852046 In the application of the three-level grid-connected system, passive damping is the most common method to keep the LCL filter working stably. However, in the case of high power density, the low switching frequency of the grid-connected converter results in the complexity of filter parameter design, and the increase in passive components leads to lower equipment utilization efficiency. To solve the above problems, based on the optimization of resonant frequency and system loss, this study proposes a set of LCL filter parameter design processes of a three-level neutral point clamped (NPC) converter, which can switch components freely and is easy to achieve. This study explores the theoretical evidence and application value of the proposed design, considering the influence of the current ripple and the reactive power limit. The design adopts the improved passive damping method to select the appropriate inductance ratio and impedance ratio to make the resonance frequency of the whole system and the extra loss of the system smaller. The simulation and experiments show that compared with the conventional method, the improved design method reduces the current THD of the grid side by 1.5% and the damping resistance loss by 0.17%.

Keywords: LCL filter, passive damping, stability, voltage-source converters, bypass inductor

1 INTRODUCTION

The three-level NPC grid-connected converter is a kind of multi-level grid-connected converter extensively used in a distributed generation dominated by solar energy and wind energy (Yao et al., 2017). In order to meet the network access standards, a grid-connected filter is introduced between the three-level NPC converter and power grid. Usually, the first-order L-type filter is too bulky and vulnerable to harmonic resonance, which attenuates the system's dynamic performance, and the voltage drop is relatively high (Kouchaki and Nymand, 2018). The third-order LCL filter meets the harmonic attenuation requirements even at a lower switching frequency, and the total inductor is smaller (Xiong et al., 2020). However, it has two zero-impedance resonance points, which amplify the current harmonics at the resonance frequency and affect the system stability, causing resonance. There are two methods for reducing resonance of LCL filter: active damping (Liu et al., 2021) and passive damping (Albatran et al., 2018). Compared with the passive damping method, the active damping method avoids the use of passive components and reduces the loss of passive components, but at the cost of the increasing control complexity (Beres et al., 2016a).

Active damping is the preferred control method when the power supply system is "weak" and the impedance change is not large. The dual-loop grid current control technology based on capacitor-current feedback is extensively used for the LCL filter system (Liu et al., 2018). In addition, Zeng et al. (2016) presented an active damping method that reshapes the harmonic impedance of the grid to suppress resonance. Simultaneously, it is necessary to detect the harmonic components in the grid voltage and current and has high real-time requirements (Xia and Kang, 2017). When the system's damping coefficient exceeds the critical value or reaches a certain resonance frequency, active damping can suppress resonant peaks (Guzman et al., 2018). In fact, the active damping strategy transfers the real damping resistance to the controller through the transformation of the transfer function (Liserre et al., 2005), and the effect is equivalent to a virtual resistor in series or parallel connected with the capacitor. It does not cause extra power loss and has a concise physical meaning, but the conventional active damping scheme requires additional current or voltage sensors (Falkowski and Sikorski, 2018), increasing system cost. In addition, active damping excessively relies on accurate parameter matching and is more sensitive to grid impedance and control parameters (He et al., 2017). Therefore, it needs a complex calculation to select the active damping coefficient and enhance the system's robustness (He et al., 2017).

Passive damping realizes the damping effect by adding actual passive components to the filter circuit, which is simple to operate at a low cost (Su et al., 2019). Young et al. (2020) proposed a new type of passive damping LCL filter based on coupled inductance, which obtains better high-frequency harmonic attenuation ability. Guo et al. (2010) compared the two passive control strategies of series and parallel resistance in the LCL filter system and concluded that series resistance reduces the extra loss. A hybrid control strategy combining active and passive damping is adopted to improve the adaptability of grid inductance and control delay (Wei and Gao, 2017). Although the above studies have improved the passive damping control strategy to some extent, they have not solved the difficulty of filter parameter design at a high power level and low switching frequency. After introducing passive components, some issues are still unsolved, such as large reactive power loss, low power factor, and decreased equipment utilization.

This study proposes an engineering design method of LCL filter based on passive control strategy (Zhang et al., 2021). The proposed method first analyzes the significance of resonance frequency in reactive power compensation and harmonic reduction (Xiong et al., 2021a). Then, the best damping topology is determined based on the attenuation curve of the LCL filter (Ben Saïd-Romdhane et al., 2017). Finally, a simple and effective design is made to obtain the appropriate component parameters by deducing the relationship between the filter parameters. The function of switching components is freely achieved according to different requirements similar to digital filters (Xiong et al., 2021b).

The main contributions of this study are as follows: 1) a passive damping strategy is used to reduce the resonant peak of the LCL filter, and the improved strategy is used to design the impedance ratio so that the equipment power factor and utilization efficiency of the entire filtration system are better improved. 2) Based on



inductor optimization, a convenient parameter design method of the current-controlled bypass inductor type LCL grid-connected converter is made. The design steps are simple, and the components are switched freely according to different needs.

The rest of this study is arranged as follows: **Section 2** introduces a three-level grid-connected NPC converter with an LCL filter, **Section 3** analyzes the relationship between the filter parameters, **Section 4** discusses the proposed method with design examples, and **Section 5** lists the experimental results to verify the effectiveness of the method.

2 SYSTEM DESCRIPTION OF THREE-LEVEL GIRD-CONNECTED NPC CONVERTER

2.1 System Description and Modeling

Figure 1 shows the structure of the three-level grid-connected NPC converter with the LCL filter (Bosch et al., 2018). u_{sx} (x = a, b, c) represents the grid-side phase voltage; u_{dc} is the DC capacitor voltage (Huang et al., 2019); C_1 , C_2 are the upper and lower DC capacitors, respectively; $S_{1x} \sim S_{4x}$ represent the four switching devices of the *x*-phase, respectively; L_1 is the converter side inductor; L_2 is the grid-side inductor, C is the filter capacitor, Rf is the damping resistor, and L_1 , C_2 , C, and R_f constitute a typical passive damping LCL filter (Zheng et al., 2019).

A single-phase model is taken for analysis as shown in **Figure 2A**, where i_i is the inverter-side current, i_g is the gridside current, and i_c is the current flowing through the filter capacitor C. The converter's output voltage is equivalent to the sum of the fundamental voltage and the harmonic voltage; u_x and u_H represent the fundamental and harmonic components, respectively; and u_{ON} represents the voltage difference between point N and midpoint O, whose value is obtained by **Eq. 1**. Here, S_x represents the switching function of phase x:

$$\begin{cases} U_{ON} = -\frac{1}{3} \sum u_x \\ u_x = S_x \cdot U_{dc}/2 \end{cases} \quad (x = a, b, c, S_x = 1 \text{ or } 0 \text{ or } -1). \quad (1)$$

The high-frequency equivalent model of the LCL filter is obtained by ignoring the harmonic component in i_{g_2} as shown



in **Figure 2B**. In this linear system, the relationship between the grid-side current and the inverter-side voltage and between the grid-side current and inverter-side current is obtained according to the superposition theorem as follows:

$$\frac{I_g(s)}{U_H(s)} = \frac{R_f C s + 1}{L_1 L_2 C s^3 + (L_1 + L_2) R_f C s^2 + (L_1 + L_2) s}$$

$$\frac{I_g(s)}{I_i(s)} = \frac{R_f C s + 1}{L_2 C s^2 + R_f C s + 1}$$
(2)

The relationship between i_g and U_H can be seen in the above equations, including an integral term, a zero point, and a quadratic term. Thus, the conventional LCL transfer function has a resonant peak at zero point, causing a stability problem, but the high-frequency attenuation rate reaches -60 dB/dec. Then, the angular frequency of the system undamped oscillation ω_{res} and the damping ratio ζ can be determined as follows:

$$\omega_{res} = \sqrt{\frac{L_1 + L_2}{L_1 L_2 C}}, \qquad (3)$$

$$\xi = \frac{\omega_{res} R_f C}{2} \ . \tag{4}$$

The zero point of the LCL filter system is calculated as $\omega_z = 1/(RC)$, and the relationship between the undamped oscillation angular frequency and the zero point can be determined as

$$2\xi = \frac{\omega_{res}}{\omega_z} \ . \tag{5}$$

Based on Eqs. 2, 5, relist the functional relationship between i_g and U_H as follows:

$$\frac{I_g(s)}{U_H(s)} = \frac{\frac{2\xi}{\omega_{res}}s + 1}{(L_1 + L_2)s(\frac{1}{\omega_{res}}s^2 + \frac{2\xi}{\omega_{res}}s + 1)} .$$
(6)

In summary, the external characteristics of the passive damping LCL filter are determined by $L_1 + L_2$, the damping coefficient, and the undamped oscillation angular frequency (Zheng et al., 2019).

2.2 Topology Contrast

The direct measure of passive damping to eliminate LCL resonance peaks is to connect inductor and capacitor elements in series or in parallel. Then, six methods are classified based on the position of the added devices (Kim and Kim, 2019). Adding a resistor directly at the position of the inductor element obviously affects the harmonics' attenuation ability. Therefore, it is better to connect the resistor in series (**Figure 1** and **Eq. 2**) or in parallel to the capacitor. The transfer function of the parallel resistor at the capacitor position is listed as follows:

$$G_{parallel}s = \frac{i_g s}{U_H s} = \frac{1}{s^3 L_1 L_2 C + s^2 L_1 L_2 / R_f + s L_1 + L_2} .$$
 (7)

Figure 2C shows the Bode diagram of the LCL filter with a series or parallel resistor on the capacitor. Comparing the Bode diagram and the transfer functions between **Eq. 2** and **Eq. 7**, it can be found that the series resistor causes the branch circuit to



FIGURE 3 | Equivalent model and Bode diagrams of four improved topologies. (A) Bypass inductor. (B) Bypass inductor plus capacitors. (C) Split capacitors. (D) Split capacitor plus bypass inductor. (E) Bypass elements. (F) Split capacitor.

TABLE 1 The characteristics of four improved topologies.				
Improved topologies	Characteristics			
Fig. 4a	1. The series resistor maintains the LCL attenuation ability of high-frequency harmonics			
Fig. 4b	 The inductor reduces damping loss The split capacitor improves the LCL attenuation ability of high-frequency harmonics 			
	 The inductor reduces damping loss More components lead to increased costs and the complex design/enleadertwooldts. 			
Fig. 4c	1. The split capacitor improves the LCL attenuation ability of high-frequency harmonics			
Fig. 4d	1. The split capacitor improves the LCL attenuation ability of high-frequency harmonics			
	2. The inductor reduces damping loss			
	3. More components lead to increased costs and the complex design			

reduce the attenuation ability of high-frequency harmonics, whereas the parallel resistor does not affect the attenuation ability of high-frequency or low-frequency harmonics, but it bears the equivalent voltage on the capacitor causing greater system loss. Comparing two passive damping strategies, this study chooses the capacitor series resistor method due to its lower extra loss of the system.

Four improvement methods and their frequency characteristics (Xiao et al., 2018) are compared to solve the problem that the passive damping method selected in this study increases the loss of the filter system, as shown in **Figure 3.** From the comparison between the Bode diagram and **Table 1**, it can be seen that the improved topology shown in **Figure 3A** has improved both in terms of system loss and design simplicity. By connecting a small L_f in parallel with R_f (Wang et al., 2018), the impedance of L_f connected in parallel at the fundamental frequency is much less than R_f . The low impedance becomes the main current flow path so that the fundamental wave loss on R_f is reduced, and the parallel

relationship between the small inductor and the resistor at a high frequency reduces the total impedance of the branch and the extra loss of the system.

2.3 The Mathematical Model of the System

According to the passive damping improved topology, is defined as the impedance ratio of L_f to R_f at the switching frequency, as shown in **Eq. 8**, and the transfer function of the system is shown in **Eq. 9**:

$$\alpha = \omega_{s}L_{f}/R_{f} , \qquad (8)$$

$$\left\{ \frac{I_{g}(s)}{U_{H}(s)} = \frac{R_{f}L_{f}Cs^{2} + L_{f}s + R_{f}}{\left(\begin{array}{c} L_{1}L_{2}L_{f}Cs^{4} + \left[L_{1}L_{2} + (L_{1} + L_{2})L_{f} \right] R_{f}Cs^{3} \\ + (L_{1} + L_{2})L_{f}s^{2} + (L_{1} + L_{2})R_{f}s \end{array} \right) .$$

$$\left\{ \begin{array}{c} I_{g}(s) \\ I_{i}(s) \end{array} = \frac{R_{f}L_{f}Cs^{2} + L_{f}s + R_{f}}{L_{2}L_{f}Cs^{3} + \left(L_{2} + L_{f} \right) R_{f}Cs^{2} + L_{f}s + R_{f}} \end{array} \right.$$

$$(9)$$

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3 INTERRELATIONSHIP BETWEEN PARAMETERS OF LCL FILTER 3.1 Constraints on Canacitor C and Tot

3.1 Constraints on Capacitor C and Total Inductor L_T

In the design process of the LCL filter, first, determine the size of capacitor C and inductor L_T . When the filter system is working normally, C has a high impedance characteristic in the low-frequency band and a low impedance characteristic in the high-frequency band. Under the same filtering effect, using a larger C reduces the used total inductor value (Beres et al., 2016b). However, an excessive C value increases the reactive current flowing into the capacitor and reduces the power factor and the system efficiency. The upper limit of C should be calculated based on the fundamental capacitive reactive power allowed by the system (Kumar et al., 2020).

When designing the inductor, C and R_f branches are usually considered open circuits so that the effect of the LCL filter is equivalent to that of an L-type filter with a total inductor of L_T , which is convenient for parameter design. The design criteria of L_T should minimize the current harmonics and meet the requirements of fast current tracking, the ripple requirements of the grid-connected current determine the lower limit of L_T , and its dynamic requirements determine the upper limit of the inductor value.

3.2 Ratio k (L₁/L₇)

One of the goals of the LCL filter design is to reduce the size of passive components as much as possible, while ensuring that sufficient harmonic attenuation and reactive power are compensated by the filter. According to the analysis of **Eqs. 3**, **4**, under the premise of determining parameters L_T and C, the proportional relationship between two inductor values also affects the angular frequency of the undamped oscillation of the system ω_{res} . Hence, it is necessary to carry out research on the proportional relationship between the inductors:

$$L_1 = k L_T , \qquad (10)$$

where k is the proportion of the inductor on the inverter side.

Then, the undamped oscillation frequency f_{res} of the LCL filter is expressed as



$$f_{res} = \frac{1}{2\pi} \cdot \sqrt{\frac{1}{k(1-k)L_TC}} \,. \tag{11}$$

Figure 4A is a diagram of the relationship between f_{res} and k at undamped oscillation frequency, which shows that the resonance frequency is the lowest when k is 0.5.

According to the relationship of **Eqs. 3, 4, 10, 11** is transformed into the expression of ω_{res} , ξ , and k:

$$\frac{I_g(s)}{U_H(s)} = \frac{1}{L_T s} \frac{(2\xi \omega_{res} s + \omega_{res}^2)}{s^2 + 2\xi \omega_{res} s + \omega_{res}^2} .$$
(12)

The equivalent switching frequency f_s is set to 3 kHz as shown in Figure 4B, Assuming that the equivalent switching frequency fs is 3 kHz, the value of ξ changes from 0.1 to 0.7, and *k* is 0.1 (0.9), 0.2 (0.8) and 0.5, respectively, the amplitudes of i_g and U_h are shown in Figure 4B. The closer k is to 0.5, the lower the resonance frequency of the LCL filter is and the better the filtering effect on the higher harmonics generated by the modulation strategy is. The larger ξ is, the better the suppression effect of the resonance peak is. However, the increase in ξ makes the LCL filter's attenuation effect on high-frequency ripple worse. Figure 4C shows the relationship between the ratio of i_{e} to the absolute value of i_i and k or ξ . From **Figure 4C**, it can be found that when k and ξ are small, the value of $|i_g/i_i|$ is also small. When k or ξ is large, the value of $|i_o/i_i|$ is also large, especially when k is close to 1 and ξ is close to 0, and $|i_{e}/i_{i}|$ may even be greater than 1. At this time, the harmonics injected by the LCL filter into the grid are greater than those of the converter side. The function of the LCL filter changes from filtering out the harmonic voltage and current, in a general sense, to amplifying harmonic currents.

3.3 Relation Between Ratio *k* and Other Key Parameters

Based on Eqs. 3, 4, it can be seen that k and R_f also affect each other.

Figure 5 shows the relationship between the power loss of the LCL filter, the inductor proportional coefficient k, and R_{f} . **Figure 5** assumes that the harmonic energy is concentrated at

the switching frequency. The larger the value of k, the smaller the loss. When the value of k is the same, the fundamental loss and switching subharmonic loss first increase and then decrease with R_f . Through the above analysis, the relationship among the ratio of correlation coefficient of the system, k, and ξ is obtained. The values of k and ξ that optimize all system performance cannot be found. Thus, a compromise should be made in the practical design, according to the operation situation.

- When k = 0.5, f_{res} is minimal. When k approaches 0(1), f_{res} gets larger.
- 2) As ξ increases, the smaller the resonance peak is, the larger the high-frequency attenuation is and vice versa.
- As k or ξ increases, |i_g/i_i| gets larger; k is the main influencing factor of |i_g/i_i|.
- As k increases, damping resistor power loss (P_{loss}) gets smaller. As ξ increases, P_{loss} increases firstly and then decreases*.

* ξ is proportional to R_f when k is determined.

3.4 Impedance Ratio α

It is obtained from Part B that the system reaches the lowest resonant frequency when *k* is 0.5, and Bode diagrams of each transfer function are obtained under the condition that R_f is a fixed value and α is different based on Eq. 8, as shown in Figures 6A–C.

From **Figures 6A,B**, it can be seen that the LCL filter with the damping inductor has a lower resonance frequency and better high-frequency harmonic filtering effect than that without the damping inductor. However, it also has the disadvantage of large gain at a resonance frequency. **Figures 6A–C** show that the harmonic current gain at the resonance frequency decreases as α increases. Because the harmonics are mainly concentrated in the part above the switching frequency, this disadvantage has little effect on the filtering effect. It can be seen from **Figure 6C** that the introduction of L_f basically does not affect the fundamental wave current flowing into C, and in terms of switching sub-harmonic currents, the introduction of the damping inductor reduces the flow of C. The switching sub-harmonic current is slightly smaller than the switching sub-harmonic current without the damping inductor.



Figure 6D shows the damping resistance loss ratio of the LCL filter with and without damping inductance. It can be seen from **Figure 6D** that, as α increases, both the fundamental wave loss The specific operation steps are as follows:

1) Select the basic damping and improved topology.

- 2) Determine k and α based on Parts 3.1 and 3.4.
- 3) Design L_T and C in the LCL filter.
- 4) Determine the R_f range achieved by the maximum attenuation ratio k and α at the resonant angular frequency and the equivalent switching angular frequency.
- 5) Properly select the inductor value and then solve the actual inductor value on the grid-connected and the inverter sides.
- 6) Perform a physical design based on harmonic attenuation analysis and THD analysis of the filter.

4.1 Calculating the Total Capacitor C

As power factor and efficiency lead to problems in the system, the upper limit of *C* needs to be considered:

$$C \le \frac{Q_C}{\omega_0 U_{line}^2} = \frac{bP_{rated}}{\omega_0 U_{line}^2} .$$
(13)

In Eq. 13, Q_c is the fundamental capacitive reactive power allowed by the system, P_{rated} is the total power of the system, b is

filter with and without damping inductance. It can be seen from **Figure 6D** that, as α increases, both the fundamental wave loss ratio and the switching harmonic loss increase because L_f has a weak impedance to the fundamental wave, so the fundamental wave current flowing through C basically flows into L_f , and the fundamental wave loss on the damping resistor is small. As α increases, L_f also increases accordingly, which increases the system cost. Therefore, this study chooses $\alpha = 1$. At this time, the loss on the damping resistor of the LCL filter with damping inductor is 1/4-1/3 of that without the damping inductor.

4 DETERMINATION OF FILTER PARAMETERS

Based on the analysis process of Part 3, the design parameters of the LCL filter k and α is set to 0.5 and 1. Under this condition, the resonant system frequency is the lowest and the damping loss is the smallest.

Figure 7 presents the algorithm proposed in this study. The LCL filter is operated as an integrated filter unit instead of separately considering the effects of the grid-connected and the inverter sides. The steps of this design are simple and





the percentage of capacitive reactive power in the total power, ω_0 is the grid voltage angular frequency, and U_{line} is the grid line voltage valid value.

4.2 Calculating the Total Inductor L_T

The lower limit of L_T is determined by the ripple requirements of the grid-connected current, and its upper limit is determined by dynamic requirements.

4.2.1 Ripple Requirements of Current

Figure 8A shows a diagram of the output phase voltage pulse waveform near the peak of the *a*-phase current of the three-level converter. At this time, the *a*-phase voltage of power grid reaches its maximum E_m (system power factor is 1). According to the basic principle of the circuit, the peak-to-peak values of Δi_{pp1} and Δi_{pp2} during the rise and fall of the inductor current are obtained:

$$\begin{cases} \Delta i_{pp1} = \frac{U_{dc}}{6} \cdot \frac{(2 - S_{b1} - S_{c1}) - E_m}{L_T} \cdot d_a T_S \\ \Delta i_{pp2} = \frac{U_{dc}}{6} \cdot \frac{(-S_{b0} - S_{c0}) - E_m}{L_T} \cdot (1 - d_a) T_S \end{cases}$$
(14)

 S_{b1} , S_{b0} and S_{c1} , S_{c0} are the switching states of phases *b* and *c* when the switching states of phase *a* are 1 and 0, respectively. In a three-phase symmetric system, it can be known from the circuit theory that when a certain voltage or current reaches its maximum value, the other two phase voltages or currents are -0.5 times the maximum value. The deduction is as follows:

$$S_{xy} = 0or - 1 (x = b, c; y = 0, 1) .$$
(15)

In order to maintain the stability of the system, Δi_{pp1} should be equal to $-\Delta i_{pp2}$. Then, based on **Eqs. 14**, **15**, the maximum ripple current Δi_{max} of phase *a* can be obtained as follows:

$$\Delta i_{\max} = \frac{2U_{dc}^2 + 3U_{dc}E_m - 9E_m^2}{18L_T U_{dc}} T_S .$$
 (16)

When the maximum ripple required by the system is $I_{rippleM}$, the total inductor L_T needs to meet

$$L_T \ge \frac{2U_{dc}^2 + 3U_{dc}E_m - 9E_m^2}{18I_{rippleM}U_{dc}}T_S .$$
(17)

4.2.2 Requirements of Current Tracking Rapidity

Figure 8B is the diagram of output phase voltage pulse and the *a*-phase ripple current near the zero-crossing point of the *a*-phase current of the three-level converter. According to the basic principle of the circuit, the peak-to-peak value Δi_{pp1} and Δi_{pp2} during the rise and fall of the inductor current are obtained:

$$\begin{cases} \Delta i_{pp1} = \frac{U_{dc}}{6} \cdot \frac{(2 - S_{b1} - S_{c1})}{L_T} \cdot d_a T_S \\ \Delta i_{pp2} = \frac{U_{dc}}{6} \cdot \frac{(-S_{b0} - S_{c0})}{L_T} \cdot (1 - d_a) T_S \end{cases}$$
(18)

According to the circuit theory, the voltages of phases *b* and *c* are $-\sqrt{3}/2$ and $\sqrt{3}/2$ times the maximum value, respectively, when the current of phase *a* crosses zero. The derivation is as follows:

$$\begin{cases} S_{by} = 0or - 1(y = 0, 1) \\ S_{cy} = 0or 1(y = 0, 1) \end{cases}$$
(19)

In order to meet the needs of fast current tracking, Δi_{pp1} and Δi_{pp2} need to meet the following relationship:

$$\frac{\Delta i_{pp1} - \Delta i_{pp2}}{T_s} \ge \frac{I_m \sin(\omega T_s)}{T_s} \approx I_m \omega \tag{20}$$

As d_a is approximately equal to 1 at the zero-crossing point of the *a*-phase current, the total inductor L_T needs to meet

$$L_T \le \frac{U_{dc}}{6I_m \omega} \quad . \tag{21}$$

Based on the above analysis, the total inductor of the LCL filter is obtained based on the ripple current and current tracking conditions, as shown in **Eq. 22**. Due to cost and volume, L_T should be as close to its lower limit as possible in practical applications of high-voltage and high-power systems:

$$\frac{2U_{dc}^{2} + 3U_{dc}E_{m} - 9E_{m}^{2}}{18I_{rippleM}U_{dc}}T_{S} \le L_{T} \le \frac{U_{dc}}{6I_{m}\omega} .$$
 (22)

4.3 Damping Resistor R_f

After the range of C and L_T are determined, R_f is designed according to the correlation degree of the parameters in part 3.

1) The lower limit is designed according to the attenuation effect at the resonant angular frequency from **Eq. 9**, at the value of ω_{res} , and the amplitude ratio of i_g to u_H is obtained as

$$\frac{|\dot{i}_g|}{|u_o|} = \frac{\sqrt{4\xi^2 + 1}}{2\xi L_f \omega_{res}} = \frac{\sqrt{1/(4\xi^2) + 1}}{L_f \omega_{res}} .$$
(23)

If the system requires the maximum attenuation at ω_{res} to be κ (0 < κ < 1), then

$$\xi = \frac{\omega_{res} R_f C}{2} \ge \frac{1}{2\sqrt{\left(\kappa L_f \omega_{res}\right)^2 - 1}} \quad (24)$$

Based on the above derivation, the lower limit value that R_f should meet when k = 0.5 is calculated:

$$R_f \ge \frac{1}{\omega_{res}C\sqrt{\left(\kappa L_f \omega_{res}\right)^2 - 1}} = \frac{1}{\sqrt{16\kappa^2 - 4C/L_f}}$$
(25)

2) Determine the upper limit value according to the current harmonic attenuation ratio from Eq. 9, at ω_{res} , and the amplitude ratio of i_g to i_i is obtained as

$$\frac{|\dot{i}_g|}{|u_o|} = \frac{\sqrt{4\xi^2 \omega_{res}^2 \omega_s^2 + \omega_{res}^4}}{\sqrt{4\xi^2 \omega_{res}^2 \omega_s^2 + (\omega_{res}^4 - \omega_s^2/k)}}$$
(26)

When k = 0.5, $\omega_{res} \ge 2\omega_{res}$, if the system requires the current harmonic attenuation ratio at the equivalent switching frequency ω_s to be the maximum γ (0 < γ < 1), then

$$R_{f} \leq \frac{\sqrt{49\gamma^{2} - 1}}{2\omega_{res}C\sqrt{1 - \gamma^{2}}} = \frac{1}{4}\sqrt{\frac{(49\gamma^{2} - 1)L_{f}}{(1 - \gamma^{2})C}}$$
(27)

Considering the attenuation effect at the resonance frequency and the current harmonic attenuation ratio, the upper and lower limits of R_f is obtained as follows:

$$\frac{1}{\sqrt{16\kappa^2 - 4C/L_f}} \le R_f \le \frac{1}{4} \sqrt{\frac{(49\gamma^2 - 1)L_f}{(1 - \gamma^2)C}} .$$
 (28)

4.4 Design Example

In order to verify the design method proposed above, the minimum resonant frequency and the system loss are considered to design the LCL filter under the parameters as shown in **Table 2**. The overall design steps are as follows.

- 1) Control grid-side current and obtain the topology of series resistor with small parallel inductor shown in **Figure 3A**, which suppresses the resonant peak and reduces loss of the resistor.
- 2) Set k and α as 1, according to the principle of the minimum resonant frequency.
- 3) Determine the range of capacitor and total inductor according to **Eqs. 13**, **22**, and determine the range of added R_{f_2} based on the harmonic attenuation ratio.

5 SIMULATION AND EXPERIMENTAL RESULTS

Based on the previous theoretical analysis, selecting a decimal point in device selection increases the difficulty of industrial design. Therefore, integer parameters are selected and the three-level NPC type SVG simulation and experimental model is established in MATLAB to verify the analysis of this study. The circuit schematic diagram is shown in **Figure 1**, and system parameters and LCL filter element parameters are shown in **Table 2**. $L_T = 6$ mH, C = 18 uF, $R_f = 1\Omega$, and $L_f = 0.08$ mH

TABLE 2 | System parameters.

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Parameters	Value	
DC capacitor C_1, C_2	2000uF	
Rated DC voltage U_d	750v	
Equivalent switching frequency f_s	3kH _z	
Equivalent value of grid-side line voltage U_{a}	380v	
Current harmonic attenuation ratio at equivalent switching frequency γ	0.2	
Undamped resonant angular frequency attenuation κ	0.707	
Impedance ratio of damping inductor to resistor α	1	
Rated active power P_{ref}	10kW	
Total inductor L_{T}	5.497m _H ~ 18.517 8 m _H	
Total capacitor C	6.144 1u _F ~ 22.043 6 u _F	
Damping resistor R _f	0.35396Ω ~ 3.9478Ω	
Damping inductor L _f	0.018778m _H ~ 0.20944 m _H	



FIGURE 9 | Simulation and experimental results of the LCL filter under different key parameters (A) simulation result (B) experimental result.

are selected, and the simulation sampling time is 2e-6s. **Figure 9A** shows the simulation waveforms U_a , i_g , i_i , and i_R flowing through R_f during full power operation under different conditions. All the simulation results of different LCL filter parameters of each figure are shown in Table 3.

Table 4 is a quantitative comparison of the simulation and experimental results of different filter parameters. It mainly presents three aspects of the filter performance of the system, the total harmonic distortion (THD) of the i_i and that of i_g in the six cases, and $|i_g/i_i|$ and P_{loss} . From the simulation results of the above six different situations, it can be seen that the LCL filter design method based on the optimization of resonance frequency proposed in this study has advantages of small harmonic current into the grid and low damping resistor loss.

TABLE 3 | Comparison of systerm parameters.

Scheme	<i>L</i> ₁ (mH)	<i>L</i> ₂ (mH)	<i>L_f</i> (mH)	R _f (Ω)	С
A	3	3	0.08	1	18u _F
В	3	3	0	1	18u _F
С	5	1	0	1	18u _F
D	5.5	0.5	0	1	18u _F
E	3	3	0	0	18u _F
F	5	1	0	0	18u _F

Specifically, comparing Figure 9A with the relevant content in Table 4, it can be found that when the inductor ratio is the same, the existence of the damping resistor reduces the THD of the grid current because it suppresses the occurrence of resonance, and

Inductor ratio	Damping resistor	Damping inductor	THD _i	THDg	Harmonic current	Ploss
К	R _f	L _f	(%)	(%)	Ratio (<i>i_g/i_i</i>)	(W)
0.5	1	0.08m _H	5.85/6.72	0.91/2.71	0.16/0.40	4.6/8.4
0.5	1	От _н	5.89/7.06	0.94/3.46	0.16/0.49	18.1/34.2
5/6	1	От _н	3.47/5.80	1.04/3.70	0.30/0.64	17.6/30.3
11/12	1	0m _H	3.15/5.25	1.68/3.99	0.53/0.76	17.4/29.5
0.5	0	0m _H	5.92/7.11	1.02/3.61	0.17/0.51	0/0
5/6	0	0m _H	3.47/5.94	1.32/3.89	0.38/0.70	0/0

TABLE 4 | Simulation/experimental results comparison of LCL filter under different key parameters.

TABLE 5 | Simulation and experimental values of THD and loss ratio with different methods.

	Simulation THD%	Experiment THD%	Simulation P _{loss} /P _{ref} %	Experiment P _{loss} /P _{ref} %
This study	0.91	2.71	0.046	0.084
Method 1 Wang et al. (2017)	0.56	2	_	_
Method 2 Jayalath and Hanif (2017)	_	4.27	0.22	0.18
Method 3 Tang et al. (2020)	0.86	1.132	0.2165	0.229



the existence of damping inductor reduces the loss of the damping resistor to about 1/4 of the original value, which is consistent with **Figure 6D**. Comparing the three graphs on the right of **Figure 9B** with **Table 4**, it can be found that when the damping resistor is the same, the closer the inductor ratio is to 0.5, the better the filtering effect is, which is consistent with **Figure 4A** and **Figure 4B**. The larger the inductor ratio is, the greater the harmonic current amplitude ratio is, which is consistent with **Figure 4C**. The larger the inductor ratio is, the smaller the damping resistor loss is, which is consistent with **Figure 6D**.

Figure 9B shows the experimental results of corresponding waveforms of grid voltage U_{ca} , inverter-side current i_{ib} , grid-side current i_{gb} , and current i_{Rb} flowing through the damping resistor when the three-level SVG system under different LCL filter parameters is running at full power. **Table 5** shows the relevant data analysis. Because the actual grid voltage contains about 2% of low-order harmonics, the grid-side current, the inverter-side current THD, and the damping resistor power are all larger than the simulated values. It can be seen from the above

analysis that the theoretical analysis and simulation results are similar. **Figures 10A,B** show the gap between the simulation data and the real experimental results. Considering the influence of background harmonics of about 2%, the theory proposed in this study is verified by the above simulation and experimental results.

*In the case of e or f in **Figure 10B**, the damping loss is not discussed without adding a damping resistor.

In order to further verify the optimization algorithm, the key parameters of the grid-side current THD and those of damping resistor loss are compared with the methods proposed in the existing literature, as shown in **Table 5**. For the parameter THD, it can be seen that there is little difference from the simulation value of Method 3 (Tang et al., 2020). Nevertheless, due to the high harmonic content of the power grid under laboratory conditions, the experimental value is larger and the THD part of the side current has a certain effect. Compared with Method 3 in terms of THD, it can be found that the method proposed in this study is not much different, which may be due to the simulation parameter setting and measurement error. For resistor loss ratio parameters P_{loss}/P_{ref} except for Method 1 (Wang et al., 2017) that presents no measurement data, the proposed optimization method is different from the other existing methods. It makes much noticeable improvement in reducing the loss of the damping resistor.

5.1 CONCLUSION

Focusing on the correlation degree of system parameters is important for realizing a high-efficiency LCL filter. This study proposes a bypass inductor type LCL filter parameter optimization for a three-level grid-connected converter. In this design, the inductance ratio k and impedance ratio α are taken as the key variables, and it is found that the optimal undamped resonant frequency is achieved and the system loss is reduced when both κ and α equal 1 in the modified, passive damping topology of parallel inductors. In addition, the design provides a simple parameter design system. Based on the reactive power limit and current ripple, the range of parameters is calculated clearly. The design has great theoretical significance and application value through the simulation and experimental results to verify the validity of the proposed theory, which is especially suitable for large-power and low-switching frequency fields.

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DATA AVAILABILITY STATEMENT

The original contributions presented in the study are included in the article/Supplementary Material, further inquiries can be directed to the corresponding author.

AUTHOR CONTRIBUTIONS

LN is responsible for the overall structure and revision. ZS, XZ, and ZA are responsible for writing and the simulation experiment. YZ and LJ are responsible for the evaluation and improvement of the paper.

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