



# Neuromorphic Dynamics at the Nanoscale in Silicon Suboxide RRAM

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Resistive random-access memories, also known as memristors, whose resistance can be modulated by the electrically driven formation and disruption of conductive filaments within an insulator, are promising candidates for neuromorphic applications due to their scalability, low-power operation and diverse functional behaviors. However, understanding the dynamics of individual filaments, and the surrounding material, is challenging, owing to the typically very large cross-sectional areas of test devices relative to the nanometer scale of individual filaments. In the present work, conductive atomic force microscopy is used to study the evolution of conductivity at the nanoscale in a fully CMOS-compatible silicon suboxide thin film. Distinct filamentary plasticity and background conductivity enhancement are reported, suggesting that device behavior might be best described by composite core (filament) and shell (background conductivity) dynamics. Furthermore, constant current measurements demonstrate an interplay between filament formation and rupture, resulting in current-controlled voltage spiking in nanoscale regions, with an estimated optimal energy consumption of 25 attojoules per spike. This is very promising for extremely low-power neuromorphic computation and suggests that the dynamic behavior observed in larger devices should persist and improve as dimensions are scaled down.

Keywords: conductive atomic force microscopy, filament, plasticity, spiking, neuromorphic, RRAM, memristor, silicon

# INTRODUCTION

The power consumption of conventional, transistor-based computers is unsustainably high, particularly with the burgeoning of fields such as neuromorphic computing and machine learning (Thompson et al., 2021). For example, the power density of typical supercomputer might be in the order of 100 Wcm<sup>-2</sup>. This is in comparison to a human brain, which can complete hugely complex cognitive tasks with a fraction of the cost, at around 10 mW cm<sup>-2</sup> (Young et al., 2019). There is therefore a need for more energy-efficient hardware, that allows us to both perform complex processing without jeopardizing our environment.

Binary oxides are promising materials for low power, high packing density resistance switching devices (sometimes referred to as memristors, or resistive random access memory, RRAM), with the potential for both non-volatile memory applications, and in-memory and neuromorphic computation (Kim et al., 2011; Ielmini, 2016; Ielmini, 2018). RRAM offers the exciting prospect of implementing computational functionality at the device level, drastically reducing power and real-estate requirements, rather than requiring complex CMOS integration and control. To date, RRAM devices and arrays have been used to perform vector matrix multiplication, to implement training

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#### Edited by:

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#### Reviewed by:

Amirkianoosh Kiani, Ontario Tech University, Canada Gianluca Milano, National Institute of Metrological Research (INRiM). Italy

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#### Specialty section:

This article was submitted to Nanodevices, a section of the journal Frontiers in Nanotechnology

Received: 22 April 2021 Accepted: 07 December 2021 Published: 22 December 2021

### Citation:

Buckwell M, Ng WH, Mannion DJ, Cox HRJ, Hudziak S, Mehonic A and Kenyon AJ (2021) Neuromorphic Dynamics at the Nanoscale in Silicon Suboxide RRAM. Front. Nanotechnol. 3:699037. doi: 10.3389/fnano.2021.699037

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and image recognition in neural networks, and in more bespoke applications such as edge detection (Li et al., 2018; Moon et al., 2019; Zhang et al., 2019; Mannion et al., 2020), although this list is non-exhaustive.

The functionality of these oxides is dependent upon their behavior under electrical stress, with conductivity changes generally thought to result from changes in oxidation state (Mehonic et al., 2016) which, when localized, lead to the formation of nanoscale conductive channels known as filaments (Buckwell et al., 2015; Li et al., 2017). Understanding filamentary dynamics remains challenging, as it is difficult to precisely probe the electrical behavior of individual filaments (Bousoulas et al., 2018; Clarke et al., 2019). Ultimately, to develop RRAM technologies for high-density memory and computation, it is necessary to determine whether functionalities are maintained as device dimensions are minimized (Indiveri et al., 2013).

Scanning probe techniques have proven useful in studying the conduction mechanisms and breakdown modes of gate oxides at the nanoscale (Blasco et al., 2001; Porti et al., 2004; Lanza et al., 2007; Lanza et al., 2011), as well as RRAM materials and devices (Szot et al., 2007; Yoshida et al., 2008; Lanza, 2014; Bousoulas et al., 2015; Wedig et al., 2015; Lanza et al., 2019). In this work, we use conductive atomic force microscopy (CAFM) as a nanoscale electrical probe for spatial and temporal measurements of the evolution and dynamics of conductivity changes in silicon suboxide, SiOx. Our devices are fully CMOS-compatible, and have excellent non-volatile memory properties, such as low switching voltages, high endurance and long retention, as well as desirable neuromorphic dynamics such as thresholding, spiking and plasticity (Mehonic and Kenyon, 2016; Zarudnyi et al., 2018). Together, these functionalities can produce a diverse set of behaviors that emulate the connectivity and signaling of the human nervous system. Using CAFM, we show that individual filaments also demonstrate these behaviors, suggesting that the functionality of our devices should persist as they are scaled down. We also find that there is a dynamic background conductivity, that might play a crucial role in device performance. Finally, we present a study of filamentary voltage spiking indicating that an idealized device array might have an ultra-low power consumption of less than 1 µWcm<sup>-2</sup>, significantly lower than that of the human brain.

It should be mentioned that the switching mechanism for our devices (Mehonic et al., 2012a; Mehonic et al., 2012b; Mehonic et al., 2016; Mehonic et al., 2018), and for binary oxide RRAM devices in general (Waser, 2009; Ielmini et al., 2011), has been discussed extensively, including the correlation between structural and electrical changes, so in this work we will not discuss it in detail. For clarity, and for further details, the reader is directed to the extensive literature on the subject. Rather, we briefly mention here that our devices to behave according to a model in which an applied electric field produces defects in the active layer, likely oxygen vacancies. With sufficient accumulation, these defects form a conductive filament between the top and bottom electrode. This filament may then be disrupted by Joule heating under current injection (Mehonic et al., 2016; Mehonic et al., 2017a). Under electrical stress there is

an intrinsic interplay between these processes, with associated deformations occurring in the active layer and electrodes as a result of oxygen migration. We also acknowledge the active role of the environment as an oxygen source in the switching process when using CAFM. This should be equivalent to a real device with a deposited top electrode, assuming some porosity, which is typically the case for our devices with sputtered top electrodes (Cox et al., 2021).

# MATERIALS AND METHODS

A 65 nm molybdenum bottom electrode layer was sputterdeposited on a p-type silicon wafer capped with 1 µm thermally grown SiO<sub>2</sub>. An 11 nm SiO<sub>x</sub> layer was then deposited by sputtering a silicon target in an oxygen atmosphere, giving  $x \approx 1.6$ . CAFM measurements were carried out with a Bruker Icon microscope under ambient conditions using platinum-coated (Spark, NuNano and SCM-PIC, Bruker) and full-platinum (RMN-Pt, Rocky Mountain Nanotechnology) probes. The nominal spring constant and deflection sensitivity were used to estimate that the applied force was no more than 100 nN (Buckwell et al., 2019). Anodic oxidation was avoided as electrons were injected from the bottom molybdenum electrode (i.e. the stage was negatively biased with respect to the probe) and all applied voltages had a magnitude of no more than 10 V (Blasco et al., 2001; Garcia et al., 2006; Polspoel and Vandervorst, 2007; Chen et al., 2018). Data were processed and analyzed using Nanoscope Analysis v1.5, as well as Matlab (for spike counting and width classification using the function findpeaks, and fitting of spikes per second and energy per second using nonlinear and linear least squares regression, respectively). All reported voltage and current values are absolute. Note that the current detector saturation is not a limit; the real current can exceed saturation, which is around 12.3 nA at an instrumental current sensitivity (i.e. amplifier gain) of 1 nAV<sup>-1</sup> or 420 nA at a sensitivity of  $100 \text{ nAV}^{-1}$ .

In the body of this manuscript we focus on the electrical characterization of material functionality, and do not present the topographical data associated with the current data from our CAFM measurements. This is because a full description of the structural changes occurring, and their relation to the associated switching mechanism, has already been discussed extensively, as noted in our introduction. Nevertheless, in the interests of completeness, we have made some comments on topographical changes in the text, and included topography images, along with more detailed discussion, in the Supplementary Material.

# **RESULTS AND DISCUSSION**

# **Spatially-Resolved Plasticity**

Synaptic plasticity describes the enhancement and suppression of a connection between neurons or, in the case of an RRAM device, a pair of electrodes (Jaafar et al., 2017; Šuch et al., 2018). Potentiation (enhancement) and depression (suppression) of





for behavioral learning and, in electronic devices, is typically achieved by applying voltage pulses, ideally with fixed characteristics (polarity, magnitude, shape, duration) (Ielmini, 2018). We have previously demonstrated plasticity in  $SiO_x$  devices when applying such pulses using a semiconductor parameter analyzer (Montesi et al., 2016; Zarudnyi et al., 2018).

Our SiO<sub>x</sub> is amorphous, although we have observed that it has an important microstructure; density variations appear to produce defect-rich columnar features and interface roughness that facilitate filament growth (Mehonic et al., 2016; Munde et al., 2017; Kenyon et al., 2019). Indeed, the behavior of many RRAM devices is dependent on the formation of localized filaments in the active layer, rather than a bulk effect (Yang et al., 2012; Lanza, 2014; Baeumer et al., 2015; Bousoulas et al., 2015; Lanza et al., 2019). It is therefore of great interest to achieve a spatial characterization of plasticity, in order to determine the nanoscale dynamics that enable such crucial functionality. With CAFM, this may be done by observing the evolution of the current in a scanned region over time, between successive scans, under the application of a constant electrical stress.

To this end, we used CAFM to scan a  $500 \times 500$  nm area repeatedly with a constant voltage stress while mapping current, as shown schematically in Figure 1A. We applied 7.7 V, which was sufficient to produce contrast in the current images and induce persistent conductivity changes. Lower voltages did not produce these effects; we suppose this is because the probe is only briefly in contact with any point on the surface. In effect, we applied lines of voltage pulses, or 'pseudopulses', of a single magnitude and polarity to our sample. Figures 1B-E show the evolution of current in the scanned area over successive scans (the full set of scan topographies and currents is shown in Supplementary Figure S1). We note two significant features; the entire area becomes more conductive, and two highly conductive spots appear, corresponding to the tops of conductive filaments. It is worth noting that these features appear rather wide, up to around 100 nm, although their size is likely overestimated due to the size of the scanning probe and the saturated color bar. Given an estimated probe diameter of 40 nm and accounting for edge convolution, we estimate the true diameter of these features to be around 14 nm (a description of the estimation method is presented in Supplementary Figure S2). We also observe that the topography of the entire scan area becomes smoother over the course of the measurements (Supplementary Figure S1). We believe this to be caused by deformation of the SiO<sub>x</sub> and/or the molybdenum, rather than by blunting of the CAFM probe, which retained its imaging quality for subsequent scans (Supplementary Figure S3). Additionally, the presence of localized features which change gradually in contrast to each other and the surrounding area suggests that we are imaging a real sample behavior, rather than an instrumental artefact.

RRAM behavior is predominantly attributed to filament dynamics (Ielmini, 2016), in some cases in parallel with a static background resistance or capacitance (Mehonic et al., 2012a), as we have also previously observed in  $SiO_x$  (Mehonic et al., 2013). However, our observations in Figure 1 demonstrate that the background may also be dynamic, as the whole scan area was modified by electrical stress. NbOx RRAM devices have been reported to conduct via a core (filament) in parallel with a shell (background) (Li et al., 2019; Nandi et al., 2019). The combination of two parallel channels can give rise to complex oscillatory dynamics-important for neuromorphic applications. We suggest that such a core/shell model might be valid for many RRAM materials. An important consideration that emerges from this previous work is that dynamic device performance and functionality might require both a core and a shell, although in smaller devices, close to the size of an individual filament, the background component may disappear. We add the caveat that, here, the CAFM probe is a mobile electrode and does not address the whole scan area simultaneously.

Figure 2A shows the evolution of the background current compared to that through each of the filaments shown in Figures 1B-E. Up to scan 8, each region demonstrates a similar increase in mean current (i.e. the average of the



pixels enclosed by the dashed boxes in Figures 1B-E). This is schematically illustrated in Figure 2B, wherein the electric field produces defects across the whole scan area, increasing the conductivity of the active layer. This is consistent with our previous report that electrically biasing silicon oxide results in large scale changes in the distribution of oxygen (Mehonic et al., 2016). After scan 9, the maximum current in the left-hand filament increases rapidly, saturating the detector in scan 11. Notably, the mean current lags the maximum by 2-3 scans. The high maximum current suggests the full formation of a filament through the SiO<sub>x</sub>, as shown in **Figure 2C**. The slower increase in mean current suggests lateral filament growth, as the region showing high current expands (as illustrated in Figure 2D and shown experimentally in Supplementary Figure S1). We can describe these processes as a gradual potentiation of a connection between the CAFM probe and the molybdenum bottom electrode. Scans 12 and 13 demonstrate stable current, corresponding to a stable filament. However, from scan 14 onwards, the mean current decreases, though the maximum remains at saturation. This suggests that the filament may be narrowing, as shown in Figure 2E, corresponding to depression of the connection between the probe and bottom electrode, a regime in which Joule heating induces sufficient defect neutralization, through re-oxidation, disrupting the filament but not fully breaking it. We also note the onset of potentiation at scan 12 for the right-hand filament. We expect that the difference in the behavior between the two filaments (i.e. the number of scans required to potentiate the sample at each locations) results from the intrinsic stochasticity of filament formation, arising from the amorphous nature of the SiO<sub>x</sub> (local roughness and density variations).

Following the disruption of the left-hand filament in Figure 1 and Figure 2, we scanned a larger area at a reduced stress of 5.5 V to try to pause the evolution of the right-hand filament. This voltage was chosen as it was sufficient to produce contrast in the current imaging between the surrounding, pristine sample area and the stressed region, without inducing any further changes (Supplementary Figure S4 demonstrates that voltages between 1V and 5V did not produce any measurable current). Figure 3A demonstrates the current map after a single reading scan at a reduced stress of 5.5 V. Both filaments are still intact, although their positions are slightly different to Figure 1E. It is possible that we are only observing the most conductive part of each filament because the reading voltage, and thus the current, is much lower. The position and shape of the left-hand filament, in particular, is quite different. This might be a charging artefact or the result of surface contamination, or perhaps the appearance of a new filament, although this is unlikely as the reduced voltage was insufficient to produce any other change in conductivity. Topographical images show that the entire scan area, including that scanned only at 5.5 V, appears smoother than the pristine  $SiO_x$  (Supplementary Figure S4 and Supplementary Figure S5). We expect this to result from deformation of the SiO<sub>x</sub> and/or molybdenum, rather than probe blunting, as we have previously discussed (i.e. that the imaging quality of this probe was maintained, as shown in Supplementary Figure S3). Interestingly, this may suggest that some structural change occurs at voltages below 7.7 V, below that required to observe pronounced changes in electrical behavior. Alternatively, those changes occurring at



7.7 V may have a de-localized distribution within the active layer or bottom electrode, affecting an area larger than the addressed region.

The previously stressed area in **Figure 3A** is more conductive that its surroundings. **Figure 3B** shows the area following an additional 40 min of reading scans (10 consecutive scans in total). The background current and right-hand filament appear consistent with **Figure 3A**, but the left-hand filament has disappeared; this occurred after 2 scans. **Figure 3C** shows the mean current in the stressed region and the right-hand filament over the course of the reading measurements, neither of which changes significantly. Thus, we have been able to potentiate and depress a filament through the use of CAFM pseudopulses, and to stabilize a potentiated filament by reducing the applied voltage.

The consistency of the current in the stressed area during the reading measurements suggests that capacitive charging has not occurred, as we would expect the current to decrease over time if this was the case. Furthermore, such areas do not exhibit evidence of discharging when the CAFM probe is held in a fixed position, applying a constant voltage stress in either polarity. Therefore, the increase in conductivity appears to be a real, persistent effect. This



further supports a core/shell model with a significant parallel background component.

# **Neuromorphic Filamentary Dynamics**

To more directly study the temporal dynamics of filaments in our SiO<sub>x</sub>, we used CAFM to apply constant voltage stresses at fixed locations. Figure 4A demonstrates the typical time dependence of the current under a constant stress of 4 V. Initially, there is no measurable deviation in current from the noise floor (period i). The current then begins measurably increasing, initially quite slowly (period ii), then more rapidly (period iii); as a filament begins to form, the effective dielectric thickness will decrease and so the electric field will increase, causing positive feedback in the filament formation. Finally, the current jumps beyond 1 nA, then fluctuates across nearly two orders of magnitude and eventually saturates the detector (period iv). In this final period, we observe the competition between filament formation and rupture. We note that over the long duration of this measurement, some lateral drift occurred, which might lead to an overestimate of the time taken for the current to change (as shown in Supplementary Figure S6). It is also interesting that the topography of the local area appears consistent before and after the application of the voltage stress, although the addressed location shows a slight deformation. This leads us to conclude that the probe was not significantly affected by the measurement (i.e. no significant blunting or platinum deposition occurred, as imaging quality



FIGURE 5 | Spiking dynamics under constant current bias. No data processing filter has been applied. (A) The voltage response at a setpoint of 1 nA. Once the target current has been reached, the voltage spikes consistently for hundreds of seconds. (B) Spiking behavior at different current biases over 10 s. As the current setpoint is increased, we observe higher magnitude, more frequent spikes. The difference between 25 and 250 pA is clear, although between 250 pA and 2.5 nA it is less distinct.

is maintained), although the  $SiO_x$  or molybdenum might have deformed slightly.

We imaged the top of a filament periodically during voltage stressing to assess its appearance following the application of increasing current, corresponding to the periods shown in Figure 4A (i.e. a different filament to that shown in Figure 4A). We initially applied 4.5 V to produce a few pA (equivalent to period i-ii), then 6 V to produce around 10 pA (equivalent to period iii), and 6 V again to produce current fluctuations between 100 pA and 10 nA (equivalent to period iv). Figures 4B-D shows current maps at equivalent periods to those shown in Figure 4A. Following periods i and ii, we observe a spot of a few pA above background when scanning at 7.8 V. We note that this voltage might affect the conductivity of the SiO<sub>x</sub>, although we were unable to resolve the spot at lower bias. However, from the trend in Figure 2A, we would not expect the effect of imaging at 7.8 V to be significant compared to the effect at the contact location when the probe is held in place for a longer duration at lower bias. The appearance of the spot does not change significantly following period iii; the conductivity increase appears not to persist once the voltage is removed. Finally, in period iv, the spot becomes much more pronounced and we measure a current at the saturation level of 12.3 nA. This highly conductive spot persisted at the measured location, such that we were able to perform current-voltage sweeps to ±50 mV, giving us Ohmic behavior below saturation, and a resistance of around 2.3 k $\Omega$ , although we note that the contact resistance between the probe and sample is unknown. Thus, although the current is unstable in period iv, this region corresponds to the presence of a filament across the SiO<sub>x</sub>. In fact, it is this rich, dynamic interplay





between filament formation and rupture that gives rise to valuable neuromorphic functionalities such as spiking (Mehonic and Kenyon, 2016). Corresponding topography images for this measurement demonstrate the appearance of a transient distortion on the sample surface (**Supplementary Figure S6**). We expect this to be either a charging artefact or a deformation of the SiO<sub>x</sub> and/or molybdenum that subsequently relaxes. Otherwise, the local area retains its appearance, so we do not expect significant probe blunting to have occurred.

We have previously observed neuromorphic spiking in larger devices (Mehonic et al., 2012a). In both biology and electronic systems, this behavior describes the capacity of neurons to generate an output spike (action potential) once a threshold input has been reached (Merolla et al., 2011). Although we will not discuss the details of behavioral models here, the key functional importance of spiking is that it provides extremely low-power communication between neurons, (Milo et al., 2020), wherein the synaptic weight (the strength of the connection) determines the likelihood of an output being generated in response to a given input (Mehonic et al., 2019). To explore the neuromorphic dynamics of  $SiO_x$  at the nanoscale, we applied constant-current stresses at fixed locations using CAFM, to create filaments and push them into voltage instability without saturating the current detector and risking hard breakdown. The target current was set, then the voltage limit was increased at around  $1 \text{ V s}^{-1}$ , with the applied voltage sampled at 200 Hz. Currents ranging from 10 pA to 10 nA were sampled at an instrumental sensitivity of  $1 \text{ nAV}^{-1}$ , and those ranging from 25 to 100 nA were sampled at a sensitivity of  $100 \text{ nA}^{-1}$ . As shown in **Figure 5A**, once the voltage limit is high enough, the current jumps above its target as a filament is formed. The applied voltage is then reduced to reduce the current to its setpoint. However, as is clear in **Figure 5A**, the voltage required to maintain the current setpoint is not stable, and we observe many spikes of varying magnitude.

Figure 5B shows the spiking dynamics taken from 10 s windows at three constant current biases. At 25 pA, the trace does not show many, if any, spikes. Rather, the voltage feedback is effectively "resting" and maintaining the current setpoint without any significant changes to the filament. However, increasing the setpoint to 250 pA produces clear, frequent spikes. This is reasonable, as a higher current will increase the likelihood of disrupting the filament via Joule heating. Each time the filament begins to break, its conductivity will decrease, and so the voltage must increase in order to reform the filament and maintain the setpoint. Thus, a spike will occur. Higher currents will produce greater Joule heating, and so we would expect the filament to be broken more quickly, increasing the frequency of spikes. However, it is not clear from Figure 5B how this behavior changes when the setpoint is increased from 250 pA to 2.5 nA.

To more quantitatively study the spiking dynamics in our  $SiO_{x}$ , in **Figure 6A** we plot the mean spikes per second,  $S_{pS}$ , defined as the total spikes counted in a measurement divided by the duration of counting in seconds, at a range of spike prominences,  $V_p$ . This corresponds to our detection threshold, i.e. we are determining whether or not to classify a given change in voltage as a spike (note that our CAFM produces a baseline noise of 0.4 mV when reading the sample bias at a fixed current setpoint, so our peak classification is statistically robust). Functionally, this represents the sensitivity of a connected component, such as another RRAM device, to input spikes. For all  $V_p$ , at low current bias,  $S_{pS}$  is low, indicating that the voltage is in its resting state, with a reset unlikely to occur. As the current bias is increased,  $S_{pS}$  also increases, as the filament is broken more frequently. This is a demonstration of a thresholding effect, wherein a low input current does not produce any output activation. Above 250 pA, a plateau is reached, with no further change as the current setpoint is increased by almost a further three orders of magnitude. We note that this plateaus in  $S_{pS}$  is at around 35 per second, which is in good agreement with spike rates of 1-100 Hz reported in the literature, as well as our previous observations (Mehonic and Kenyon, 2016; Tang et al., 2019). As we increase  $V_p$ ,  $S_{pS}$  is lower at all values of *I*<sub>set</sub>, though the shape of the trend remains consistent. This simply demonstrates that we record spikes more frequently if we have a lower detection threshold, as would be expected.

The data in **Figure 6A** are, in all cases, fitted well by a hyperbolic tangent, according to:

$$S_{pS} = \frac{1}{2}S_{max}\left\{1 + tanh\left(p\left[I_{set} - I_{0}\right]\right)\right\}$$

where  $S_{\text{max}}$  is the maximum value of  $S_{pS}$  for each  $V_p$ ,  $I_0$  is the threshold current to transition to a spiking state (i.e.  $S_{DS} > \frac{1}{2}$  $S_{\text{max}}$ ), and p characterizes the sharpness of this transition. We have previously used a hyperbolic tangent to model the reset coefficient in our devices, and would therefore expect a similar shape for  $S_{pS}$  (Mehonic and Kenyon, 2016). However, we note that a logistic sigmoid was also a good fit for the data in Figure 6A. Both of these functions are continuous and nonlinear, and are commonly implemented in transformations between layers in convolutional neural networks (Roy et al., 2019). Thus, functionally, SiO<sub>x</sub> appears able to produce a weighted output in response to inputs of varying magnitude. The additional decrease in the  $R^2$  value of the fitting as  $V_p$ increases demonstrates that more robust behavior, i.e. a more reliable output, would occur for a lower spike detection threshold. Phenomenologically, such logistic functions resemble the on/off activation and deactivation coefficients of the axonal ion pumps during generation of an action potential in a biological neuron (Gerstner et al., 2014; Tang et al., 2019). While a full functional and phenomenological classification of the spiking behavior of our devices with CAFM is beyond the scope of this work, it is very promising that we are able to observe these dynamics at the scale of individual filaments (Akinaga and Shima, 2010; Li et al., 2017; Tang et al., 2017), and at three orders of magnitude lower current than we have previously observed (Mehonic and Kenyon, 2016). Crucially, these observations confirm that neuromorphic functionality in RRAM devices can be a highly localized phenomenon, and is further encouragement that this behavior should persist as devices are scaled down.

It should be mentioned that we observed some topographical changes to the sample at the location of the probe contact point as a result of constant current stress measurements (Supplementary Figure S7). Up to 10 nA, these are a few nm in height, appearing similar to those observed following other measurements (Supplementary Figure S6), though they are not present following every applied stress. The appearance of the surrounding SiOx also does not change significantly after the stress has been applied, and we found no evidence of platinum on or in the  $SiO_x$  following the application of up to 250 pA (Supplementary Figure S8). As such, we do not expect significant probe blunting or platinum deposition to have occurred at lower currents, suspecting instead that the SiO<sub>x</sub> and/or molybdenum have deformed. Conversely, above 10 nA the surface distortions are taller and the appearance of the surrounding material changes significantly, with the surface features appearing much wider and smoother. Therefore, we are unable to rule out probe blunting as a result of platinum deposition at higher currents. However, it is important to note that these values of  $I_{set}$  are well above the plateau in  $S_{pS}$ , which occurs at around 250 pA, i.e. they are outside of the optimal range of device operation, so do not correspond to useful behavior in the present context. This suggests that platinum deposition is not

responsible for the functionality discussed above, specifically a weighted output in response to the input current, following a logistic shape.

Information in a spiking neural network is encoded by the timing and frequency of spikes (Tang et al., 2019). As an event-based rather than continuous-output paradigm, this offers a very energy-efficient means of processing information, with both CMOS and RRAM devices demonstrating spikes of just a few fJ (Merolla et al., 2011; Wang et al., 2014; Sourikopoulos et al., 2017; Tang et al., 2019). We have estimated the energy per spike,  $E_{pS}$ , in our CAFM measurements according to:

$$E_{pS} = W_S \cdot V_S \cdot I_{set}$$

where  $W_S$  and  $V_S$  are the full width at half maximum (which is lognormally distributed, so we have used the modal value) and height (which is normally distributed, so we have used the mean value) of spikes, respectively, at a current bias of  $I_{set}$ . As shown in **Figure 6B**, for all  $V_p$ ,  $E_{pS}$  increases quadratically with  $I_{set}$ ; this seems reasonable, given that the spiking voltage should be a function of the current, according to Ohm's law. Up to around 1 nA current bias, we can see that  $E_{pS}$  is up to a few tens of pJ, except for  $V_p$  of 0.5 V, which is around 100 pJ as we are excluding the peaks of lower prominence and thus lower height. Therefore, for a device operating up to the  $S_{pS}$  plateau observed in **Figure 6A**, we can expect the spike energy to be a few tens of pJ; increasing the current would not produce more spikes, though it would increase  $E_{pS}$ .

We expect that  $E_{pS}$  would be reduced through optimization of the experimental setup. Using a CAFM probe as an electrode necessitates higher operational voltages, due to the high contact resistance that would not be present with a deposited top electrode (Mehonic et al., 2015). and we have previously observed that less than 1 V may be used to switch devices with a top gold electrode and a 37 nm SiO<sub>x</sub> switching laver (Mehonic et al., 2017b), more than three times the thickness of the layer used in the present work. Furthermore, the voltage feedback of our microscope is slower than might be achieved with a semiconductor parameter analyzer, with which we have observed switching processes to take place in tens of nanoseconds (Montesi et al., 2016). Therefore, we cautiously estimate, for a 100 nanosecond-wide spike at 1 V and 250 pA, an  $E_{pS}$  of 25 aJ. This is a highly efficient event, representing the communication of information between devices, or changing in weight of a particular connection, with an energy consumption two orders of magnitude lower than that of conventional CMOS or other RRAM devices (Merolla et al., 2011; Wang et al., 2014; Sourikopoulos et al., 2017; Tang et al., 2019). We are able to propose such a low value because the threshold for our SiO<sub>x</sub> to enter into a spiking state can be a very low current. If we consider an ideal, lossless, 1 cm  $\times$  1 cm RRAM array composed of 100  $\times$ 100 nm devices (i.e. around  $25 \times 108$  devices), each firing at a rate of 10 Hz, an  $E_{pS}$  of 25 aJ leads to a hypothetical power consumption of 0.6 µWcm<sup>-2</sup>, more than four orders of magnitude lower than that of the human brain, which consumes around 10 mWcm<sup>-2</sup> (Young et al., 2019). While this is hypothetical, as any real system will have a reduced efficiency due to line losses and device variability, for example, this is

nevertheless a very exciting prospect for low power, high density neuromorphic computation.

Finally, we note that the behavior presented in this work is also observed at other locations and on other samples, although a full statistical quantification of these effects across samples of, for example, differing active layer thickness, or bottom electrode material, is beyond the scope of this manuscript.

### CONCLUSION

In this work, we have demonstrated the use of CAFM as a tool for studying the plasticity and neuromorphic dynamics of SiO<sub>x</sub> at the nanoscale. The behavior that we observe is in line with that of larger devices, although at a significantly lower energy of 25 aJ, which is very encouraging from the perspective of scaling and power consumption to produce significantly more efficient neuromorphic devices than those of conventional CMOS or other RRAM materials. More specifically, this equates to a power consumption of less than  $1 \mu W cm^{-2}$ , which is lower than that of the human brain. While we would expect losses and inefficiencies to be present in fully-integrated hardware, the prospect of such low device power requirements suggests the potential for great reductions in energy consumption for complex computations, such as are currently required in machine learning. Thus, there is great potential for reducing the carbon footprint of emerging techniques that require huge energy inputs for handling very large quantities of data.

We have additionally shown that not only do filamentary locations demonstrate conductivity enhancement, but that the surrounding background material also becomes more conductive under electrical stress. This core/shell behavior is an important finding, supporting the conclusions of previous work and suggesting that this parallel nature of RRAM devices could be the source of rich dynamic behavior that may be exploited in novel neuromorphic systems.

We have established that the instabilities in the behavior of individual filaments are well-suited to neuromorphic functionality. The interplay of filament formation and rupture produces gradual plasticity under repeated electrical stressing, as well as voltage spiking under constant current stress. In the case of spiking,  $SiO_x$  exhibits a current dependence that is fitted well by a continuous, logistic-type function, which suggests promising behavior for neuromorphic computation. However, a full classification of the nanoscale neuromorphic behavior of our  $SiO_x$  is certainly necessary future work to fully understand these phenomena and further develop their functionalities. In particular, a thorough investigation of spike dynamics in nanoscale devices, including consideration of any background components is essential to fully understand their capacity for low-energy, high density neuromorphic applications.

# DATA AVAILABILITY STATEMENT

The raw data supporting the conclusion of this article will be made available by the authors, without undue reservation.

# **AUTHOR CONTRIBUTIONS**

MB: Designed the study, performed the CAFM measurements, analyzed data and prepared the manuscript; WN: Fabricated the devices and optimized their performance; DM: Assisted with interpretation of the data and preparation of the manuscript; HC: Performed secondary ion mass spectroscopy measurements, and analyzed and interpreted associated data; SH: Assisted with CAFM instrumentation and study design; AM: Assisted with study design and device optimization; AK: Oversaw the work and assisted with study refinement and interpretation of the data.

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### FUNDING

This work was supported by the Engineering and Physical Sciences Research Council, United Kingdom (grant no. EP/K01739X/1) Leverhulme Trust (grant no. RPG-2016-135) and The Worshipful Company of Scientific Instrument Makers.

### SUPPLEMENTARY MATERIAL

The Supplementary Material for this article can be found online at: https://www.frontiersin.org/articles/10.3389/fnano.2021.699037/full#supplementary-material

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The handling Editor declared a past co-authorship with the authors (AK, DM).

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