



A Broadband High-Efficiency Hybrid Continuous Inverse Power Amplifier Based on Extended Admittance Space

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A novel hybrid continuous inverse power amplifier (PA) that is constituted by a continuum of PA modes from the continuous inverse class-F to the continuous inverse class-B/J is proposed, and a detailed mathematical analysis is presented. The fundamental and second harmonic admittance spaces of the hybrid PA proposed in this article are analyzed mathematically. By introducing the phase shift parameter into the current waveform formula of the hybrid continuous inverse PA, the design space of the fundamental and second harmonic admittance is expanded, further increasing the operating bandwidth. The efficiency of the amplifier under different parameter conditions is calculated. In order to verify this method, a broadband high-efficiency PA is designed and fabricated. The drain voltage and current waveforms of the amplifier are extracted for analysis. The experimental measured results show a 60.7–71.5% drain efficiency across the frequency band of 0.5–2.5 GHz (133% bandwidth), and the designed PA can obtain an 11.8–13.9 dB gain in the interesting frequency range. The measured results are confirmed to be in good agreement with theory and simulations.

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INTRODUCTION

RF power amplifiers are widely used in various wireless communication systems. With the rapid growth of modern mobile communication services, the requirements of low consumption, wide bandwidth, high efficiency, and small size are also increasing rapidly [1–5]. The RF power amplifier (PA) is one of the modules with the most power loss in RF transmission. Therefore, the improvement of amplifier efficiency and the expansion of bandwidth have become the focus of current research [6–12].

The continuous operation modes based on class-B were proposed by S. C. Cripps et al. [13]. By introducing the expansion factor into the voltage or current equation, a new mode of amplifier is formed, which has obvious advantages in expanding the fundamental and second harmonic impedance spaces. In addition, the continuous PA can achieve higher efficiency in a wide frequency range by increasing the suppression of high harmonic components. Therefore, in recent years, the study of continuous PAs has attracted extensive attention.

Having gone through several important stages, the research on continuous power amplifiers has made great progress recently. For example, as mentioned by the authors in reference [14], even–odd

1

mode analysis and a series of continuous modes are first proposed to design broadband and high-efficiency PAs. Under the action of the ring resonator microstrip band-pass filter, the operating bandwidth of the designed PA is across 0.8–3.2 GHz with a drain efficiency of 57–74%. A continuous class-B/J PA using a nonlinear embedding technique was proposed by the authors in reference [15], with which the designed amplifier can operate across 1.3–2.4 GHz with a drain efficiency of 63–72%.

An ultra-wideband high-efficiency PA based on optimal fundamental input and output loads was designed by the authors in reference [16]. Starting from the optimum ideal loads, the input and output matching network of the amplifier was designed step by step. The PA was designed over the frequency band from 0.8 to 4.0 GHz with a drain efficiency of 40-55%.

Some achievements have also been made in the field of the inverse class-F PA. A comparison between class F and inverse class F is faced and a novel analytical method for studying the inverse class-F PA is proposed by the authors in reference [17]. The amplifier was designed at a center frequency of 9.6 GHz, and the peak efficiency is 54%.

The hybrid continuous inverse PA can be formed by introducing different parameters into the current equation of the continuous inverse modes. In this study, a phase-shifted current waveform is proposed to be explored in the design of the hybrid continuous inverse PA. Based on the new theoretical model, the current and voltage waveforms of the amplifier are reconstructed so that the design space of the fundamental admittance of the mixed continuous inverse PA is expanded. In addition, the new waveforms enable the second harmonic admittance of the hybrid continuous inverse PA to have both resistive and reactive parts. Meanwhile, with the introduction of the phase shift parameter, the proposed impedance space is very different from the previous modes [18-23]. A high-efficiency PA is designed and fabricated to validate this theory. Simulation and experimental results show that the drain efficiency of this PA is 60.7-71.5%, and there is an output power of 39.8-41.9 dBm in the target band from 0.5 to 2.5 GHz.

POWER AMPLIFIER DESIGN METHODOLOGY

The drain voltage of the proposed PA is the same as that of the traditional inverse continuous modes, and the waveform is similar to that of the square wave [24], as shown below:

$$V_{DC}(\theta) = V_{DC} \cdot \left(1 + \sqrt{2}\cos\theta + \frac{1}{2}\cos 2\theta\right),\tag{1}$$

where V_{DC} is the operating voltage.

The drain current of the continuous inverse class-F PA is shown in **Eq. 2** as follows:

$$I_{DS}(\theta) = I_{MAX} \cdot (i_{dc} - i_1 \cos \theta + i_3 \cos 3\theta) \cdot (1 - \gamma \sin \theta) , \quad 1 \ge \gamma \ge -1,$$
(2)

where I_{max} is the saturated drain current, and $i_{dc} = 0.37$, $i_1 = 0.43$, and $i_3 = 0.06$ [27]. Normalize the above voltage and current

expressions. The normalized voltage and current expressions are given below:

$$\nu_{ds}(\theta) = 1 + \sqrt{2}\cos(\theta) + \frac{1}{2}\cos(2\theta), \qquad (3)$$

$$i_{ds}(\theta) = \left(1 - \frac{43}{37}\cos\theta + \frac{6}{37}\cos3\theta\right) \cdot (1 - \gamma\sin\theta), \quad 1 \ge \gamma \ge -1.$$
(4)

By increasing the coefficient and the phase shift factor in the current equation, **Eq. 4** is expanded and the expansion expression is as follows:

$$i_{ds}(\theta) = \left[1 - \alpha \cos\left(\theta + \varphi\right) + \beta \cos\left(3\theta + 3\varphi\right)\right] \cdot \left[1 - \gamma \sin\left(\theta + \varphi\right)\right],$$

$$1 \ge \gamma \ge -1,$$
(5)

where parameter φ is the phase shift factor. The normalized current expression in **Eq. 5** is mathematically expanded as shown below:

$$i_{ds}(\theta) = 1 - (\alpha \cos \varphi + \gamma \sin \varphi) \cos \theta + (\alpha \sin \varphi - \gamma \cos \varphi) \sin \theta + \left(\frac{\alpha \gamma}{2} + \frac{\beta \gamma}{2}\right) \sin 2\varphi \cos 2\theta + \left(\frac{\alpha \gamma}{2} + \frac{\beta \gamma}{2}\right) \cos 2\varphi \sin 2\theta + \beta \cos 3\theta \cos 3\varphi - \beta \sin 3\varphi \sin 3\theta - \frac{\beta \gamma}{2} \sin 4\varphi \cos 4\theta - \frac{\beta \gamma}{2} \cos 4\varphi \sin 4\theta \quad 1 \ge \gamma \ge -1.$$
(6)

In **Eq. 6**, the ranges of the α and β values are (1, 43/37) and (0, 6/37), respectively [28]. When α and β are the fixed values, the parameter γ is varied over the range of (-1, 1). According to **Eqs 1** and **2**, the fundamental and harmonic admittances can be calculated by using the following:

$$Y_{nf} = -\frac{i_{ds,n}}{v_{ds,n}}.$$
(7)

The harmonic admittance of the PA can be calculated by combining expressions (3), (6), and (7). The first three harmonic admittances can be derived as follows:

$$Y_1 = \sqrt{2} \left(\gamma \sin \varphi + \alpha \cos \varphi + j \cdot \left(\gamma \cos \varphi - \alpha \sin \varphi \right) \right) \cdot G_{opt}, \quad (8)$$

$$Y_2 = -2(\alpha \gamma + \beta \gamma)(\sin 2\varphi + j \cdot \cos 2\varphi) \cdot G_{opt}, \qquad (9)$$

$$Y_3 = \infty. \tag{10}$$

In **Eqs 8** and **9**, $G_{opt}=(1/2) \cdot [I_{peak}/(V_{ds}-V_{knee})]$, which is defined as the optimal admittance of transistors at the current generator (I-gen) plane [28]. From the above expression of admittance calculation, it can be derived that both fundamental admittance and second harmonic admittance are composed of two parts: resistance and reactance. Where the design space of fundamental admittance is determined by parameters α , γ , and φ , the design space of second harmonic admittance is determined by parameters α , β , γ , and φ .

The fundamental and second harmonic admittances must be inside the Smith diagram, so the real part of the admittance is greater than or equal to zero. Therefore, the phase shift parameter φ must satisfy the following expressions:

$$\begin{cases} \varphi \in [\arctan(-\alpha), \ 0] & when \quad 0 \le \gamma \le 1 \\ \varphi \in [0, \ \arctan(\alpha)] & when \quad -1 \le \gamma \le 0 \end{cases}$$
(11)



FIGURE 1 | Theoretical voltage and current waveforms and drain efficiency. (A–D) Theoretical voltage and current waveforms when $\varphi = \arctan(-\alpha)$, $\varphi = 0$, $\varphi = \arctan(\alpha)$. (A) $\alpha = 43/37$, $\beta = 6/37$, and $\varphi \in [\arctan(-\alpha), 0]$. (B) $\alpha = 43/37$, $\beta = 6/37$, and $\varphi \in [0, \arctan(\alpha)]$. (C) $\alpha = 1$, $\beta = 0$, and $\varphi \in [\arctan(-\alpha), 0]$. (D) $\alpha = 1$, $\beta = 0$, and $\varphi \in [0, \arctan(\alpha)]$. (E–H) Theoretical drain efficiency with respect to γ and φ . (E) $\alpha = 43/37$, $\beta = 6/37$, and $\varphi \ge 0$. (F) $\alpha = 43/37$, $\beta = 6/37$, and $\varphi \le 0$. (G) $\alpha = 1$, $\beta = 0$, and $\varphi \ge 0$. (H) $\alpha = 1$, $\beta = 0$, and $\varphi \ge 0$.



FIGURE 2 (A) Equivalent circuit model of transistor modeling showing the parasitics. (B) Output matching network and the admittances at the intrinsic drain plane. (C) De-embedded voltage and current waveforms of the PA at the I-gen plane.

The normalized voltages and currents of the proposed PA based on **Eqs 1** and **2** are shown in **Figure 1**. The current waveform curve without the phase shift is blue, and the current waveform curve after the phase shift is red. From **Eq. 11**, we can see that the range of the phase shift parameters is $[\arctan(-\alpha), \arctan(\alpha)]$. **Figure 1A** shows the normalized current and voltage waveforms at the boundary values of $\alpha = 43/37$, when $\varphi = \arctan(-\alpha)$, $\varphi = 0$. **Figure 1B** shows the normalized current and voltage waveforms at the boundary values of $\alpha = 43/37$, when $\varphi = 0$, $\varphi = \arctan(\alpha)$. **Figure 1C** shows the waveforms at the

boundary values of $\alpha = 1$, when $\varphi = \arctan(-\alpha)$, $\varphi = 0$. Figure 1D shows the waveforms at the boundary values of $\alpha = 1$, when $\varphi = 0$, $\varphi = \arctan(\alpha)$.

The DC and RF power of the PA can be calculated using the following expressions: **Eqs 12** and **13**. The drain efficiency (DE) of the modified hybrid inverse continuous modes can be calculated based on **Eqs 3**, **6**, **12**, and **13** as follows:

$$P_{dc} = \int i_{ds,dc} \times v_{ds,dc} dt, \qquad (12)$$

$$P_{RF} = \int i_{ds,1f} \times v_{ds,1f} dt.$$
 (13)

Using the above expression, the DE which is defined as the ratio of PRF to Pdc can be written as follows:

$$DE = \frac{\int \dot{t}_{ds,1f} \times v_{ds,1f} dt}{\int \dot{t}_{ds,dc} \times v_{ds,dc} dt} = \frac{\sqrt{2}}{2} \left(\gamma \sin \varphi + \alpha \cos \varphi\right).$$
(14)

The DE of the hybrid inverse continuous mode PA, which depends on γ , α , and φ , can be clearly found from the above expression. According to the relation between γ and φ and the range of γ , α , and φ , DE will decrease with the increase in the absolute value of φ . By calculating, it is concluded that when the DE is greater than 60%, the range of γ will be reduced with the increase in the absolute value of φ and when $\alpha = 1$ and $\alpha = 43/37$, the corresponding relations for the theoretical DE are plotted in Figures 1E,F, respectively. Figure 1E shows DE at the boundary values of $\alpha = 43/37$, $\beta = 6/37$, and $\varphi \ge 0$. When $\varphi = 0$, the amplifier has a maximum DE of 82.17%, and the DE does not change with the γ . With the increase in φ , the DE decreases gradually. At the same time, the DE will decrease with the decrease in γ . The maximum value of DE also appears at $\varphi = 0$, as shown in Figure 1F. As the absolute value of φ increases, the DE decreases. When φ has a fixed value, the DE will also decrease with the increase in y. Figure 1G,H show DE at the boundary values of $\alpha = 1$ and $\beta = 0$. The DE has a similar variation law to those in Figure 1E,F.

In order to obtain the design space of the fundamental and second harmonic admittances for DE over 60%, the expressions in **Eqs 8, 9, 11**, and **14** must be combined. **Figure 2B** shows the fundamental and second harmonic design spaces for a = 1, a = 9/8, and a = 43/37. It can be seen from **Figure 2B** that the fundamental admittance space is a large continuous region, which improves the sufficient design space for us to design broadband power amplifiers. In addition, the second harmonic admittance has a completely new region compared with the traditional inverse continuous type, and the efficiency of the amplifier can be further improved by controlling the harmonic component.

REALIZATION AND EXPERIMENTAL RESULTS

Output Network and Output Waveform

A 10-W Cree CGH40010F GaN HEMT packaged device was used for implementation. An ultra-wideband high-efficiency PA is designed to prove the theory proposed in this study. As the theoretical PA mode analysis refers to the intrinsic drain plane (I-generation plane), the parasitics of the transistor need to be properly modeled. Hence, computer-aided design (CAD)-based modeling can be performed in combination with bare sheet models and encapsulation models, which are provided by the manufacturer [25]. The typical equivalent-circuit model of this transistor is shown in **Figure 2A**, indicating the intrinsic and package parasitics [26].

The output matching network with the dimensions of transmission lines and the admittances at the intrinsic drain plane are shown in **Figure 2B**, where the fundamental impedance is consistent with the results obtained from the previous analysis. However, since the bandwidth of the PA exceeds one octave, the second harmonic admittance does not fully enter the ideal space. As a result, the efficiency of the PA will be affected before 1.5 GHz. In other words, the efficiency of the PA will be increased after 1.5 GHz. Additionally, when the frequency is higher than 2.5 GHz, the efficiency of the PA will decrease significantly.

To verify the operating mode of the designed PA, the simulated voltage and current waveforms at the intrinsic drain plane are investigated using the harmonic balance simulator available in ADS. **Figure 2C** shows the simulated voltage and current waveforms when the PA is operating at 0.5, 1.0, 2.0, and 2.5 GHz, respectively. The voltage and current waveforms are basically in agreement with those of the hybrid continuous inverse power amplifiers. The drain voltage is set to 28 V with a quiescent current of 62 mA.

Amplifier Circuit Fabrication and Testing

The circuit is fabricated on Rogers's RO4350B with $\varepsilon_r = 3.66$ and H = 0.762 mm. The photograph of the proposed PA is presented in **Figure 3A**. The PA is first tested under the stimulus of a single-tone





TABLE 1	Comparison with recentl	y reported broadband PAs.
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Reference	BW (GHz, %)	Pout (dBm)	Gain (dB)	DE (%)
[14]	0.8–3.2, 120	39.7–42.9	9.7–12.9	57–74
[15]	1.3–2.4, 59	40.1-41.2	11.4–14.3	63-72
[18]	1.2–3.6, 100	40-42.2	10.5-12.5	60-72
[20]	0.8–3.05, 117	40-43.2	11.8–13.6	57.4–79
[24]	1.8–3, 117	40-43.2	10–13.3	57.4–79
[29]	0.3–1, 107.7	37-40.3	12-15.3	62-81
[30]	0.8-2.4, 100	39-41.7	9–11.7	61–86
This work	0.5/2.5,133	39.8/41.9	11.8–13.9	60.7–71.5

continuous wave signal from 0.5 to 2.5 GHz with a step of 0.1 GHz. The drain bias voltage and the gate bias voltage of the transistor are both set to the value that causes the best drain efficiency (DE) in the test.

The simulated and measured gain, drain efficiency, and output power of the designed amplifier versus the input power at 0.8 GHz are shown in Figure 3B. The simulated and measured results of the output power, drain efficiency, and gain of this PA over the entire frequency band are given in Figure 3C. The simulated and measured results show that the drain efficiency can reach more than 60% in the frequency range of 0.5-2.5 GHz (relative bandwidth 133%). The test results of drain efficiency are 60.7-71.5%. Over the same band, the measured output power from 39.8 to 41.9 dBm is obtained. The gain is 11.8-13.9 dB within the same frequency band. In addition, the simulation results of gain and output power are in good agreement with the measured results due to the EM simulation of the layout in the ADS. It can be seen from Figure 3C that the drain efficiency of the amplifier fluctuates in the operating frequency range. The reason is that the operating bandwidth of the amplifier is very wide, the operating modes of different frequencies are different, and the efficiency of different operating modes is different, so the drain efficiency of the amplifier will fluctuate in the entire frequency range.

Compared with the previous design, the proposed PA designed in this study has wider bandwidth, higher drain efficiency, and stable output power, as shown in **Table 1**.

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CONCLUSION

A high-efficiency and wideband hybrid continuous invert PA with phase shift parameters is presented in this article, the current equation of which has been rebuilt to gain greater design space. It is shown that the fundamental and second harmonic admittances change with the introduction of the phase shift parameters. On the basis of this theory, the drain efficiency of the designed PA is 60.7–71.5% and the output power is 39.8–41.9 dBm.

DATA AVAILABILITY STATEMENT

The original contributions presented in the study are included in the article/Supplementary Material; further inquiries can be directed to the corresponding author.

AUTHOR CONTRIBUTIONS

CN developed the concept and supervised the whole project. JL carried out the simulations. CN and HW analyzed the simulation data. JL designed the structure and fabricated the sample. ZZ and LZ performed the experiments. CN and JL contributed to writing and finalizing the manuscript. JZ perfected the final manuscript. MC contributed to manuscript revision and language editing. All authors participated in the discussion of the results.

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Conflict of Interest: The authors declare that the research was conducted in the absence of any commercial or financial relationships that could be construed as a potential conflict of interest.

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