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An improved dq -frame wideband impedance identification method for power supply network with only one disturbance

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To cope with the energy crisis and environmental degradation, the new energy represented by wind and photovoltaic power generations have developed rapidly, which leads to a large number of power electronic converters being connected to the power grid. In this context, the power-electronic-based power system has become a trend. However, when wideband impedance mismatch happens between the power electronic converter and the connected power supply network, the wideband oscillation will occur in the system, which seriously endangers the safe and stable operation of the power system. To solve it, impedance identification is a key step to obtain the dq -frame impedance characteristics to analyze the power system stability. This article focuses on the power supply network and proposes an improved method to identify its dq -frame wide-band impedance by only one disturbance, which greatly simplifies the dq -frame wideband impedance identification process to improve the identification efficiency. It is of great significance for the safe and stable operation of the power-electronic-based power system.

KEYWORDS

wideband impedance identification, dq -frame impedance, power supply network, power-electronic-based power system, wideband disturbance

1 Introduction

With the gradual depletion of fossil fuels and the continuous deterioration of the natural environment globally, the clean power generation is increasingly receiving attentions (Hassan Zamani et al., 2016; Rebello et al., 2019; Hara et al., 2022). Among them, wind and photovoltaic power generations that rely on power electronic converters are increasingly connected to the power grid for their mature technology. However, when the wideband impedances of the wind/photovoltaic power generations are mismatched with that of the connected power supply network, the wideband oscillation will occur in the system, which seriously endangers the safe and stable operation of the power system (Xie et al., 2017; Zhang et al., 2020). Thus, to obtain the wideband impedance is the key to solve this problem (Zhou et al., 2019; Hu et al., 2020a; Wu et al., 2020).

According to whether the harmonic disturbance signals are applied, the methods to identify impedance are mainly divided into active and passive strategies (Pan et al., 2018; Hu et al., 2020b; Pan et al., 2021a). Passive strategy estimates the impedance using mathematical analysis and numerical processing without injecting any additional disturbance (Pegoraro et al., 2019; Lin et al., 2020a; Lin et al., 2020b). Although this kind of strategy has no impact on the original system, it has a large computational load and extensive estimation. This

“estimation” inevitably brings the errors to the impedance identification results, which causes it difficult to accurately identify the wideband impedance in practical engineering applications. Different from it, the active identification strategy requires injecting disturbance signals to the system, such as switching capacitors, bridge power electronic circuits, etc. Although the disturbance-injection-based active strategy has some impacts on the original system, it can more accurately identify the impedance within the allowable range of the impact and have developed rapidly in recent years. In addition, the identification process of the active strategy is simpler, since it does not involve a large amount of redundant numerical calculations.

The active method can be further divided into single-frequency-based method and wideband-based method. The single-frequency-based method injects a single-frequency harmonic disturbance into the tested system at once, relying on the idea of “frequency scanning” to identify the wideband impedance characteristics of the tested system. For example, L. Asiminoaei extracts the impedance of the tested system by injecting a single specific frequency harmonic disturbance into the common coupling point of the tested system (Asiminoaei et al., 2004). Subsequently, a large number of identification techniques similar to this idea have emerged (Jakšić et al., 2017; Berg et al., 2018; Alves et al., 2019; Salis et al., 2019). The Virginia Tech research team proposes impedance identification method based on power electronics building block (PEBB), and designs high-power impedance identification devices based on these methods, which can be well applied to the on-site identifications. The research team of Hunan University has developed a MW-level impedance identification device based on a modular approach, and the device is used to measure the wideband impedance of VSC equipment in clean energy generation systems. However, this type of method can only identify the impedance at the corresponding injected disturbance frequency and requires the idea of “frequency scanning” to repeatedly inject harmonic disturbances into the tested system. The measurement process is time-consuming and has poor real-time performance.

The wideband-based identification method injects all the harmonic disturbances at the target frequencies into the tested system at once, greatly improving the identification speed and real-time performance. For example, J Huang uses the pulse width modulation (PWM) signals to switch and control H-bridge or chopper type circuits, which can inject wideband harmonic disturbances into the system (Jing et al., 2009). In addition, by using uncontrolled rectification bridge, combined with the random pulse width modulation (RPWM) signal or directly using the PWM switching signal to control the DC load, it can also generate harmonic disturbances over a wide frequency range in the tested system at once (Jordan et al., 2011). Furthermore, relying on disturbing the control system loop, a maximum-length binary sequence (MLBS) signal with high SNR has a good performance in detecting the impedance of the strong power supply network, and also has a good applicability for sensitive systems (Roinila et al., 2018). In summary, the overall development trend of impedance identification is from single-frequency-based method to wideband-based method.

Since the generalized Nyquist stability criterion is based on the dq -frame impedances (Pan et al., 2021b). To directly measure the dq -frame impedance of the power supply network can be conveniently used to analyze the stability of the system. Thus, this paper focuses on the power supply network, and proposes an improved dq -frame impedance identification method based on the advanced wideband-based method. The main contributions are listed as follows.

- 1) The traditional dq -frame impedance identification methods require at least two disturbance injections. Compared to it, the proposed method only requires one wideband disturbance injection, which is simpler and more efficient.
- 2) The proposed method does not rely on a particular disturbance mode, and the disturbance generated by any wideband disturbance device can be used for the proposed method, which has higher practicality.

The rest of the article is organized as follows. Section 2 briefly introduces the traditional dq -frame impedance identification method of the power supply network. Then, the detailed mathematical derivation processes of dq -frame impedance at the positive/negative frequencies are presented in Section 3. Based on this, Section 4 proposes the corresponding wideband impedance identification method. Section 5 shows the simulation validations. Finally, Section 6 concludes this article.

2 Traditional dq -frame impedance identification method

The traditional dq impedance identification method for power supply networks can be divided into four steps (Pan et al., 2021b), as shown in Figure 1, which are: 1) Disturbance injection; 2) Responses acquisition and $\alpha \rightarrow \alpha\beta$ transform; 3) $\alpha\beta \rightarrow dq$ transform; 4) Impedance calculation. Through the above steps, the dq -frame impedance \mathbf{Z}_{dq} of the power supply network can be identified. The following will provide the detailed introductions to these four steps. Furthermore, it is worth noting that the discussed object of this article is single-phase power supply network, which has better applicability because the three-phase power supply network can be seen as three single-phase networks, and its impedance in the dq frame can be obtained by identifying the impedance of any one phase. Further, the disturbance used in this paper is a current-type disturbance.

2.1 Disturbance injection

To identify the dq -frame impedance at the target frequency ω_m , the traditional disturbance injection needs to be specially designed to obtain two sets of dq -frame responses at the identification frequency ω_m , as shown in Eq. 1.

$$\begin{aligned} \mathbf{u}_{dq1,2} &= [\mathbf{u}_{dq1} \quad \mathbf{u}_{dq2}] = \begin{bmatrix} u_{d1} & u_{d2} \\ u_{q1} & u_{q2} \end{bmatrix} \\ &= \begin{bmatrix} U_m \sin(\omega_m t) & U_m \cos(\omega_m t) \\ U_m \cos(\omega_m t) & U_m \sin(\omega_m t) \end{bmatrix} \end{aligned} \quad (1)$$

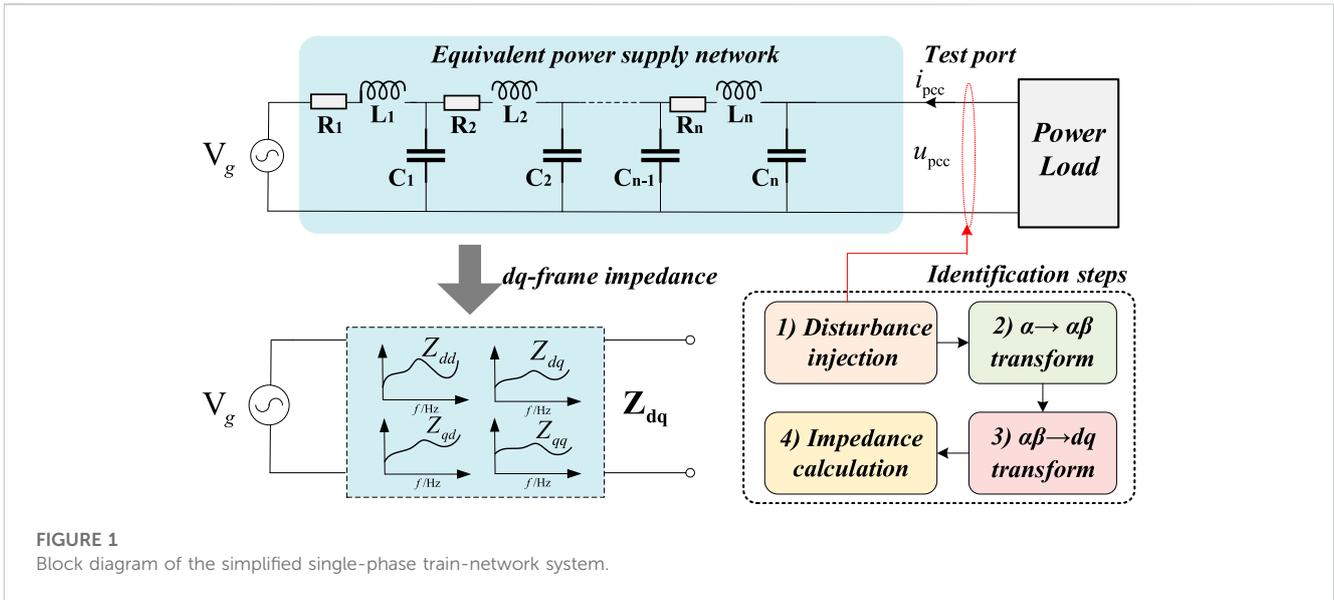


FIGURE 1
Block diagram of the simplified single-phase train-network system.

where $\mathbf{u}_{dq1,2}$ denotes the desired two sets of dq -frame voltage responses. Notably, \mathbf{u}_{dq1} cannot be linearly represented by \mathbf{u}_{dq2} , and introductions of linearly independent vectors can be seen in (Pan et al., 2018). To obtain these kinds of dq -frame responses, generally, two sets of disturbances are injected at $\omega_m - \omega_0$ and $\omega_m + \omega_0$, where ω_0 is the fundamental frequency. Considering the above characteristics, it is also a limitation of the traditional dq -frame impedance identification methods, as there are some strict restrictions on disturbance injection link to obtain the desired responses, which leads to the complexity of the impedance identification process. It is worth noting that the disturbance injection time must be greater than the sampling time, which is the reciprocal of the frequency resolution.

2.2 $\alpha \rightarrow \alpha\beta$ transform

After injecting the designed harmonic disturbance into the test port of the power supply network, the response voltage/current data i_{pcc} and u_{pcc} (α frame) can be further collected. Then the second order generalized integrator (SOGI) or Hilbert transform can be utilized to get the data (u_α, u_β) in the $\alpha\beta$ frame. Since the identification method proposed in Section 4 is based on the Hilbert transform, the following will introduce the Hilbert-transform-based method to get u_α and u_β in detail (Pan et al., 2021b).

Hilbert transform can be expressed as

$$\mathbf{H}(u_\alpha(t)) = u_\alpha(t) * (1/\pi t) \tag{2}$$

where H represents the Hilbert transform; $*$ is the convolution operation. Then, its frequency-domain characteristics can be obtained with Fourier transform, and the result is given by

$$\mathbf{F}(\mathbf{H}(u_\alpha(t))) = \mathbf{F}(u_\alpha(t)) \times \mathbf{F}(1/\pi t) = \begin{cases} -j \times \dot{U}_\alpha(j\omega) & \omega > 0 \\ 0 & \omega = 0 \\ j \times \dot{U}_\alpha(j\omega) & \omega < 0 \end{cases} \tag{3}$$

where F represents the Fourier transform. It can be seen from Eq. 3 that when the target frequency ω is greater than 0, the Hilbert transform can shift the original signal phase by -90° . Furthermore, when the target frequency is less than 0, the phase shift angle is 90° . It should be noted that when the frequency is equal to 0, the Hilbert transform is invalid since the result is constant 0. However, this situation does not affect the field of impedance identification as there is no need to perform the corresponding transformations on the DC values. Further, the orthogonal u_α and u_β can be obtained, represented as follows

$$\begin{cases} u_\alpha = u_{pcc} \\ u_\beta = \mathbf{H}(u_{pcc}) \end{cases} \tag{4}$$

Furthermore, the Hilbert transform can be easily realized by MATLAB toolbox function “hilbert.” In detail, u_α equals u_{pcc} , and u_β equals the imaginary part of the Hilbert transform results.

2.3 $\alpha\beta \rightarrow dq$ transform

Through Section 2.2, the $\alpha\beta$ -frame values can be obtained. Then, the dq -frame values can be further transformed from $\alpha\beta$ frame, and the transform formula is as follows

$$\begin{bmatrix} u_d \\ u_q \end{bmatrix} = \begin{bmatrix} \cos \varphi & \sin \varphi \\ -\sin \varphi & \cos \varphi \end{bmatrix} \begin{bmatrix} u_\alpha \\ u_\beta \end{bmatrix} \tag{5}$$

where, u_d and u_q are the dq -frame voltage responses. The above process takes the voltage transform as an example. Furthermore, for the current transform, it is consistent with the above process. Thus, i_d and i_q can be further obtained with the above process.

2.4 Impedance calculation

When the dq -frame voltage/current values are obtained, the power supply network impedance can be calculated. The equivalent

dq -frame impedance matrix is marked as Z_{dq} , and its corresponding circuit relationship can be derived as

$$\begin{bmatrix} u_d \\ u_q \end{bmatrix} = \begin{bmatrix} Z_{dd} & Z_{dq} \\ Z_{qd} & Z_{qq} \end{bmatrix} \begin{bmatrix} i_d \\ i_q \end{bmatrix} \quad (6)$$

where, Z_{dd} , Z_{dq} , Z_{qd} and Z_{qq} are the four elements of the dq -frame impedance matrix Z_{dq} . To calculate the Z_{dq} , two sets of linearly independent dq -frame voltage/current data are required, which can be expressed as

$$Z_{dq} = \begin{bmatrix} Z_{dd} & Z_{dq} \\ Z_{qd} & Z_{qq} \end{bmatrix} = \begin{bmatrix} u_{d1} & u_{d2} \\ u_{q1} & u_{q2} \end{bmatrix} \cdot \begin{bmatrix} i_{d1} & i_{d2} \\ i_{q1} & i_{q2} \end{bmatrix}^{-1} \quad (7)$$

where, subscripts “1” and “2” represent two sets of dq -frame voltage/current responses, respectively. As can be seen, the traditional dq -frame impedance identification methods require at least two harmonic disturbance injections to calculate the corresponding dq -frame impedance Z_{dq} , which is a limitation and should be further improved.

3 Detailed mathematical derivations of dq -frame impedance at the positive and negative frequencies

This section presents the impedance expressions of the basic components of the power supply network (inductor, capacitor, and resistor) at positive and negative frequencies, which provides a theoretical basis for proposing the novel dq -frame impedance identification method in Section 4.

3.1 Inductor impedance at the positive/negative frequencies

Firstly, the impedance expression of the inductor at the positive frequency (f_+) condition is discussed. Assuming the expression for the current flowing through the inductor is $i(t) = \sin(\omega t)$, the corresponding differential expression can be expressed as

$$\begin{aligned} u(t) &= L \cdot \frac{di(t)}{dt} = L \cdot \frac{d \sin(\omega t)}{dt} = \omega L \cdot \cos(\omega t) \\ &= \omega L \cdot \sin(\omega t + 90^\circ) \end{aligned} \quad (8)$$

where, L is the inductance value; d represents the differential operator; $u(t)$ is the voltage of the inductor. According to (8), the relationship can be built as

$$i(t) = \sin(\omega t) \xrightarrow{Z_L(f_+)} u(t) = \omega L \cdot \sin(\omega t + 90^\circ) \quad (9)$$

where, $Z_L(f_+)$ represents the inductor impedance at the positive frequency (f_+). Further, through the relationship presented in Eq. 9, the impedance of the inductor at this condition can be expressed as

$$Z_L(f_+) = j\omega L \quad (10)$$

Then, the impedance expression of the inductor at the negative frequency (f_-) is discussed. At this condition, assuming the expression for the current flowing through the inductor is $i(t) = \sin(-\omega t)$, the corresponding differential expression is shown as

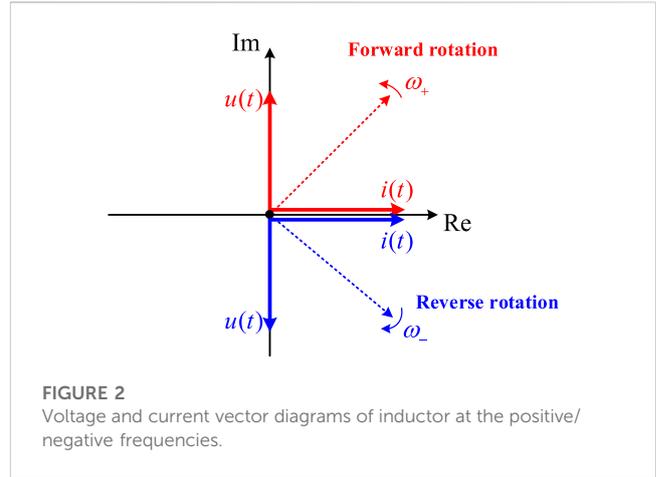


FIGURE 2 Voltage and current vector diagrams of inductor at the positive/negative frequencies.

$$\begin{aligned} u(t) &= L \cdot \frac{di(t)}{dt} = L \cdot \frac{d \sin(-\omega t)}{dt} = -\omega L \cdot \cos(-\omega t) \\ &= \omega L \cdot \sin(-(\omega t + 90^\circ)) \end{aligned} \quad (11)$$

According to (11), the following relationship can be obtained.

$$i(t) = \sin(-\omega t) \xrightarrow{Z_L(f_-)} u(t) = \omega L \cdot \sin(-(\omega t + 90^\circ)) \quad (12)$$

where, $Z_L(f_-)$ represents the inductor impedance at the negative frequency (f_-). Further, through the relationship presented in Eq. 12, the impedance of the inductor at this condition can be expressed as

$$Z_L(f_-) = j\omega L \quad (13)$$

From Eqs. 10, 13, it can be seen that the impedance expressions of the inductor at positive and negative frequencies are consistent, and the following contents will further explain this reason.

By observing the above inductor impedance expressions at the positive and negative frequencies, the voltage and current vector diagrams are shown in Figure 2. As can be seen, regardless of whether the frequency is positive or negative (forward and reverse rotation), the characteristic expressions of the inductor are the same, with voltage leading current of 90° . Thus, the impedance expressions of the inductor at positive and negative frequencies are consistent.

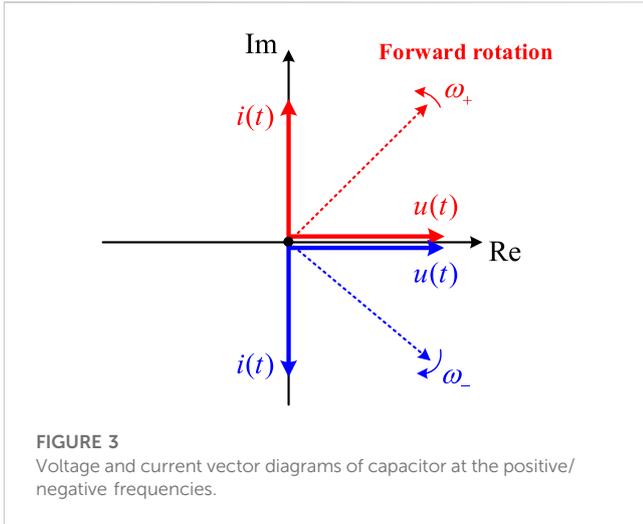
3.2 Capacitor impedance at the positive/negative frequencies

For capacitor, its impedance expression at the positive frequency (f_+) condition is first discussed. Assuming the voltage applied to the capacitor is $u(t) = \sin(\omega t)$, the corresponding differential expression is shown as

$$\begin{aligned} i(t) &= C \cdot \frac{du(t)}{dt} = C \cdot \frac{d \sin(\omega t)}{dt} = \omega C \cdot \cos(\omega t) \\ &= \omega C \cdot \sin(\omega t + 90^\circ) \end{aligned} \quad (14)$$

where, C is the capacitance value. According to (14), the relationship can be built as

$$u(t) = \sin(\omega t) \xrightarrow{Y_C(f_+)} i(t) = \omega C \cdot \sin(\omega t + 90^\circ) \quad (15)$$



where, $Y_C(f_+)$ represents the capacitor admittance at the positive frequency (f_+). Further, through the relationship presented in Eq. 15, the admittance of the capacitor at this condition can be derived as

$$Y_C(f_+) = j\omega C \tag{16}$$

Accordingly, its impedance at the positive frequency can be obtained by

$$Z_C(f_+) = \frac{1}{Y_C(f_+)} = \frac{1}{j\omega C} \tag{17}$$

Then, the impedance expression of the capacitor at the negative frequency (f_-) is discussed. At this condition, assuming the voltage applied to the capacitor is $u(t) = \sin(-\omega t)$, the corresponding differential expression is expressed as

$$\begin{aligned} i(t) &= C \cdot \frac{du(t)}{dt} = C \cdot \frac{d \sin(-\omega t)}{dt} = -\omega C \cdot \cos(-\omega t) \\ &= \omega C \cdot \sin(-(\omega t + 90^\circ)) \end{aligned} \tag{18}$$

From Eq. 18, the following relationship can be obtained.

$$u(t) = \sin(-\omega t) \xrightarrow{Y_C(f_-)} i(t) = \omega C \cdot \sin(-(\omega t + 90^\circ)) \tag{19}$$

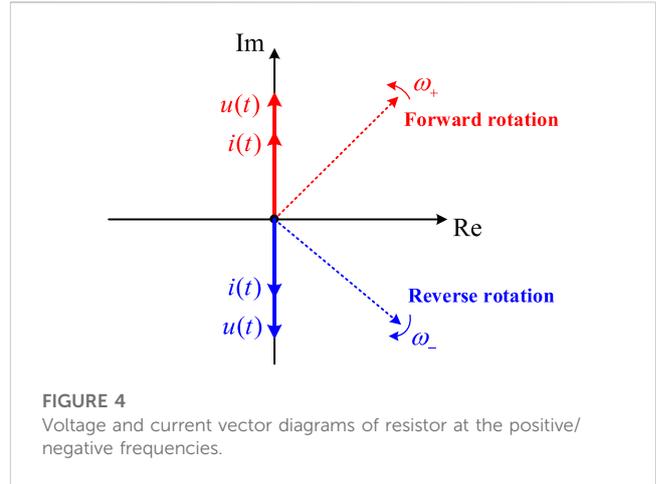
where, $Y_C(f_-)$ represents the capacitor admittance at the negative frequency (f_-). Further, through the relationship presented in Eq. 19, the admittance of the capacitor at this condition can be expressed as

$$Y_C(f_-) = j\omega C \tag{20}$$

Further, its impedance at the negative frequency can be obtained by

$$Z_C(f_-) = \frac{1}{Y_C(f_-)} = \frac{1}{j\omega C} \tag{21}$$

From Eqs. 17, 21, it can be seen that the impedance expressions of the capacitor at positive and negative frequencies are also consistent, and the reason can be further explained as follow.



By observing the above capacitor impedance expressions at the positive and negative frequencies, the voltage and current vector diagrams are shown in Figure 3. As can be seen, regardless of whether the frequency is positive or negative (forward and reverse rotation), the characteristic expressions of the capacitor are the same, with current leading voltage of 90°. Thus, the impedance expressions of the capacitor at positive and negative frequencies are consistent.

3.3 Resistor impedance at the positive/negative frequencies

For resistor, its impedance is not related to frequency. Therefore, at the positive and negative frequencies, the impedance expressions are the same, represented as follows

$$Z_R(f_+) = Z_R(f_-) = R \tag{22}$$

where, $Z_R(f_+)$ and $Z_R(f_-)$ represent the resistor impedance at the positive and negative frequencies, respectively.

The corresponding voltage/current vector diagram of resistor is shown in Figure 4. As can be seen, regardless of whether the frequency is positive or negative, the voltage and current are in phase.

To sum up, the impedance expressions of inductors, capacitors, and resistors are respectively the same at positive and negative frequencies. This is also the basis for proposing the novel dq-frame impedance identification method in the following section. Furthermore, the simulation results in Section 5 can also verify the correctness of the analysis and conclusions in this section in turn. Noteworthy, the physical significance of the negative-frequency impedance can be described by the impedance characteristics of inductance, capacitance and resistance under the reverse rotation of the motor. For inductor and capacitor, their impedance is only related to the frequency value and not related to the direction of motor rotation. For resistors, their impedance is completely independent of frequency. Therefore, the negative-frequency impedances of inductors, capacitors and resistors

derived in this paper are the same as those under the positive frequencies.

4 Proposed impedance identification method with only one wideband disturbance

To improve the efficiency of the *dq*-frame impedance identification, this section introduces how to rely on only one wideband disturbance injection to identify the impedance characteristics of the power supply network. Firstly, the external characteristics of the power supply network at positive and negative frequencies are introduced. Then, the *dq*-frame voltage/current extractions at the positive and negative frequencies are conducted with the forward and reverse *dq* transformation. Finally, based on the above analysis, the corresponding detailed wideband impedance identification steps are developed, which can quickly and efficiently identify the *dq*-frame wideband impedance of the power supply network.

4.1 External characteristics of the power supply network at positive and negative frequencies

In Section 3, it can be found that the expressions for the positive- and negative-frequency impedances of the inductor, capacitor, and resistor are consistent. For the power supply network, it is also composed of multiple inductance, capacitance, and resistance links. Therefore, the impedance expressions of the power supply network are the same at positive and negative frequencies, and the relationship can be expressed as:

$$Z_{dq}^{f+} = Z_{dq}^{f-} = Z_{dq} = \begin{bmatrix} Z_{dd} & Z_{dq} \\ Z_{qd} & Z_{qq} \end{bmatrix} \tag{23}$$

where, Z_{dq}^{f+} and Z_{dq}^{f-} are the *dq*-frame impedance of the power supply network at the positive and negative frequency, respectively.

Further, the external characteristics of the power supply network can be further obtained by

$$\begin{cases} U_{dq}^{f+} = Z_{dq}^{f+} \cdot I_{dq}^{f+} = Z_{dq} \cdot I_{dq}^{f+} \\ U_{dq}^{f-} = Z_{dq}^{f-} \cdot I_{dq}^{f-} = Z_{dq} \cdot I_{dq}^{f-} \end{cases} \tag{24}$$

where, U_{dq}^{f+} and U_{dq}^{f-} are the *dq*-frame voltages at the positive and negative frequency, respectively; I_{dq}^{f+} and I_{dq}^{f-} are the *dq*-frame currents at the positive and negative frequency, respectively. Thus, Z_{dq} can be derived as

$$Z_{dq} = \begin{bmatrix} U_{dq}^{f+} \\ U_{dq}^{f-} \end{bmatrix} \begin{bmatrix} I_{dq}^{f+} \\ I_{dq}^{f-} \end{bmatrix}^{-1} \tag{25}$$

It is worth noting that compared to the traditional method of injecting two harmonic disturbances to calculate the impedance in Eq. 7. The impedance calculation method for the power supply network shown in Eq. 25 only requires one disturbance injection, which greatly reduces the complexity of impedance identification.

4.2 Dq-frame voltage/current extractions at the corresponding positive and negative frequencies

From (Fu and Zhou, 2023), it can be seen that the *dq*-frame voltage/current data at positive/negative frequencies is the key to calculate Z_{dq} . Therefore, the following contents will introduce how to obtain these *dq*-frame voltage/current data.

For the rotation factor at positive frequency (forward rotation), there is the following relationship.

$$e^{j\omega t} = \cos(\omega t) + j \sin(\omega t) = \alpha + j\beta \tag{26}$$

where, $e^{j\omega t}$ is the forward rotation factor. Further, if exchanging the values of α and β , the new phasor can be obtained by

$$\begin{aligned} \dot{A} &= \beta + j\alpha = \sin(\omega t) + j \cos(\omega t) = \cos(90^\circ - \omega t) + j \sin(90^\circ - \omega t) \\ &= e^{j(-\omega t + 90^\circ)} \end{aligned} \tag{27}$$

Notably, the phase of the calculated impedance is the difference between the voltage and current phases. Based on this, the phase shift of 90° in Eq. 27 can be eliminated, and the reverse rotation factor $e^{-j\omega t}$ can be obtained. Further, comparing (Chen et al., 2023a; Chen et al., 2023b), it can be found that exchanging the values of α and β is equivalent to transforming the original forward rotation into reverse rotation.

To extract the *dq*-frame voltage/current, firstly, the Hilbert transform presented in Section 2.2 is utilized to obtain the $\alpha\beta$ -frame values. Then, based on the forward and reverse rotation principles mentioned above, combined with $\alpha\beta \rightarrow dq$ transform in Section 2.3, the *dq*-frame voltage/current data can be obtained. In detail, the forward-rotation U_{dq}^{f+} and I_{dq}^{f+} can be obtained with the principle presented in Eq. 26, and the reverse-rotation U_{dq}^{f-} and I_{dq}^{f-} can be obtained with the principle shown in Eq. 27.

4.3 Proposed wideband impedance identification steps

To make the proposed *dq*-frame wideband impedance identification method of the power supply network clearer, this section provides the detailed identification steps, which are shown below.

Step 1: Inject a wideband disturbance into the test port of the power supply network. Notably, the disturbance does not depend on a particular mode, and the disturbance generated by any wideband disturbance device can be used here.

Step 2: Collect the voltage and current response data of the test port.

Step 3: Utilize the $\alpha \rightarrow \alpha\beta$ transform and the $\alpha\beta \rightarrow dq$ transform, combined with the forward rotation transformation, and the *dq*-frame data U_{dq}^{f+} and I_{dq}^{f+} can be obtained.

Step 4: Similar to step 3, combined with the reverse rotation transformation, U_{dq}^{f-} and I_{dq}^{f-} can be obtained.

Step 5: Calculate the impedance to obtain Z_{dq} with Eq. 25.

Further, for the disturbance-based impedance measurement, the selection of disturbance energy is crucial. Excessive disturbance energy can threaten the normal operation of the system, while low disturbance energy can lead to inaccurate measurement

TABLE 1 Parameters of power supply network.

Parameter	Symbol	Value
AC voltage of the network	V_g	10 kV
Resistor 1	R_1	1 Ω
Inductor 1	L_1	1 mH
Capacitor 1	C_1	1 μ F
Resistor 2	R_1	0.1 Ω
Inductor 2	L_1	2 mH
Capacitor 2	C_1	2 μ F

TABLE 2 Parameters of PWM-based wideband disturbance.

Parameter	Symbol	Value
PWM driving signal frequency	f_{pwm}	10 Hz
PWM driving signal duty cycle	D	0.1%

results. In the actual measurement process, the disturbance energy can be minimized first, and then gradually increased until satisfactory measurement accuracy is achieved. Noteworthy, during this process, the total harmonic distortion (THD) of the system should not exceed the standard (IEEE Std 519-2014).

5 Additional Requirements

5.1 Simulation results with injecting PWM-based wideband disturbance

Taking the second-order power supply network structure as an example, its detailed parameters are listed in Table 1. In addition, the PWM-based wideband disturbance injection method is detailed in (Pan et al., 2018), and its corresponding parameters are shown in Table 2. For example, when the target impedance identification frequency reaches to 8,000 Hz and the frequency resolution is 10 Hz, in order to better cover this frequency band with the PWM-based disturbance, the frequency of PWM should be set to 10 Hz and the duty cycle can be set to 0.1%. Further, the simulation time step is set as 1×10^{-6} s.

The proposed dq -frame impedance identification method is utilized to the simulation model, and the identified results are shown in Figure 5. The red solid lines represent the dq -frame impedance reference values, and the blue circles denote the identified dq -frame impedances. As can be seen, the identified dq -frame impedances, including their magnitudes and phases, can be well consistent with the reference values, which verifies the correctness of the proposed method. In addition, for the phase curve of the Z_{dq} element, some points around approximately 5,000 Hz do not coincide with the reference value. However, it is a normal phenomenon because the phase angles of these points are

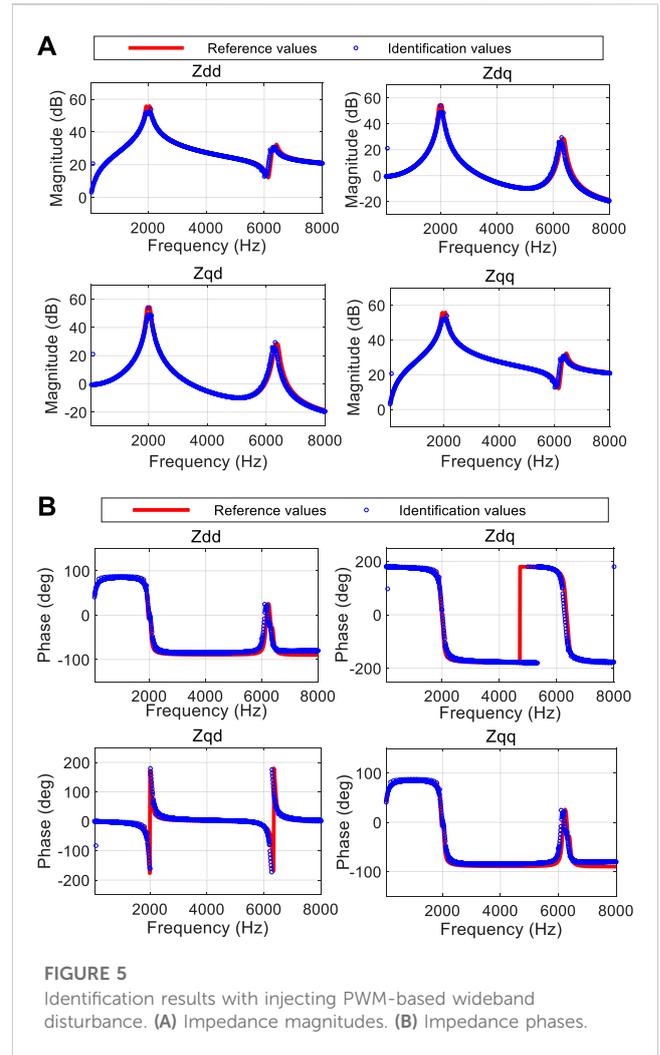


FIGURE 5 Identification results with injecting PWM-based wideband disturbance. (A) Impedance magnitudes. (B) Impedance phases.

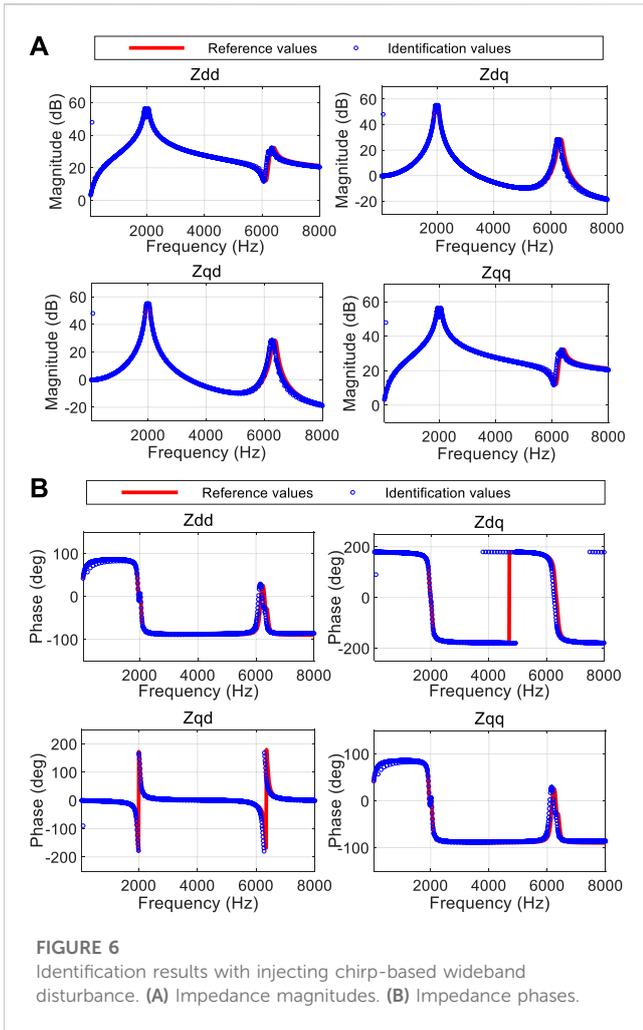
TABLE 3 Parameters of chirp-based wideband disturbance.

Parameter	Symbol	Value
Time cycle of the chirp signal	T	0.1 s
Specified minimum frequency	f_{min}	10 Hz
Specified maximum frequency	f_{max}	8,000 Hz

approximately 180° and -180° , which are equivalent for the identified impedance.

5.2 Simulation results with injecting chirp-based wideband disturbance

The chirp-based wideband disturbance is detailed in (Hu et al., 2020b), and its corresponding parameters are shown in Table 3. Consistent with the identification objectives in Section 5.1, the specified minimum and maximum frequencies are set as 10 Hz and 8,000 Hz, respectively, to cover the target frequency band, and



the time cycle of the chirp signal is set as 0.1s to meet the frequency resolution.

Similar to Section 5.1, the impedance identification results by the proposed dq -frame impedance identification method are shown in Figure 6. The red solid lines also represent the impedance reference values, and the blue circles are the identified dq -frame impedances. As can be seen, the identified dq -frame impedances of the power supply network can be also well consistent with the references, and the correctness of the proposed method is further verified. Furthermore, under the above two types of wideband disturbances, the proposed impedance identification method can identify the accurate dq -frame impedance. It is also verified that the proposed method does not rely on a particular disturbance mode.

6 Conclusion

In this article, an improved dq -frame wideband impedance identification method with only one disturbance is proposed to identify the power supply network impedance. This proposed method is based on the forward- and reverse-rotation dq -frame data extractions. Also, the proposed method has been validated through a simulation model based on MATLAB/SIMULINK. Some valuable conclusions can be drawn.

- 1) For the power supply network, the impedance expressions at the positive and negative frequencies are consistent. Therefore, this characteristic can be used to expand a set of dq -frame voltage/current data for impedance calculation, which reduces the traditional multiple wideband disturbance injection to only once and improving the impedance identification efficiency.
- 2) For power supply network, from the impedance identification results, it can be seen that the four elements of dq -frame impedance are symmetrical, so there is no need to consider the frequency interference caused by asymmetric characteristics. Therefore, for wideband disturbances, it does not rely on a particular disturbance mode, which has higher practicality.

Data availability statement

The original contributions presented in the study are included in the article/supplementary material, further inquiries can be directed to the corresponding author.

Author contributions

CZ: Conceptualization, Software, Validation, Writing–review and editing. KW: Conceptualization, Investigation, Methodology, Writing–original draft. XY: Investigation, Methodology, Writing–review and editing. HL: Software, Validation, Writing–review and editing.

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Conflict of interest

Authors CZ and KW were employed by Gansu Electric Power Company.

The remaining authors declare that the research was conducted in the absence of any commercial or financial relationships that could be construed as a potential conflict of interest.

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