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Megawatt-level converter grid-forming control technology by adopting MPC for medium voltage distribution network

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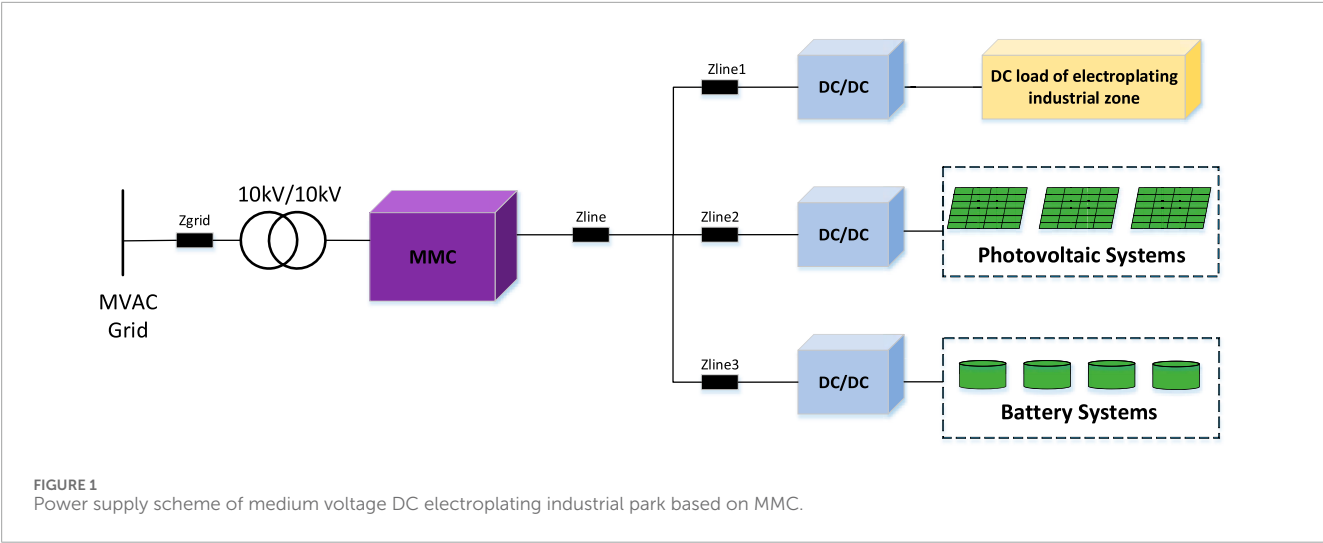
The grid-forming control (GFM) is treated as controlled voltage sources, which can enhance synchronize stability in weak grid case. However, the dynamic response of existing GFM method is limited, and the transient overcurrent in fault is still not solved well, which limits its wide application in distribution network. Drawing from a DC transformation project in an electroplating industrial park in China, this paper proposes an improved GFM control strategy for medium-voltage megawatt converters. The proposal includes the following: firstly, this paper presents an overview of GFM/GFL converters control method; secondly, the establishment of a mathematical model for MMC converters; thirdly, the development of a new MMC grid-forming control technology based on model predictive control, for achieving improved current limitation, dynamic response, and power quality, finally, the verification of these concepts through simulations. This paper provides new insights and strategies for Megawatt-level converter, and reducing carbon emissions in DC industrial parks.

KEYWORDS

DC industrial park, MW-level converter, grid-forming control, model predictive control, carbon reduction, distribution network

1 Introduction

The distribution network is increasingly populated by DC industrial loads, such as those from the electroplating industry, data centers, and energy storage systems, contributing to a significant rise in carbon emissions and environmental pollution. For instance, an electroplating industrial park in Pingyang County, Zhejiang Province, China, has an average annual load of 5 MW, with annual electricity consumption exceeding 100 million kilowatt-hours, 70% of which is DC load. This high energy consumption is accompanied by stringent power quality demands. A critical challenge, therefore, is how to reduce carbon emissions in DC industrial parks, particularly electroplating facilities, through the integration of new energy sources. In 2023, a demonstration project to transform the medium-voltage DC power supply was implemented at Pingyang Electroplating Industrial Park in China. This project utilized two small-capacity VSC converters for ± 10 kV DC energy conversion, leading to significant reductions in energy consumption. However, as the power grid's carbon reduction requirements become more stringent, there is a need to incorporate larger distributed photovoltaic



and energy storage systems. Modular multilevel converter (MMC) technology offers a significant advantage over the C-NPC topology by easily increasing the output voltage and capacity of a single converter. This makes it particularly suitable for applications involving large-scale distributed photovoltaic systems and DC industrial parks. The power supply scheme for a medium-voltage DC electroplating industrial park is shown in Figure 1.

MMC technology is widely utilized in renewable energy, energy storage systems, and HVDC transmission due to its high efficiency. The MMC converters are typically controlled as current sources, which follow the frequency and phase of the grid by phase-locked loops (Pan et al., 2017; Du et al., 2021). This grid synchronization control is known as grid-following control (GFL). The GFL mode based on phase-locked loop control can ensure system stability and power control rapidity under a strong grid, which has many limitations in terms of stability, system voltage, and frequency adjustment, and adapting to a scenario where a high proportion of renewable energy generation units are connected is difficult. When connected to weak grid, the synchronization dynamics of GFL converters is more susceptible to perturbation and then instability (Wang et al., 2018; Hu et al., 2019; Hu et al., 2021).

GFM converters reproduce the behavior of a voltage source behind an impedance, and contributing to the strength of weak power grid by regulating voltage and frequency. Gao et al. (2017) presents an adaptive virtual frequency modulation control strategy that dynamically adjusts the inertia coefficient to improve frequency stability. Zhao et al. (2022) provides a detailed analysis of the virtual synchronous generator's parameter design, focusing on stability and dynamic performance, and outlines the principles for parameter setting. Several GFM control methods are proposed in the literature (Zhong et al., 2014; Rodriguez et al., 2018; Zhang et al., 2010; Matevosyan et al., 2019), comparisons between GFL and GFM converters illustrated in Table 1.

Due to the voltage source behavior of the GFM converters in contrast to GFL converters, the overcurrent protection requires particular attention. Therefore, various current-limiting threshold control methods for GFM converters are reported in the literature, including current threshold limiters, virtual impedance, virtual admittance.

TABLE 1 Comparison between GFL and GFM converters.

	GFL	GFM
PLL	Need	No need
Dynamic performance	fast	slow
Inertia provision	No	Yes
Type of applicable grid	Strong power grid	Weak power grid
FRT	easy	difficult

To control the output current of GFM inverter during faults, paper (Bottrell and Green, 2014) restricts the phase current magnitude with the maximum allowed value through closed-loop current control, authors in (Pirsto et al., 2022) have proposed a state feedback control based cascaded voltage and current loop. When the converter is overloaded, the controller shifts to the current control mode (CCM) from the voltage control mode (VCM) to control the output current. Since the references generated by the voltage controller are unutilized, the voltage source behaviour of the GFM is lost. Also, this necessitates using an anti-windup control for the voltage controller, leading to delays in the recovery period. The current limiting control performance of GFM converters with and without transitioning to GFL with varying grid strengths is compared in (Taul et al., 2020). The comparison have shown that when GFL performed satisfactorily with strong grids, it might lead to unstable behaviour in weak grids because of the stability issues associated with phase locked loop (PLL).

GFM based on virtual impedance control with aim to adjust the impedance $R_v + jX_v$ to limit the phase current magnitude. The virtual impedance with cascaded voltage control proposed in (Lu et al., 2016; Qoria et al., 2019), and without inner-loop control is presented in (Vilathgamuwa et al., 2006; Gouveia et al., 2021). The virtual admittance control method is applied in (Rosso et al., 2020; Rosso et al., 2021) for current limitation, the stabilities of admittance

TABLE 2 Comparisons of existing current-limiting control methods.

References	GFM mode	Enhancement of transient stability	During fault current limiting	Temporary current limiting
Control-mode-switching (Pirsto et al., 2022; Taul et al., 2020)	✗	✓	✓	✗
Current limiter (Bottrell and Green, 2014)	✓	✗	✓	✗
virtual impedance With no inner loop (Vilathgamuwa et al., 2006; Gouveia et al., 2021)	✓	✓	✗	✗
virtual impedance With cascaded voltage control (Lu et al., 2016; Qoria et al., 2019)	✓	✗	✓	✗
virtual admittance With inner current control (Rosso et al., 2020; Rosso et al., 2021)	✓	✓	✓	✗

method and impedance method are compared initially in paper (Huang et al., 2021), resulted that the equivalent circuits of these two methods are basically the same. With the use of conventional cascaded PI control, the bandwidth of the loop is limited owing to sluggish behaviour, and leads to temporary overcurrents or overvoltages when fault is initiated or recovery period. Comparisons of different current limiting methods illustrated in Table 2.

Conventionally, a cascaded dual-loop linear feedback control is deployed in the inner loop of GFM. However, this control scheme PI parameter setting is complex, suffers from a slow transient response, limited control bandwidth, and instantaneous current overlimits. Moreover, it is hard for a linear controller to handle the multi-objective optimization and various system constraints, for example, MMC (Vazquez et al., 2017).

A improved GFM strategy based virtual admittance combined with direct modulation MPC method is proposed in this work. The improved control strategy has simplified structure, fast dynamic response speed and strong current limiting ability.

The main contributions of the work are:

- The proposed scheme combines an virtual admittance reference current and currentlimit objective into the MPC cost functions, which enhanced fault ride-through of GFM-MMC converter.
- Without any transient overcurrents and overvoltages during fault initiation or recovery after fault clear, compared with PI control strategy, MPC method can provide faster active power recovery.
- Verified that the proposed control strategy can run stably under wide SCR, the power quality and dynamic response speed are better than the PI control.

In the parts to follow: Section 2 details the establishment of a mathematical model for MMC converters. The controller design for the MPC-VSG MMC are discussed in Section 3. Section 4 shows verification of these concepts through simulations include current

limitation, dynamic response, and power quality. Finally conclusions of the work are summarised in Section 5.

2 Mathematical model of grid-connect MMC

The MMC consists of a three-phase structure with six bridge arms, where each bridge arm has an inductance L_{arm} , and resistance R_{arm} , with N half-bridge submodules connected in series. The topology of the MMC is illustrated in Figure 2.

As shown in Figure 2, u_{sj} and i_{sj} ($j = a, b, c$) are the phase voltage and phase current of the MMC grid side, respectively. u_{pj} and u_{nj} are the upper and lower bridge arm voltages, respectively. i_{pj} and i_{nj} are the current of the upper and lower bridge arms, respectively. L_g and R_g are the equivalent inductance and resistance of the grid sides, respectively. V_{dc} and I_{dc} are the DC voltage and DC current, respectively. MMC circuit equation is established based on Kirchhoff's voltage law.

$$-\frac{V_{dc}}{2} + u_{nj} + R_{arm}i_{nj} + L_{arm}\frac{di_{nj}}{dt} + R_gi_{sj} + L_g\frac{di_{sj}}{dt} = u_{sj} \quad (1)$$

$$\frac{V_{dc}}{2} - u_{pj} - R_{arm}i_{pj} - L_{arm}\frac{di_{pj}}{dt} + R_gi_{sj} + L_g\frac{di_{sj}}{dt} = u_{sj} \quad (2)$$

Add and subtract Equation 1 and Equation 2, respectively.

$$\frac{di_{sj}}{dt} = -\left(\frac{R_{arm} + 2R_g}{L_{arm} + 2L_g}\right)i_{sj} + \frac{u_{nj} - u_{pj}}{L_{arm} + 2L_g} - \frac{2}{L_{arm} + 2L_g}u_{sj} \quad (3)$$

$$\frac{di_{zj}}{dt} = -\frac{R_{arm}}{L_{arm}}i_{zj} - \frac{u_{nj} + u_{pj}}{2L_{arm}} + \frac{1}{2L_{arm}}V_{dc} \quad (4)$$

where, $i_{zj} = \frac{i_{pj} + i_{nj}}{2}$, $i_{sj} = i_{nj} - i_{pj}$.

$$u_{pj} = \frac{n_{pj}u_{pj}^{\Sigma}}{N} \quad (5)$$

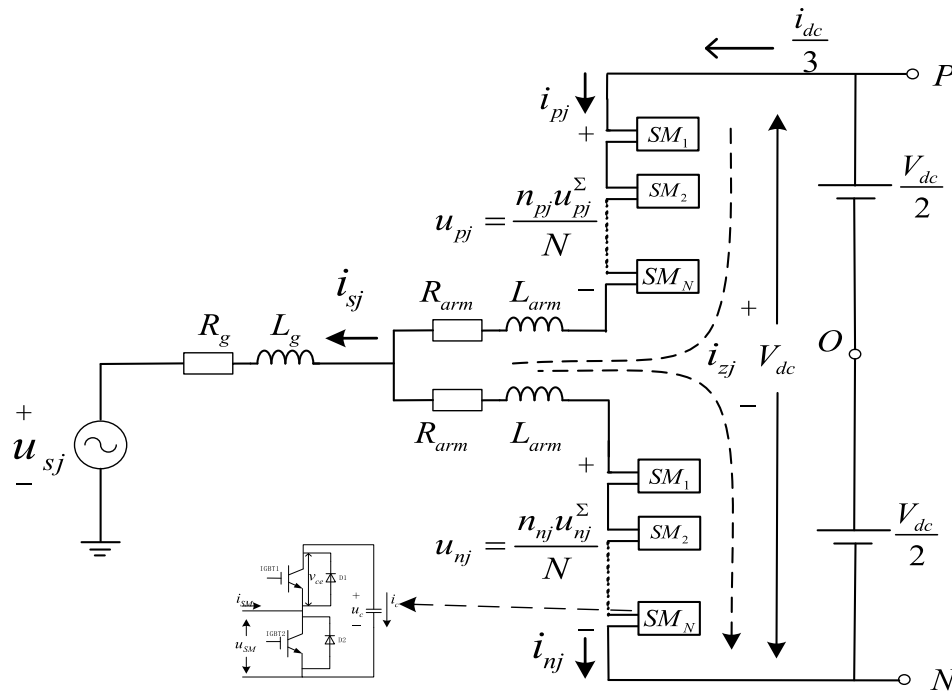


FIGURE 2
Topology of grid-connect MMC.

$$u_{nj} = \frac{n_{nj} u_{nj}^{\Sigma}}{N} \quad (6)$$

where n_{pj} and n_{nj} are the upper and lower bridge arm submodules, respectively. N is the number of submodules put into each phase. u_{pj}^{Σ} and u_{nj}^{Σ} are the sum of upper and lower bridge arm capacitor voltages, respectively.

By Equations 3, 4, the unbalanced current inside MMC i_{zj} can be controlled by the sum of bridge arm voltage ($u_{nj} + u_{pj}$). The AC current i_{sj} of grid side controlled by the difference bridge arm voltage ($u_{nj} - u_{pj}$). This means that the DC circuit and AC circuit of MMC are decoupling from each other (Figure 3). Using the forward Euler to discretize Equations 3, 4, an MMC discrete mathematical model can be obtained, k is the k -th sampling point and T_s is the sampling frequency.

$$\frac{i_{sj}(k+1) - i_{sj}(k)}{T_s} = - \left(\frac{R_{arm} + 2R_g}{L_{arm} + 2L_g} \right) i_{sj}(k+1) + \frac{u_{nj}(k+1) - u_{pj}(k+1)}{L_{arm} + 2L_g} - \frac{2u_{sj}(k+1)}{L_{arm} + 2L_g} \quad (7)$$

$$\frac{i_{zj}(k+1) - i_{zj}(k)}{T_s} = - \frac{R_{arm}}{L_{arm}} i_{zj}(k+1) - \frac{u_{nj}(k+1) + u_{pj}(k+1)}{2L_{arm}} + \frac{1}{2L_{arm}} V_{dc}(k+1) \quad (8)$$

3 MMC grid-forming control method based on model predictive control

3.1 MMC traditional double closed loop PI control

The MMC typically employs a double-closed-loop PI control, as shown in Figure 4. P_{ref} , V_{dcref} , Q_{ref} , U_{smref} , i_{dref} , i_{qref} , u_{abcref} are the references of active power, reactive power, DC voltage and the PCC AC voltage, d-axis current, q-axis current, modulation of arm voltage, respectively; and P , Q , V_{dc} , U_{sm} , i_{sabc} , u_{sd} , u_{sq} , ω_{ref} are the output active power, reactive power, DC voltage, PCC AC voltage, PCC AC current, d-axis voltage, q-axis voltage, angular frequency, respectively; k_{p1} , k_{i1} , k_{p2} , k_{i2} , are out-loop and inner-loop proportional and integral coefficients.

The powers are calculated in the dq-frame as:

$$\begin{cases} P = 1.5(u_{sd}i_{sd} + u_{sq}i_{sq}) \\ Q = 1.5(u_{sq}i_{sd} - u_{sd}i_{sq}) \end{cases} \quad (9)$$

In the dq reference frame, (Equation 9) expressed as:

$$L_{eq} \frac{d}{dt} \begin{bmatrix} i_{sd} \\ i_{sq} \end{bmatrix} = -R_{eq} \begin{bmatrix} i_{sd} \\ i_{sq} \end{bmatrix} + \begin{bmatrix} u_{diffd} \\ u_{diffq} \end{bmatrix} - \begin{bmatrix} u_{sd} \\ u_{sq} \end{bmatrix} + \begin{bmatrix} \omega L_{eq} \\ -\omega L_{eq} \end{bmatrix} \begin{bmatrix} i_{sd} \\ i_{sq} \end{bmatrix} \quad (10)$$

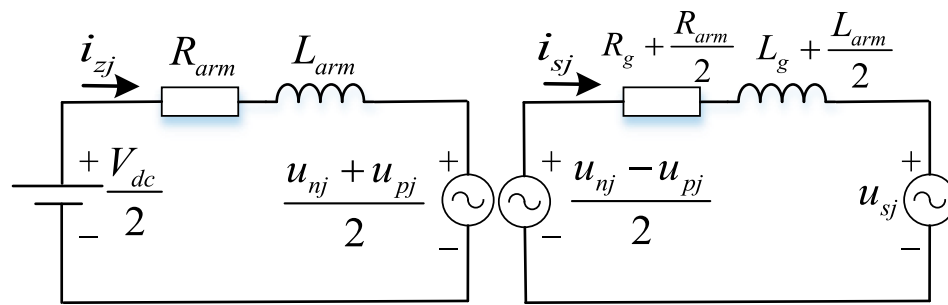


FIGURE 3
MMC AC and DC equivalent circuit.

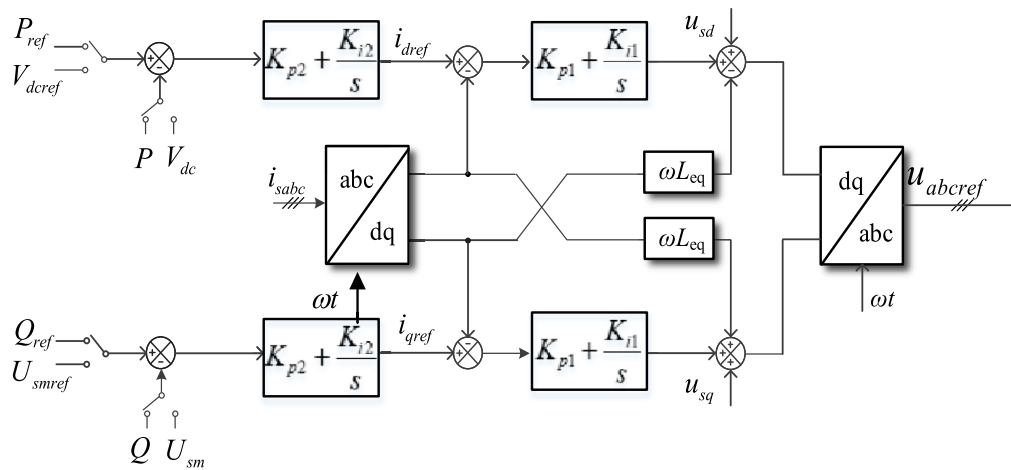


FIGURE 4
Traditional PI double closed loop control.

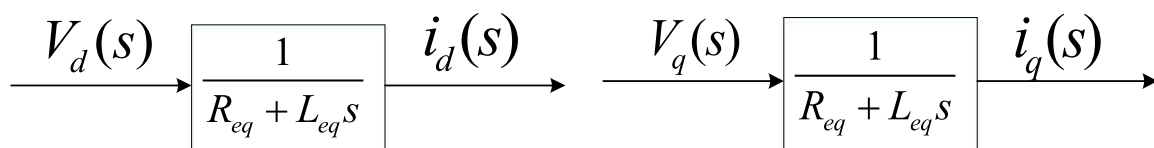


FIGURE 5
The input-output relationship of d-q current.

$$R_{eq} \text{ and } L_{eq} \text{ in (Equation 10) are expressed in } R_{eq} = R_g + \frac{1}{2}R_{arm}L_{eq} = L_g + \frac{1}{2}L_{arm},$$

In (Equation 10) expressed Laplace domain as:

$$\begin{cases} (R+Ls)i_{sd}(s) = -u_{sd}(s) + u_{diffd}(s) + \omega Li_{sd}(s) \\ (R+Ls)i_{sq}(s) = -u_{sq}(s) + u_{diffq}(s) + \omega Li_{sq}(s) \end{cases} \quad (11)$$

Transfer functions for output variables i_{sd} , i_{sq} and voltages V_d and V_q expressed as:

$$\begin{cases} \frac{i_{sd}(s)}{V_d(s)} = \frac{1}{R_{eq} + L_{eq}s} \\ \frac{i_{sq}(s)}{V_q(s)} = \frac{1}{R_{eq} + L_{eq}s} \end{cases} \quad (12)$$

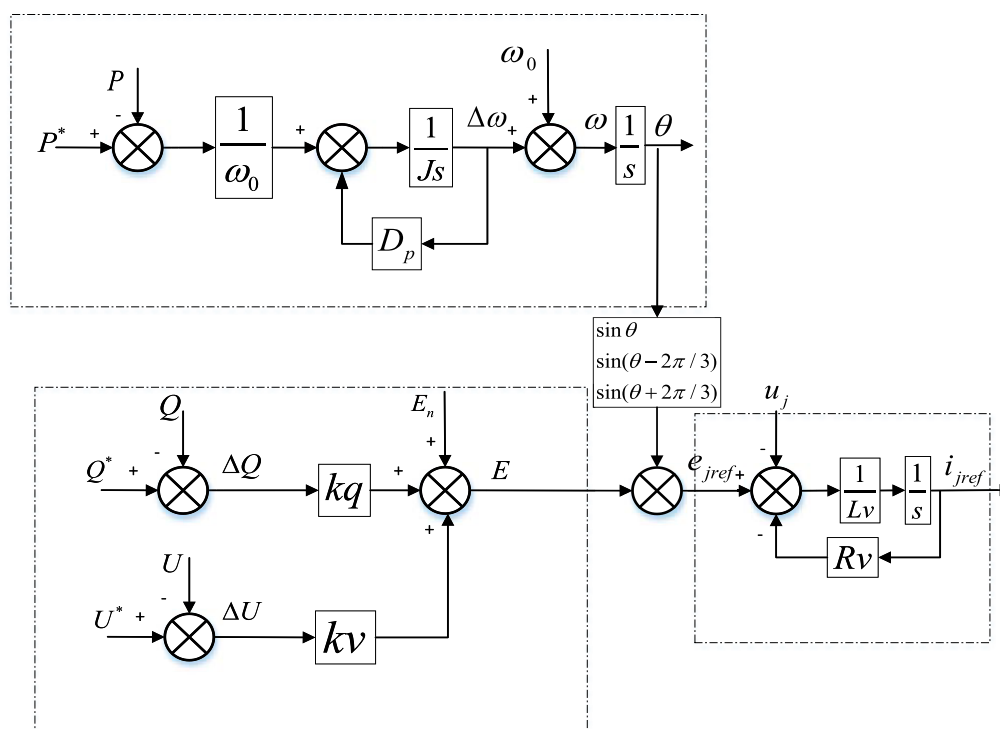


FIGURE 6
Virtual synchronous control block diagram.

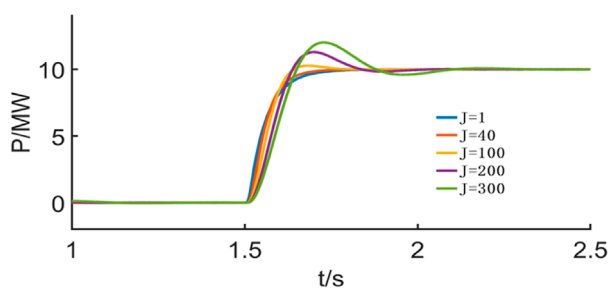


FIGURE 7
Dynamic response of different J .

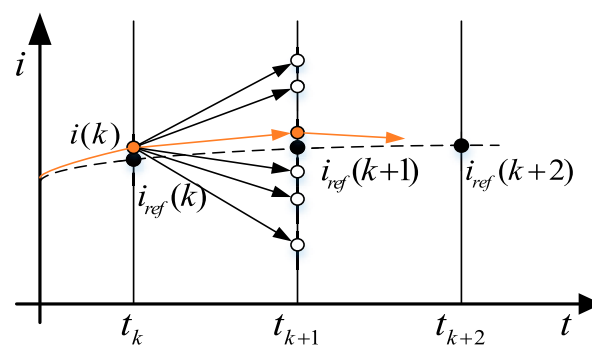


FIGURE 9
Basic principles of model predictive control.

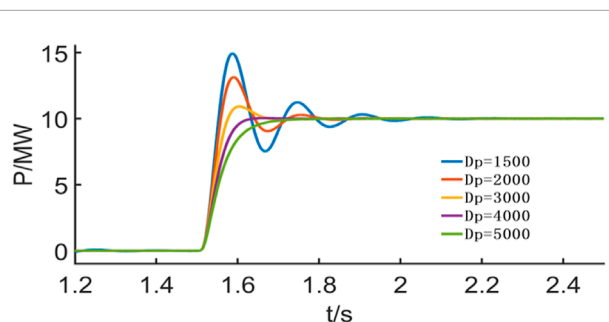


FIGURE 8
Dynamic response of different D_p .

$$\text{Where } \begin{cases} V_d(s) = -u_{sd}(s) + u_{diffd}(s) + \omega Li_{sd}(s) \\ V_q(s) = -u_{sq}(s) + u_{diffq}(s) + \omega Li_{sq}(s) \end{cases}$$

By Equations 11, 12, the input-output relationship (Figure 5) of d-axis current, q-axis current expressed as:

According to the negative feedback theory, the current reference value i_{dref} and i_{qref} can be well tracked by PI control, similarly the active power and reactive power can be well tracked and controlled.

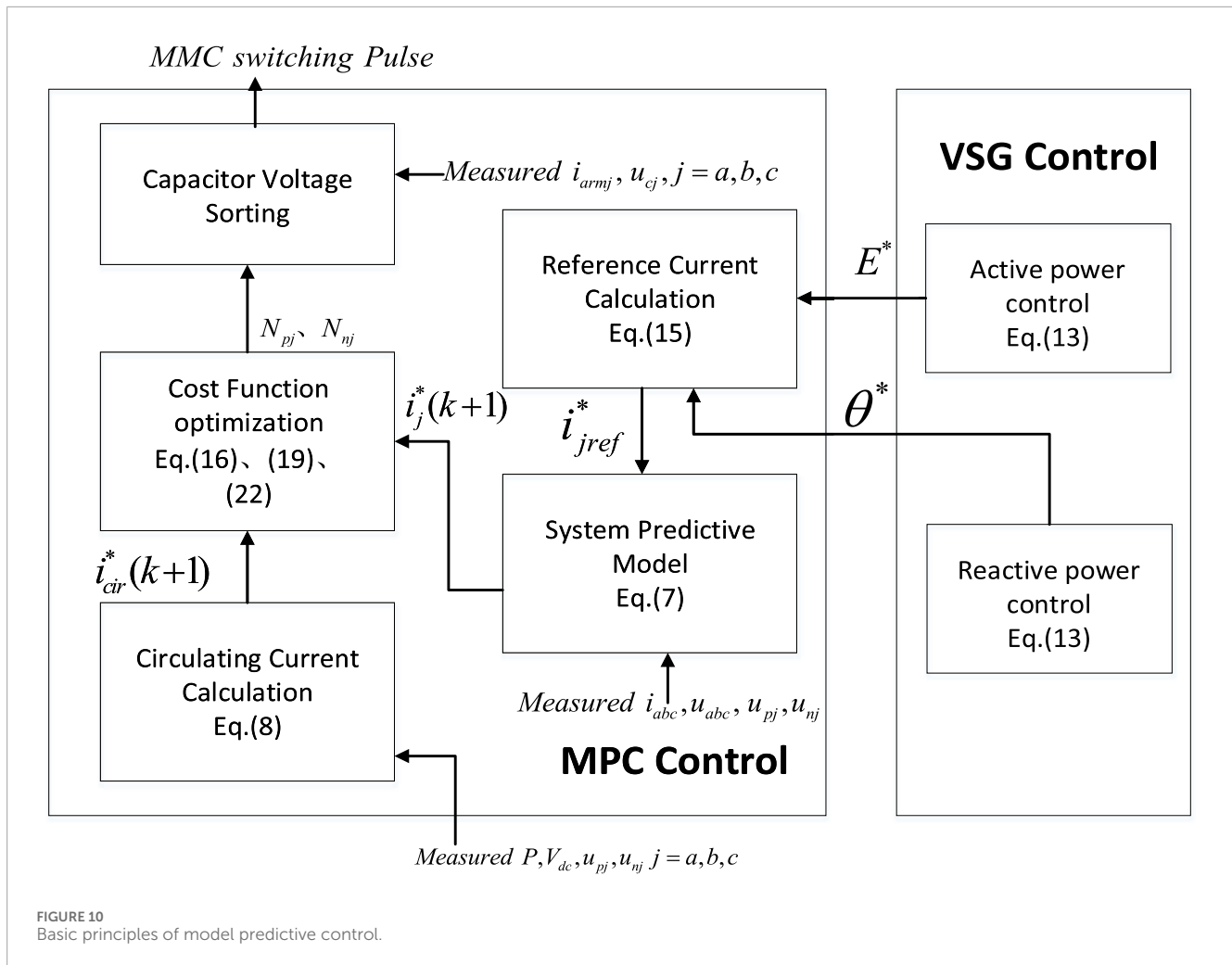


FIGURE 10
Basic principles of model predictive control.

3.2 Grid-forming virtual synchronization control

Traditional MMC double-PI control typically uses fixed reference values for its targets, making it incapable of actively adjusting to variations in grid voltage and frequency. The core concept of grid-forming control is to modify the converter control mode, enabling it to exhibit rotational inertia and autonomously provide active or reactive power support to the grid. The mathematical equations governing the grid-forming virtual synchronous generator (VSG) control for the MMC are presented as follows.

$$P^* - P = J\omega_0 \frac{d\omega}{dt} + D_p\omega_0(\omega - \omega_0) \quad (13)$$

$$E = E_n + k_q(Q^* - Q) + k_v(U^* - U) \quad (14)$$

$$L_v \frac{di_{jref}}{dt} + R_v i_{jref} = e_{jref} - u_j \quad (15)$$

Equations 13–15 describe the rotor motion equation, the reactive power-voltage control equation, and the electrical equation of the VSG body used for virtual synchronous control.

In these equations, P^* and P are the reference power and actual active power, respectively. J and D_p are the VSG control parameters refer to virtual rotational inertia and damping coefficient respectively, ω_0 and ω refer to the rated velocity and actual electrical angular velocity, respectively, Q^* and Q are the reference power and actual active power, respectively. E_n is the no-load electromotive force in the synchronous generator. R_v and L_v are the virtual resistance and inductance of VSG, respectively. i_{jref} is the three-phase current reference value of VSG output. e_{jref} is the internal potential reference value. u_j is the three-phase voltage of the MMC grid connection point.

The control block diagram for the VSG in the MMC is shown in Figure 6. The stator voltage is determined using P - f control and Q - V control, with virtual impedance R_v and L_v incorporated into the electrical section of the VSG model. The current command value i_{jref} , which satisfies the stator voltage equation, is computed through the virtual impedance loop. The d-q current references, i_{dref} and i_{qref} , are then used as inputs to the current inner loop in Figure 4, completing the simulation of the stator voltage equation in VSG control.

By adjusting the values of J and D_p , different active power response characteristics of the MMC can be achieved. The dynamic response of the output power for the MMC, with different J and D_p coefficients, under VSG control is shown in Figures 7, 8.

TABLE 3 Simulation parameters.

Type	Parameters
Rated power of the MMC/MW	10
Rated voltage of the AC grid/kV	10
Dc side voltage/kV	±10
Transformer leakage/mH	5
Grid side equivalent resistance/Ω	0.01
Number of submodules of the bridge arm	20
Arm inductance/mH	3
Arm resistance/Ω	1
Submodule capacitance value/mF	10
$J/(\text{kg}\cdot\text{m}^2)$	300
$D_p/(\text{N}\cdot\text{m}\cdot\text{s}/\text{rad})$	2000
Reactive power adjustment coefficient kq	0.005
Voltage regulation coefficient kv	1
Control period fs/kHz	10
Number of submodules m for circulation suppression	1

As illustrated in Figures 7, 8, increasing the virtual inertia J results in a larger amplitude and a longer response time to the P/Q reference signals. Conversely, increasing the virtual damping coefficient D_p leads to a smaller amplitude and a shorter response time. In practice, D_p is generally determined by grid standards, and the dynamic performance of the MMC is primarily adjusted by modifying J .

3.3 Grid-forming based on model predictive control

Model predictive control (MPC) is an advanced control method for power electronics, based on predicting the next AC grid-side current by establishing a discrete mathematical model of the converter. It then performs a traversal calculation of all possible switching states of the power electronics, ultimately achieving optimal control within each control period.

Unlike traditional control methods, MPC does not rely on a PI control loop, making it structurally simpler, requiring no complex parameter tuning. MPC offers fast tracking responses, minimal overshoot, and superior dynamic performance (Zheng, 2020). In this paper, MPC is applied to the virtual synchronous control of the MMC, and a corresponding cost function is designed to address circulating currents and DC voltage fluctuations, as illustrated in Figure 9.

The power control outer loop of grid-forming control is implemented as MPC-VSG, where the traditional PI current loop is replaced by the MPC model predictive controller. In this setup, the current reference $i_{j\text{ref}}$ of the VSG output (as shown in Figure 6) serves as the input reference for the model predictive control. The discrete mathematical Equations 7, 8, of the MMC are combined to construct the cost function for MPC.

Depending on disturbances in the AC grid frequency or voltage, or input changes in the active power command (P_{ref} or V_{dc}) or reactive power command (Q_{ref} or U_{sm}), the three-phase current reference $i_{sj\text{ref}}(t + T_s)$ ($j = a, b, c$) is calculated using Equations 13–15.

The first cost function of MPC-VSG is built.

$$J_j = |i_{j\text{ref}}(t + T_s) - i_{sj}(t + T_s)| \quad (16)$$

Combined with (Equation 7), the upper and lower bridge arms voltage difference value $e_j(t + T_s) = u_{nj}(k + 1) - u_{pj}(k + 1)$ corresponding to the minimum J_j can be denoted as $e_j^*(t + T_s)$, whose value range is $[\frac{V_{\text{dc}}}{N}][-\frac{N}{2}, -\frac{N-1}{2}, \dots, 0, \dots, -\frac{N-1}{2}, \frac{N}{2}]$, and V_{dc} is the DC bus voltage. According to the combined Equations 5, 6, the number of upper and lower bridge arms can be calculated, as n_{nj}^* and n_{pj}^* .

Equation 8 highlights the presence of circulating currents within the MMC, which, while circulating only inside the MMC, do not affect the external AC or DC sides. However, these currents contribute to increased power device losses and must be suppressed. In this work, we address this issue by controlling the circulating currents through adjustments to the additional voltage v_{diff} applied simultaneously by Equations 17, 18:

$$\begin{aligned} \frac{i_{sj}(k+1) - i_{sj}(k)}{T_s} = & -\frac{i_{sj}(k+1)R_{\text{arm}}}{L_{\text{arm}}} \\ & -\frac{[u_{nj}(k+1) + v_{\text{diff}}] + [u_{pj}(k+1) + v_{\text{diff}}]}{2L_{\text{arm}}} + \frac{V_{\text{dc}}(k+1)}{2L_{\text{arm}}} \end{aligned} \quad (17)$$

$$v_{\text{diff}j} = [V_{\text{dc}}/N][-(m-1), \dots, 0, m-1, m] \quad (18)$$

The second cost function of MPC-VSG is obtained.

$$J_{\text{diff}j} = |i_{\text{dc}}(t + T_s)/3 - i_{zj}(t + T_s)| \quad (19)$$

where i_{dc} is the DC side current. When $J_{\text{diff}j}$ is the lowest value, the corresponding v_{diff} value is calculated and m denoted as $v_{\text{diff}j}^*(t + T_s)$ and m^* . The upper bridge arm submodules number under the MPC-VSG control strategy is obtained by combining cost function (Equations 16, 19).

$$N_{pj} = n_{pj}^* + m^* \quad (20)$$

The lower number under the MPC-VSG control strategy is obtained.

$$N_{nj} = n_{nj}^* + m^* \quad (21)$$

Submodule voltage balancing is achieved using the nearest-level approximation method. By combining Equations 20, 21, the submodule sequence for each arm is determined, and the corresponding control pulses are applied to the MMC.

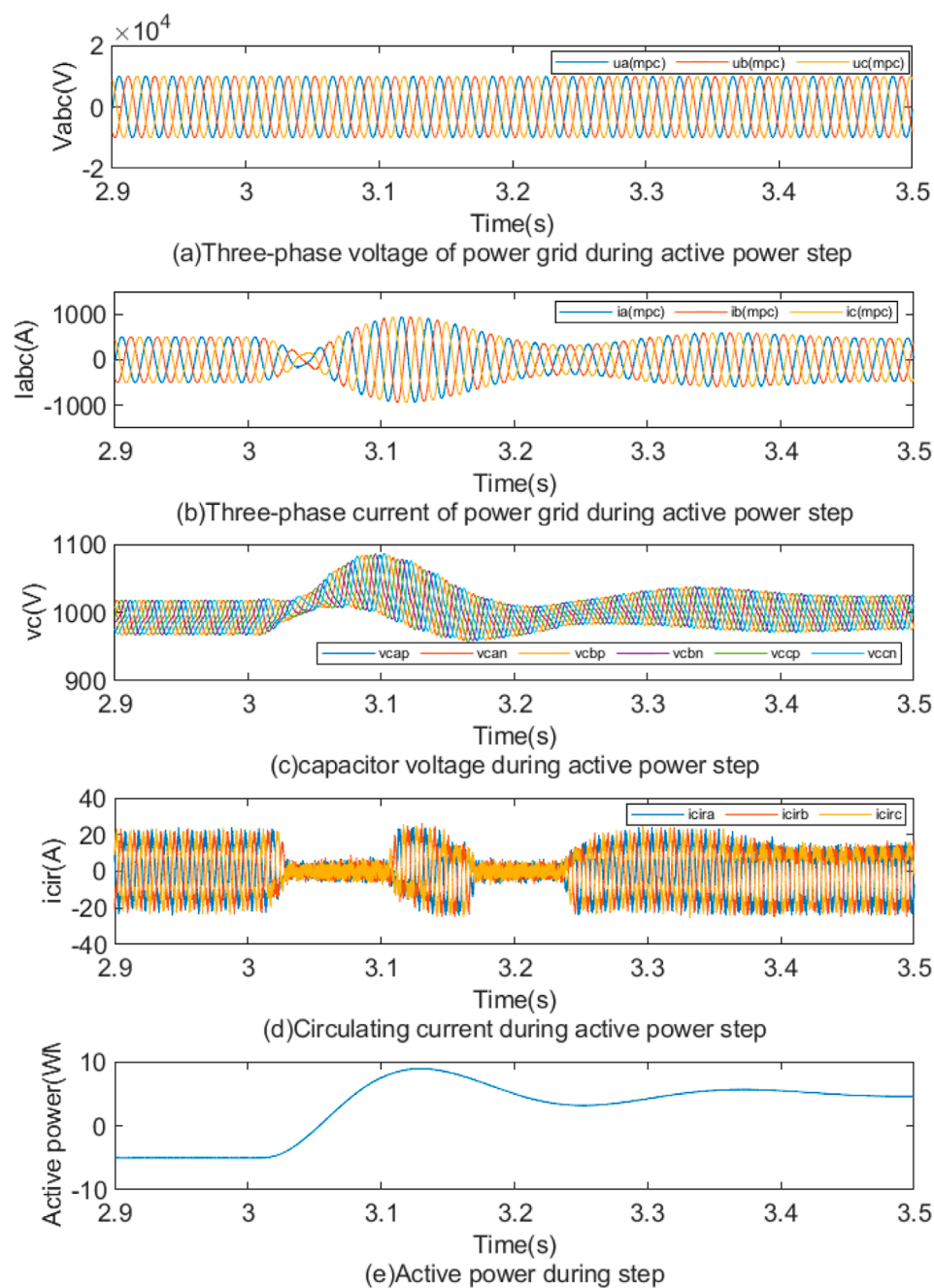


FIGURE 11

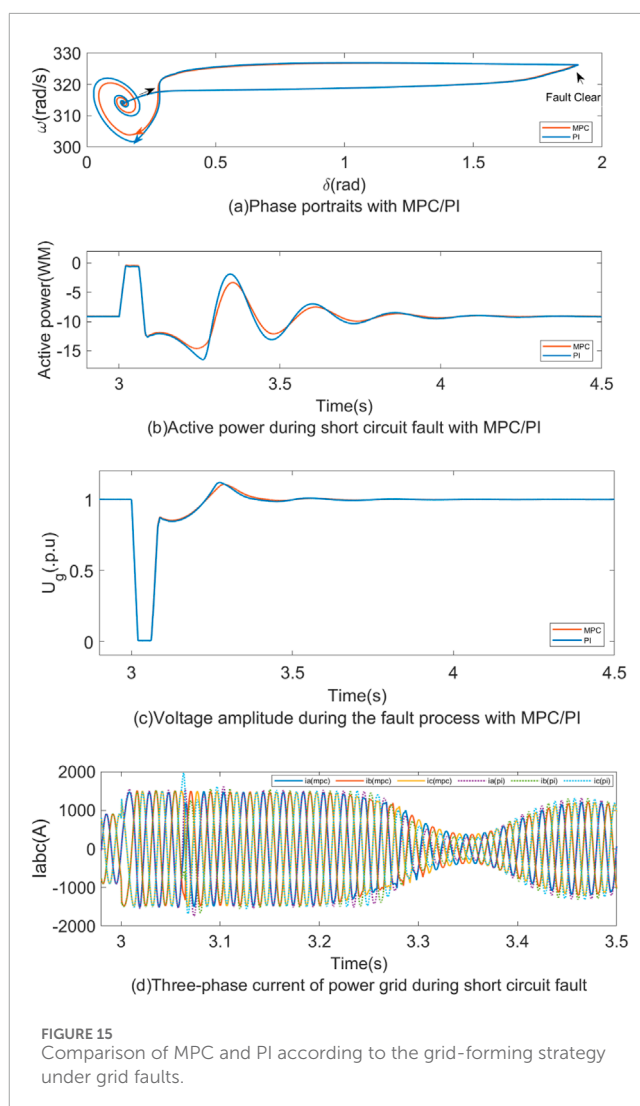
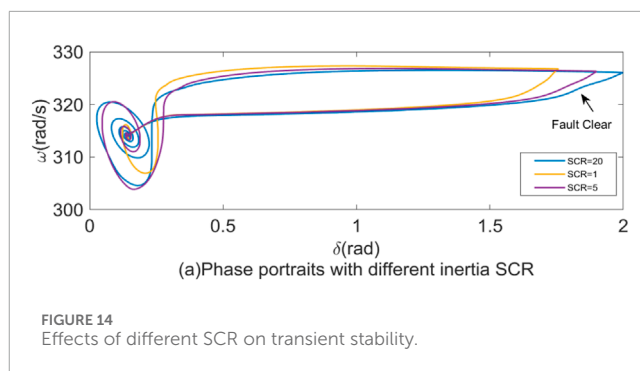
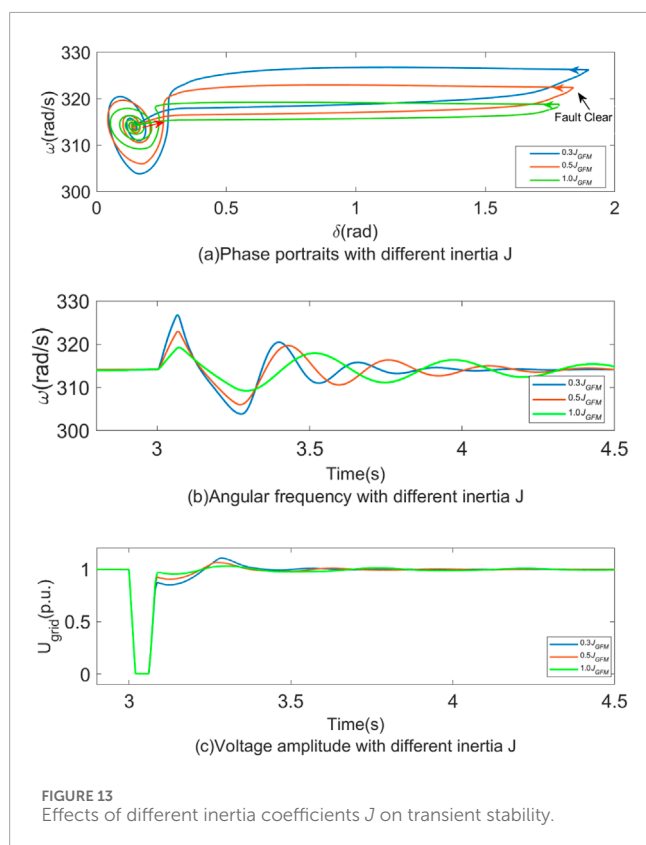
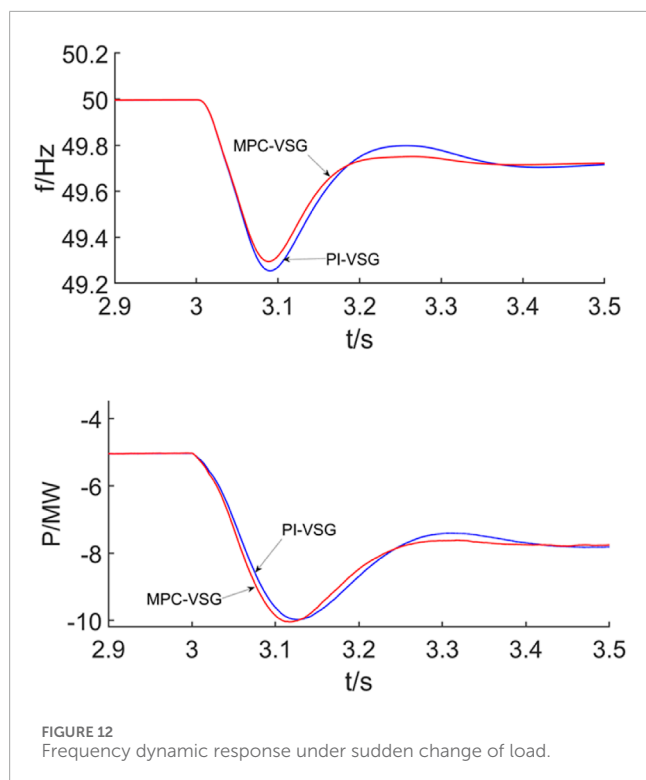
(A) Three-phase voltage of power grid during active power step. (B) Three-phase current of power grid during active power step. (C) capacitor voltage during active power step. (D) Circulating current during active power step. (E) Active power during step.

Three control objectives are incorporated into the proposed Level MPC these are AC current, circulating current, and strict fault current limit. control objectives are achieved through the cost function J outlined in Equation 22.

$$J = (\lambda_1 + \xi) |i_{jref}(t + T_s) - i_{sj}(t + T_s)| + \lambda_2 |i_{dc}(t + T_s)/3 - i_{zj}(t + T_s)| \quad (22)$$

$$\xi = \begin{cases} \infty, & \|i_{sj}\| \geq I_{\max} \\ 0, & \|i_{sj}\| < I_{\max} \end{cases} \quad (23)$$

Within the cost function, λ_1 λ_2 introduces the weighting factor, in order to tracking of reference and regulate the circulating current to its desired value, ξ is additional penalty weighting factor to the fault current strict limit in Equation 24.



3.4 Threshold current controller design of grid-forming

To mitigate transient overcurrents, the proposed method limits the amplitude of the reference current under the d-q framework while maintaining the current phase. This ensures that power supply quality is preserved during fault conditions to the greatest extent possible. The strategy is described as follows:

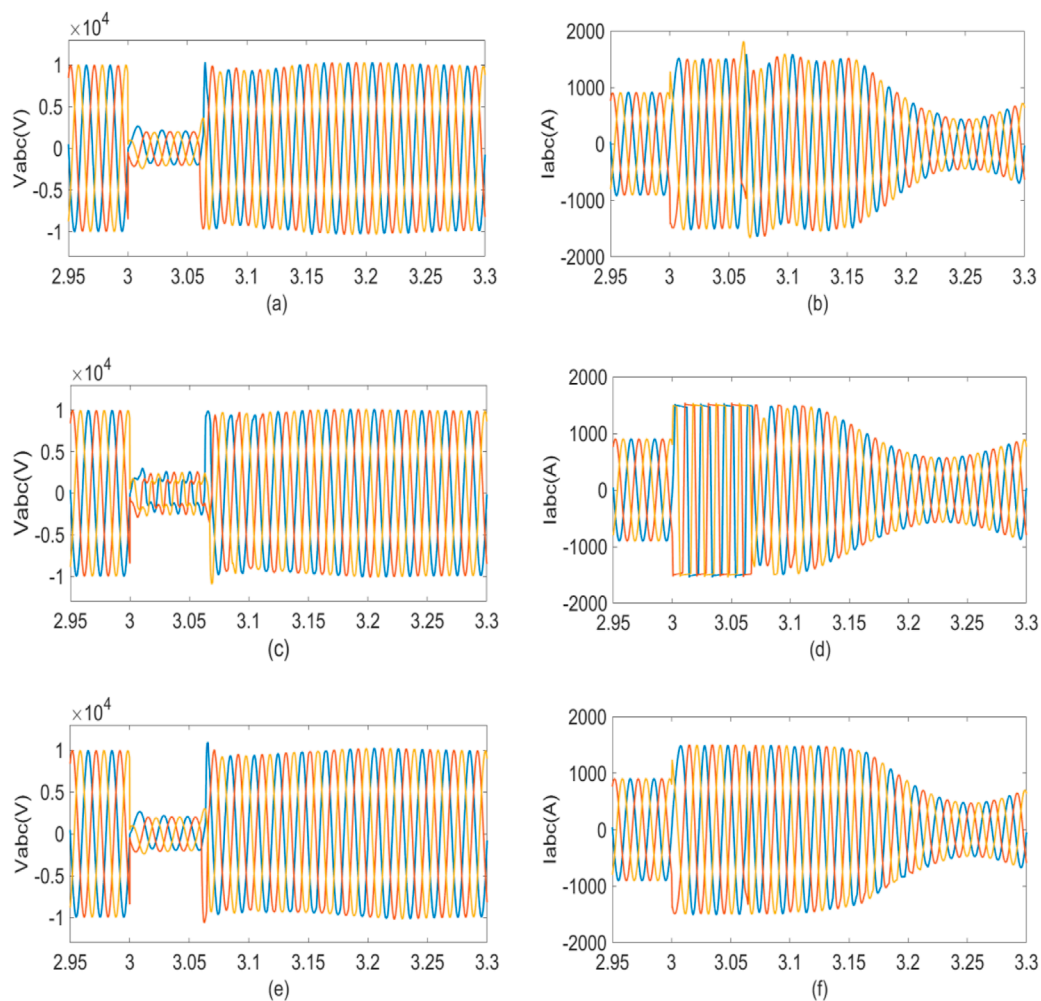


FIGURE 16

Three-phase voltage and current in (A, B) the PI-based strategy, (C, D) the MPC-based strategy in (Matevosyan et al., 2019), and (E, F) the proposed MPC strategy.

where I_{max} denotes the maximum allowable current of the converter. $i_{sd_lim}^*$ denotes the saturated d-q current. i_{sq}^* , i_{sd}^* , $i_{sd_lim}^*$, and $i_{sq_lim}^*$ denote the reference current of axis d and q before and after the limiting respectively.

$$\begin{cases} i_{sd_lim}^* = i_{sd}^* & \sqrt{i_{sd}^{*2} + i_{sq}^{*2}} < I_{max} \\ i_{sd_lim}^* = I_{max} \cos \left[\arctan 2 \left(\frac{i_{sq}^*}{i_{sd}^*} \right) \right] & \sqrt{i_{sd}^{*2} + i_{sq}^{*2}} \geq I_{max} \\ i_{sq_lim}^* = I_{max} \sin \left[\arctan 2 \left(\frac{i_{sq}^*}{i_{sd}^*} \right) \right] & \sqrt{i_{sd}^{*2} + i_{sq}^{*2}} \geq I_{max} \end{cases} \quad (24)$$

The basic principles of model predictive control refer to Figure 10.

4 Simulation verification

To validate the proposed grid-forming control strategy, a grid-forming MMC system was built and tested in Simulink. For the purpose of analysis and comparison, the DC side of the grid-connected MMC is connected to a constant DC voltage source, while

the AC side uses a three-phase programmable voltage source to simulate frequency and voltage fluctuations. The system simulation parameters are provided in Table 3.

4.1 Simulation of dynamic characteristics of MMC

The active power step simulation of the MMC converter is conducted under a grid-forming control strategy. The simulation parameters for the MMC converter and system are provided in Table 3. The number of submodules, denoted as “m,” used to suppress circulating current is set to 1. The time-domain simulation results are presented in Figure 11. Before $t = 3$ s, MMC operates statically at -5 MW, with a circulating current of approximately 20 A. At $t = 3$ s, the active power jumps to 5 MW, with the circulating current remaining around 20 A. During this transition, the capacitor voltage fluctuates by about 6% due to the circulating current. The MMC converter operates stably throughout the step process, fulfilling the specification requirements.

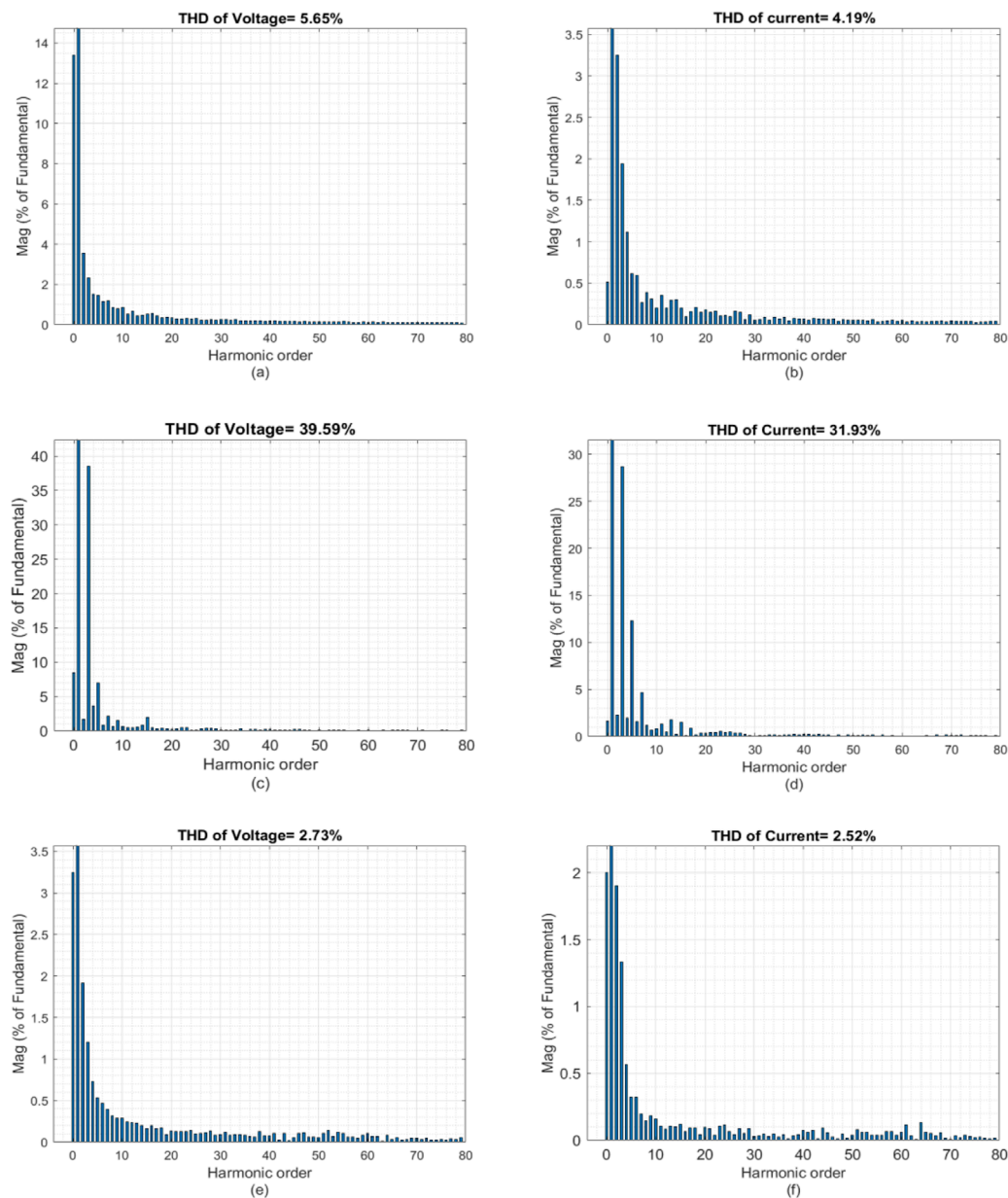


FIGURE 17 THD of voltage and current in (A, B) the PI-based strategy, (C, D) the MPC-based strategy in (Matevosyan et al., 2019), and (E, F) the proposed MPC strategy.

To fully compare the dynamic response characteristics of the inertia support with the proposed control strategy, the simulation experiment under sudden change of load is carried out. During normal operation, the synchronous generator is 20 MVA, the load is 20 MW, and MMC-HVDC provided 5 MW power to the load. At 3 s, the 7 MW load is suddenly put into, with the frequency transient response characteristics under the two controls observed. As shown in Figure 12, at $t = 3$ s, based on PI-VSG control, the grid frequency f drops to 49.25 Hz. However, based on MPC-VSG control, the frequency occurs 49.29 Hz, which reduced by 0.04 Hz.

When the system frequency decreases, both strategies can achieve the rapid response adjustment of active power. Compared

with PI-VSG control, MMC under MPC-VSG control has a faster power tracking speed.

4.2 Grid-forming MMC transient characteristics under grid faults

4.2.1 Effects of different inertia coefficients J and SCR on transient stability

A three-phase short-circuit fault is simulated in the power grid, with the fault location at the converter outlet and a fault resistance of 0.01Ω . The fault occurs at $t = 3$ s and lasts for 60 m, with a

system short-circuit ratio (SCR) of 3. The reference value for the inertia coefficient J is set to 300. The transient fault characteristics are examined for inertia coefficients of 0.3, 0.5, and 1.0 p.u.

Figure 13 illustrates the transient fault characteristics of grid-forming MMC converters under different inertia coefficients J during grid faults. Figure 10A shows the phase portraits of MMC converters during the fault process. It is evident that a smaller inertia coefficient J_{GFM} leads to a larger power angle δ , making the converter more prone to instability. Figures 13B, C demonstrate that increasing the inertia coefficient J_{GFM} enhances the system's ability to suppress frequency ω fluctuations, accelerates voltage recovery, and improves overall system stability.

Figure 14 illustrates the transient fault characteristics of grid-forming MMC converters under different SCR during grid faults. The result shows that when increasing the grid SCR, the power angle δ become larger, that means the stability margin decreases when the grid is stiff.

4.2.2 Comparison of MPC and PI with grid-forming strategy under grid faults

The fault simulation conditions are consistent with those outlined in Section 4.2.1, with the fault current limited to 1.5 p.u. Figure 15 presents the phase diagram and fault current limiting waveform for grid-forming MMC converters under both MPC and PI control strategies. Upon fault clearing, the power angle and active power under the MPC strategy recover to their pre-fault conditions more rapidly than under the PI strategy. As shown in Figure 11C, the grid voltage increases less during fault recovery with the MPC strategy. Furthermore, Figure 11D clearly illustrates that under the PI strategy, the C-phase current reaches 2 kA (2 p.u.), while the current in all three phases under the MPC strategy remains within the threshold.

4.2.3 Power quality assessment

To evaluate the power quality of the output voltage and current under the proposed strategy, the fault simulation conditions are kept consistent with those described in Section 4.2.1, with a fault current limit of 1.5 p. u. and a fault resistance of 0.5 Ω .

The proposed control strategy is compared with the PI strategy and the MPC current limiting strategy from reference (Zheng, 2020). The voltage and current waveforms are shown in Figure 16, and the THD of both voltage and current under the three strategies are compared in Figure 17. As seen in Figures 16E, F, 17E, F, the proposed control strategy maintains excellent power quality during both steady-state and fault conditions, with voltage and current THD of 2.73% and 2.52%, respectively, and accurately limits the current below 1.5 p. u. In contrast, under the PI strategy, current overruns are observed during the fault, with voltage and current THD of 5.65% and 4.19%, respectively. Under the MPC current limiting strategy from reference (Zheng, 2020), the current is strictly limited to less than 1.5 p.u., but the waveform is clipped, resulting in poor power quality with voltage and current THD of 39.59% and 31.93%, respectively.

5 Conclusion

Grid forming converters have become attractive candidates in the high penetration of distributed energy resources grid. This paper first compared existing GFL and GFM strategies, a improved GFM-MMC strategy based virtual admittance combined with direct modulation MPC method is proposed, which obtained faster and strict current-limiting capability, improved dynamic response, and power quality. Finally, the verification of these concepts through simulations. This strategy aims to enhance the stability of the power supply system and improve the fault tolerance of the grid, offering a novel approach to carbon emission reduction in DC industrial parks.

Data availability statement

The original contributions presented in the study are included in the article/supplementary material, further inquiries can be directed to the corresponding author.

Author contributions

SL: Methodology, Writing–review and editing. XD: Conceptualization, Methodology, Writing–review and editing. YX: Supervision, Writing–review and editing. YL: Conceptualization, Writing–review and editing. RZ: Supervision, Writing–review and editing.

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Conflict of interest

Authors SL, YX, YL, and RZ were employed by State Grid Ningxia Electric Power Co., Ltd Econ-tech Research Institute. Author XD was employed by Beijing DC T&D Engineering Technology Research Center (NARI China-EPRI Electrical Engineering Co., Ltd.).

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